TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

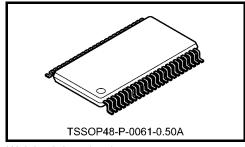
TC74LCX16244FT

Low-Voltage 16-Bit Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX16244FT is a high-performance CMOS 16-bit bus buffer. Designed for use in 2.5-V or 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (2.5-V or 3.3-V) $_{\rm VCC}$ applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

This device is non-inverting 3-state buffer having four active-low output enables. It can be used as four 4-bit buffers two 8-bit buffers or one 16-bit buffer. When the $\overline{\rm OE}$ input is high, the outputs are in a high-impedance state. This device is designed to be used with 3-state memory address drivers, etc.



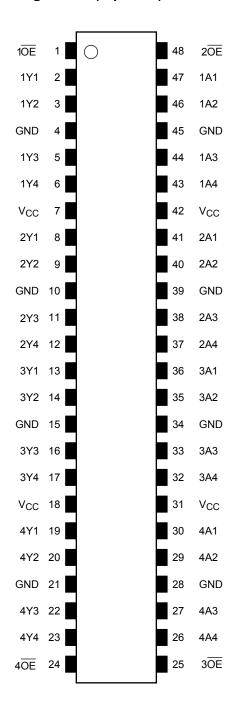
Weight: 0.25 g (typ.)

All inputs are equipped with protection circuits against static discharge.

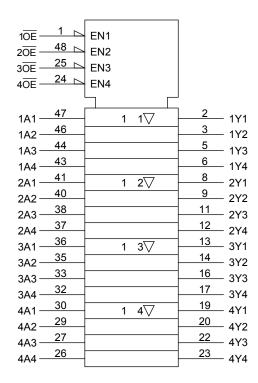
Features

- Low-voltage operation: V_{CC} = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 4.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Package: TSSOP
- · Power-down protection provided on all inputs and outputs

Pin Assignment (top view)



IEC Logic Symbol



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Truth Table

Inp	Outputs	
1OE	1A1-1A4	1Y1-1Y4
L	L	L
L	Н	Н
Н	Х	Z

Inp	Outputs	
2OE	2A1-2A4	2Y1-2Y4
L	L	L
L	Н	Н
Н	Х	Z

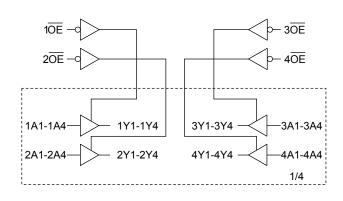
Inp	Outputs	
3 OE	3A1-3A4	3Y1-3Y4
L	L	L
L	Н	Н
Н	Х	Z

Inp	Outputs	
4 OE	4A1-4A4	4Y1-4Y4
L	L	L
L	Н	Н
Н	Х	Z

X: Don't care

Z: High impedance

System Diagram



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Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 6.0	V
Input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
Output voltage	V _{OUT}	$-0.5 \text{ to V}_{CC} + 0.5$ (Note 3)	V
Input diode current	l _{IK}	-50	mA
Output diode current	I _{OK}	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	P _D	400	mW
DC V _{CC} /ground current per supply pin	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: Vout < GND, Vout > Vcc

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	2.0 to 3.6	V	
Fower supply voltage	v CC	1.5 to 3.6 (Note 2)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 3)	V	
Output voltage	VOU1	0 to V _{CC} (Note 4)	V	
		±24 (Note 5)		
Output current	I _{OH} /I _{OL}	±12 (Note 6)	mA	
		±8 (Note 7)		
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V_{CC} or GND.

Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V



Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteris	etice	Symbol	Test Condition		Test Condition		Max	Unit
Characteris	Sucs	Symbol			V _{CC} (V)	Min	IVIAX	Offic
	H-level	VIH				1.7	_	
Input voltage	i i-level	VIH	-	_	2.7 to 3.6	2.0	_	V
Input voltage	L-level	Min			2.3 to 2.7	_	0.7	V
	L-ievei	V _{IL}	-		2.7 to 3.6	_	0.8	
				I _{OH} = -100 μA	2.3 to 3.6	V _{CC} -0.2	_	
				$I_{OH} = -8 \text{ mA}$	2.3	1.8	_	
	H-level	Voн	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	
Output voltage	put voltage			I _{OH} = -24 mA	3.0	2.2	_	V
				$I_{OL} = 100 \ \mu A$	2.3 to 3.6	_	0.2	
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.6	
	L-level	V _{OL}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
				$I_{OL} = 16 \text{ mA}$	3.0	_	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0		0.55	
Input leakage currer	nt	I _{IN}	V _{IN} = 0 to 5.5 V		2.3 to 3.6		±5.0	μΑ
3-state output off-sta	ate current	l _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5 \text{ V}$		2.3 to 3.6	_	±5.0	μА
Power off leakage c	Power off leakage current I _{OFF}		V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μΑ
Quioscont supply ou	ırront	loo	V _{IN} = V _{CC} or GND		2.3 to 3.6	_	20.0	
Quiescent supply cu		Icc	V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.3 to 3.6		±20.0	μΑ
Increase in Icc per in	nput	Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.3 to 3.6	_	500	

AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition			Min	Max	Unit
Characteristics	Symbol	rest Condition	V _{CC} (V)	C _L (pF)	IVIII	IVIAX	Offic
	t _{pLH}		2.5 ± 0.2	30	1.5	5.4	
Propagation delay time	+	Figure 1, Figure 2	2.7	50	1.5	5.2	ns
	t _{pHL}		3.3 ± 0.3	50	1.5	4.5	
	t		2.5 ± 0.2	30	1.5	7.2	
3-state output enable time	t _{pZL}	Figure 1, Figure 3	2.7	50	1.5	6.3	ns
	t _{pZH}		3.3 ± 0.3	50	1.5	5.5	
	.	t _{pLZ} Figure 1, Figure 3	2.5 ± 0.2	30	1.5	6.5	
3-state output disable time	lpL∠		2.7	50	1.5	5.7	ns
t _p			3.3 ± 0.3	50	1.5	5.4	
•			2.5 ± 0.2	30	_	_	
Output to output skew	t _{osLH}	(Note)	2.7	50	_	_	ns
	t _{osHL}		3.3 ± 0.3	50	_	1.0	

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Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics

(Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $R_L = 500$ Ω)

Characteristics		Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum	V _{OL}	V _{OLP}	V _{IH} = 2.5 V, V _{IL} = 0 V, C _L =30pF	2.5	0.6	V
dynamic	VOL	VOLP	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}, C_L = 50 \text{pF}$	3.3	0.8	V
Quiet output minimum	Voi	Mand	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}, C_L = 30 \text{pF}$	2.5	0.6	V
dynamic	V_{OL}	V _{OL} V	V _{IH} = 3.3 V, V _{IL} = 0 V, C _L =50pF	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

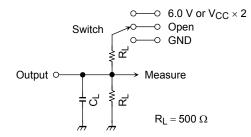
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	3.3	8	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$ (Not	e) 3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/16 \text{ (per bit)}$

AC Test Circuit



Parameter	Switch			
t _{pLH} , t _{pHL}	Open			
t _{pLZ} , t _{pZL}	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			
t _{pHZ} , t _{pZH}	GND			

Figure 1

AC Waveform

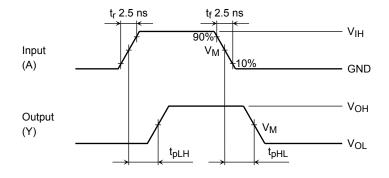


Figure 2 t_{pLH}, t_{pHL}

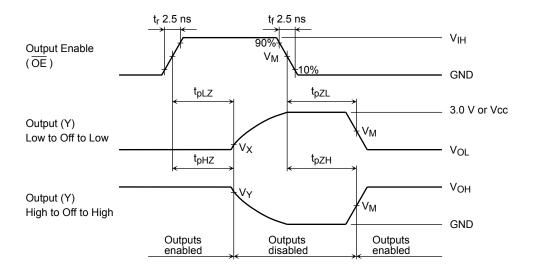


Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

Symbol		V _{CC}	
Symbol	$3.3\pm0.3~\textrm{V}$	2.7 V	$2.5\pm0.2~\textrm{V}$
V _{IH}	2.7 V	2.7 V	V _{CC}
V _M	1.5 V	1.5 V	V _{CC} /2
VX	V _{OL} + 0.3 V	V _{OL} + 0.3 V	V _{OL} + 0.15 V
VY	V _{OH} – 0.3 V	V _{OH} – 0.3 V	V _{OH} – 0.15 V

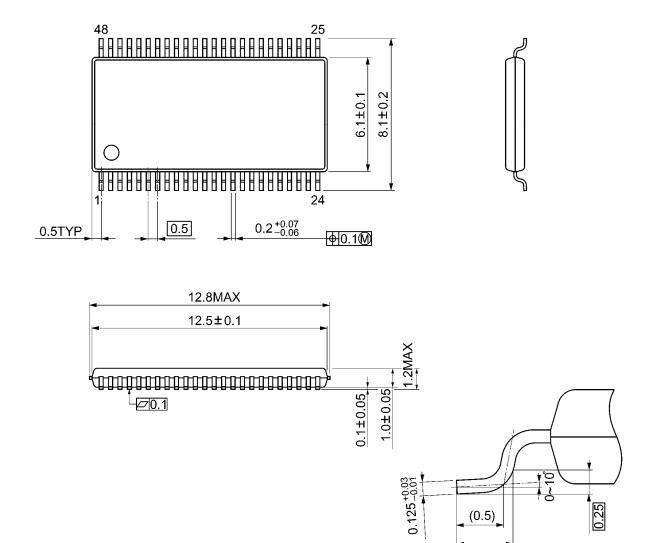
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2014-03-01

Package Dimensions

TSSOP48-P-0061-0.50A





Weight: 0.25 g (typ.)

0.45~0.75

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