TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ86F, TC7SZ86FU

2-Input EXCLUSIVE OR Gate

Features

High output current : ±24 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 2.9 ns (typ.)

at V_{CC} = 5 V, C_L = 50 pF

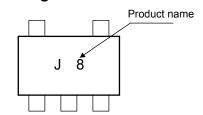
Operating voltage range : V_{CC} = 1.8 to 5.5 V

• 5.5-V tolerant inputs.

5.5-V power down protection output.

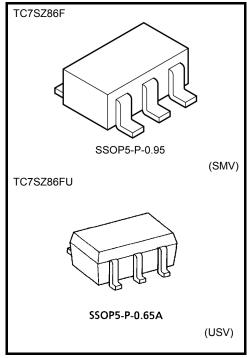
 Matches the performance of TC74LCX series when operated at 3.3 V V_{CC}.

Marking



Absolute Maximum Ratings (Ta=25°C)

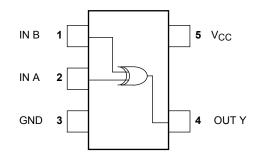
Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 6	V
DC input voltage	V _{IN}	-0.5 to 6	V
DC output voltage	Vour	-0.5 to 6 (Note 1)	V
	V _{OUT}	-0.5 to V _{CC} + 0.5 (Note 2)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C



Weight:

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

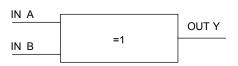
Note 1: V_{CC} = 0V

Note 2: High or Low State. Do not exceed $I_{\mbox{\scriptsize OUT}}$ of absolute maximum ratings.

Start of commercial production 2002-06

Note 3: V_{OUT} < GND

IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristic	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.8 to 5.5	V	
Supply voltage		1.5 to 5.5 (Note 4)		
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V	
		0 to V _{CC} (Note 6)		
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 20 (V _{CC} = 1.8 V, 2.5 V \pm 0.2 V)		
		0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V	
		0 to 5 (V _{CC} = 5.0 V \pm 0.5 V)		

Note 4: Data retention only

Note 5: $V_{CC} = 0 \text{ V}$

Note 6: High or Low state

Electrical Characteristics

DC Characteristics

Characteristic Symbo		Complete Test Condition			Ta = 25°C			Ta = -40 to 85°C		Lloit	
		Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	- Unit
High level	V _{IH}				V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	- v	
	VIН			2.3 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_		
Input voltage	Low level	VIL			1.8			V _{CC} × 0.25		V _{CC} × 0.25	V
	Low level	VIL		_	2.3 to 5.5			V _{CC} × 0.3		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	
				I _{OH} = -100 μA	1.8	1.7	1.8	_	1.7		V
			V _{IN} = V _{IH} or V _{IL}		2.3	2.2	2.3	_	2.2		
		I V _{OH}			3.0	2.9	3.0	_	2.9		
	High level				4.5	4.4	4.5	_	4.4		
	riigirievei			$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9		
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8		2.4	1	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3		
Output voltage				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8		
Output voltage		Low level V _{OL}	V _{IN} = V _{IH} or V _{IL}	Ι _{ΟL} = 100 μΑ	1.8		0	0.1		0.1	
Low level					2.3		0	0.1		0.1	
					3.0		0	0.1		0.1	
	Low level				4.5		0	0.1		0.1	
	LOW level			$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3		0.3	V
				$I_{OL} = 16 \text{ mA}$	3.0		0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55		
Input leakage cu	urrent	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±1	_	±10	μА
Power off leakage	ge current	l _{OFF}	V _{IN} or V _{OL}	_{JT} = 5.5 V	0.0	_		1	_	10	μА
Quiescent supply current I _{CC} V _{II}		$V_{IN} = V_{CC}$	V _{IN} = V _{CC} or GND		_		2	_	20	μА	

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristic	Symbol	Test Condition		Ta = 25°C Ta = -40 to 85°C			Unit		
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH} t _{pHL}	C_L = 15 pF, R_L = 1 M Ω	1.8 ± 0.15	2.0	5.7	11.5	2.0	12.0	ns ns
			2.5 ± 0.2	0.8	3.8	8.0	0.8	8.5	
			3.3 ± 0.3	0.5	3.0	5.7	0.5	6.0	
			5.0 ± 0.5	0.5	2.4	5.0	0.5	5.4	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.2	3.5	6.2	1.2	6.5	
			5.0 ± 0.5	0.8	2.9	5.4	0.8	5.8	
Input capacitance	C _{IN}	_	0 to 5.5		4		_		pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3		21		_	_	- pF
			5.5		24	_	_	_	

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

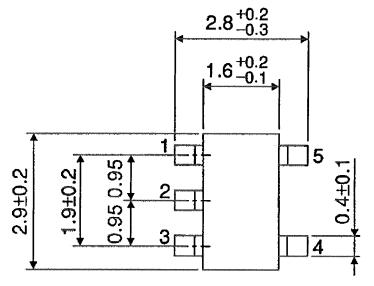
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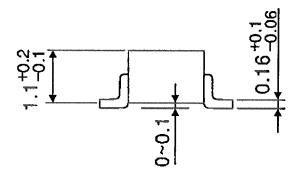
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SSOP5-P-0.95 Unit: mm



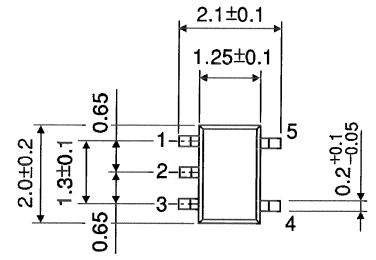


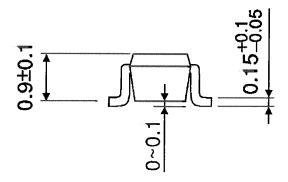
Weight: 0.016 g (typ.)

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Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006.g (typ.)

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