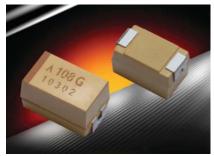
Conductive Polymer Solid Electrolytic Chip Multianode Capacitors



FEATURES

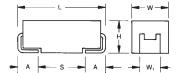
- Conductive polymer electrode, multianode design
- Benign failure mode under recommended use conditions
- Extremely Low ESR
- 3x reflow 260°C compatible
- Volumetric efficiency
- High frequency capacitance retention







SnPb termination option is not RoHS compliant.

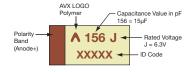


APPLICATIONS

- Telecommunication routers
- Basestations with high power DC/DCs

MARKING

E, V CASE



CASE DIMENSIONS: millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W₁±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.	
Е	2917	7343-43	7.30 (0.287)	4.30 (0.169)	4.10 (0.162)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)	
٧	V 2924 7361-38 7.30 (0.287)		6.10 (0.240)	3.55 (0.140)	3.10 (0.120)	1.30 (0.051)	4.40 (0.173)		
W. dimension applies to the termination width for A dimensional area only									

W₁ dimension applies to the termination width for A dimensional area only

HOW TO ORDER



108

Capacitance Code pF code: 1st two digits represent significant figures, 3rd digit represents multiplier (number of zeros to follow)

M

Tolerance M=±20%

Rated DC Voltage 002=2.5Vdc 004=4Vdc 006=6.3Vdc 010=10Vdc 035=35Vdc

100=100Vdc

004

R

Packaging R = Pure Tin 7" Reel S = Pure Tin 13" Reel H = Tin Lead 7" Reel (contact manufacturer) K = Tin Lead 13" Reel (contact manufacturer)



ESR in m0

TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C								
Capacitance Range:	10 μF to 1000 μF								
Capacitance Tolerance:	±20%								
Leakage Current DCL:		0.10	V						
Rated Voltage (V _R)	≤ +85°C:	2.5	4	6.3	10	35	100		
Category Voltage (V _C)	≤ +105°C:	2	3.2	5	8	28	80		
Surge Voltage (V _S)	≤ +85°C:	3.3	5.2	8	13	46	130		
Surge Voltage (V _S)	≤ +105°C:	2.5	4	6	10	35	100		
Temperature Range:	-55°C to +105°C								
Reliability:	1% per 1000 hours at 85°C, V_B with $0.1\Omega/V$ series impedance, 60% confidence level								
Termination Finish:		Sn F	Plating (sta	andard) ar	nd SnPb I	Plating up	on reque	st	

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.



Conductive Polymer Solid Electrolytic Chip Multianode Capacitors

CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capacitance			Rated Voltage DC (V _R) to 85°C									
μF Code		2.5V (e)	4V (G)	6.3V (J)	10V (A)	35V (V)	100V (A)					
10	106						V(50)					
22	226					E(25)						
33	336											
47	476											
68	686											
100	107											
150	157											
220	227											
330	337			E(10,15)	E(10,15)							
470	477			E(7,10)								
680	687		E(12)	E(12)								
1000	108	E(6,10)	E(6,8,10,12)									

Released ratings, (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.

RATINGS & PART NUMBER REFERENCE

AVX			Rated Voltage	Maximum Operating	DCL Max.	DF Max.	ESR Max.	100kHz RMS Current (mA)			MSL
Part No.	Size	(μ F)	(V)	Temperature (°C)	(μΑ)	(%)	@ 100kHz (mΩ)	45°C	85°C	105°C	
2.5 Volt @ 85°C											
TCME108M002#0006	Е	1000	2.5	105	250	10	6	8300	5800	3700	3
TCME108M002#0010	Е	1000	2.5	105	250	10	10	6400	4500	2900	3
	4 Volt @ 85°C										
TCME687M004#0012	Е	680	4	105	272	8	12	5800	4100	2600	3
TCME108M004#0006	Е	1000	4	105	400	8	6	8300	5800	3700	3
TCME108M004#0008	Е	1000	4	105	400	8	8	7200	5000	3200	3
TCME108M004#0010	Е	1000	4	105	400	8	10	6400	4500	2900	3
TCME108M004#0012	Е	1000	4	105	400	8	12	5800	4100	2600	3
	6.3 Volt @ 85°C										
TCME337M006#0010	Е	330	6.3	105	198	8	10	6400	4500	2900	3
TCME337M006#0015	E	330	6.3	105	198	8	15	5200	3600	2300	3
TCME477M006#0007	Е	470	6.3	105	296	10	7	7700	5400	3500	3
TCME477M006#0010	Е	470	6.3	105	296	10	10	6400	4500	2900	3
TCME687M006#0012	Е	680	6.3	105	408	8	12	5800	4100	2600	3
				10 V	olt @ 85°C						
TCME337M010#0010	Е	330	10	105	330	8	10	6400	4500	2900	3
TCME337M010#0015	Е	330	10	105	330	8	15	5200	3600	2300	3
				35 V	olt @ 85°C						
TCME226M035#0025	Е	22	35	105	77	8	25	4000	2800	1800	3
				100 \	/olt @ 85°C						
TCMV106M100#0050	V	10	100	105	100	8	50	2900	2000	1300	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of $\pm 25^{\circ}$ C. Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

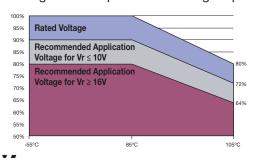
ESR allowed to move up to 1.25 times catalog limit post mounting.

For typical weight and composition see page 261.

NOTE: AVX reserves the right to supply higher voltage ratings or tighter tolerance part in the same case size, to the same reliability standards.

RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr





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PRODUCT CATEGORY 105°C

TEST		Condition			Ch	aracteris	stics				
				Visual examination	no vi	sible dar	nage				
		ed voltage (Ur) at 85°C an		DCL	1.25 x initial limit						
Endurance		Jc) at 105°C for 2000 hou se of ≤0.1Ω/V. Stabilize at		/C	within ±20% of initial value						
		ours before measuring.	. room tomporataro	DF	1.5 x initial limit 2 x initial limit						
				ESR							
				Visual examination		sible dar					
	Ctore of 1	OFOC no veltore emplied	fa = 0000 h a : : : :	DCL ($V_R \le 75V$)		x initial I					
Storage Life	Store at 1	05°C, no voltage applied at room temperature for 1	, for 2000 nours.	DCL $(V_R > 75V)$		2 x initial limit					
otorago <u></u>	measuring.			ΔC/C		within ±20% of initial value					
				DF	1.5 x initial limit						
				ESR	2 x initial limit						
	Visual examination no visible of										
		65°C and 95% relative h th no applied voltage. S		DCL	3 x initial limit						
Humidity	temperat	ure and humidity for 1-2	2 hours before	ΔC/C	within +30/-20% of initial value						
	measurin	g.		DF	1.5 x initial limit						
	-			ESR	2 x initial limit						
_	Step 1	Temperature°C +20	Duration(min) 15		+20°C	-55°C	+20°C	+85°C	+105°C	+20°C	
Temperature	2	-55 +20	15 15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	'IL*	
Stability	4	+85	15	ΔC/C	n/a	+0/-20%	±10%	+20/-0%	+30/-0%	±10%	
	5 6	+105 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*	
				Visual examination	no visible damage						
Surge		ox category voltage (Uc) les of duration 6 min (30		DCL	initia	l limit					
Voltage	5 min 30	sec discharge) through e resistance of 1000Ω		ΔC/C		n +10/-2 n +20/-3					
				DF	1.25	x initial l	imit				

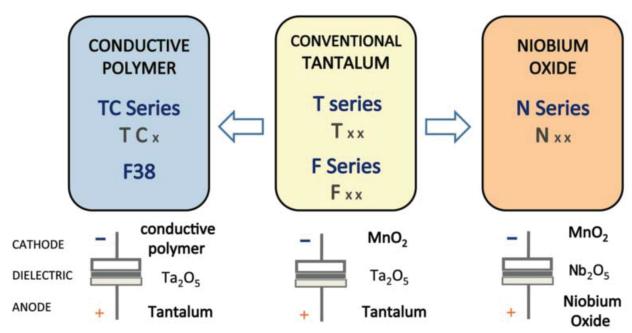
*Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.

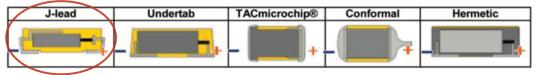


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AVX SOLID ELECTROLYTIC CAPACITOR ROADMAP



Five Capacitor Construction Styles



SERIES LINE UP: CONDUCTIVE POLYMER

