

U_FPGA_B2B_Connectors
FPGA_B2B_Connectors.SchDoc

PMODs
PMODs.SchDoc

U_ARM_JTAG
ARM_JTAG.SchDoc

U_HDMI
HDMI.SchDoc

U_FMC
FMC.SchDoc

Ethernet
Ethernet.SchDoc

FTDI
FTDI.SchDoc

U_POWER_5V0
POWER_5V0.SchDoc

U_CameraLink
CameraLink.SchDoc

U_POWER_3V3
POWER_3V3.SchDoc

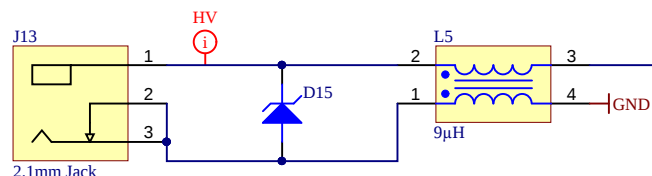
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LED_SW_BUT.SchDoc

SDCard
SDCard.SchDoc

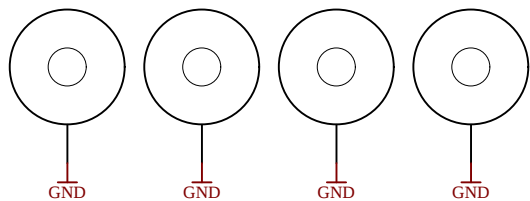
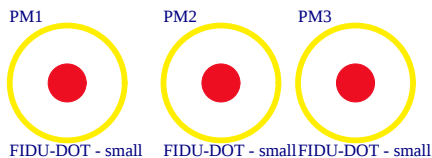
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POWER_MISC.SchDoc

U_ARM_JTAG
ARM_JTAG.SchDoc

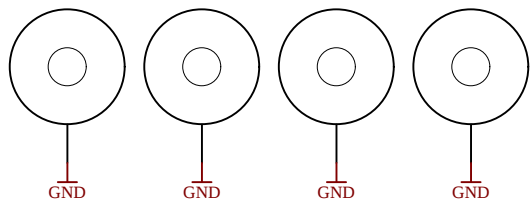
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CPLD.SchDoc



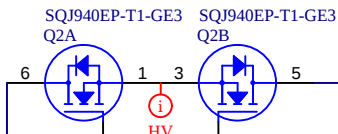
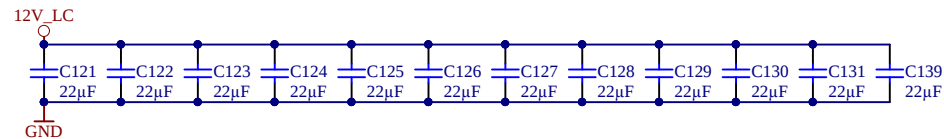
SMBJ20A, 20V, 600W



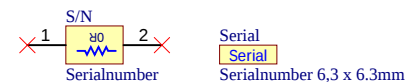
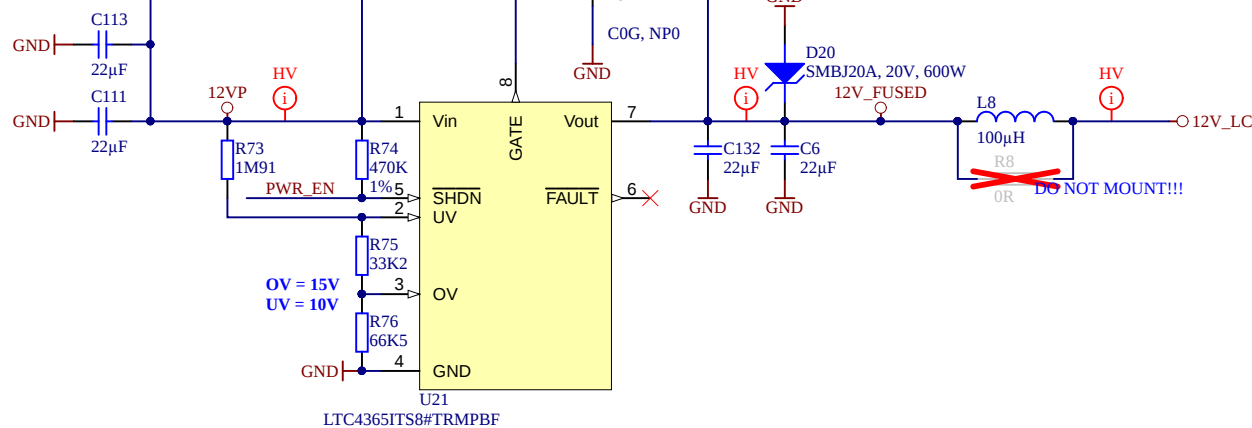
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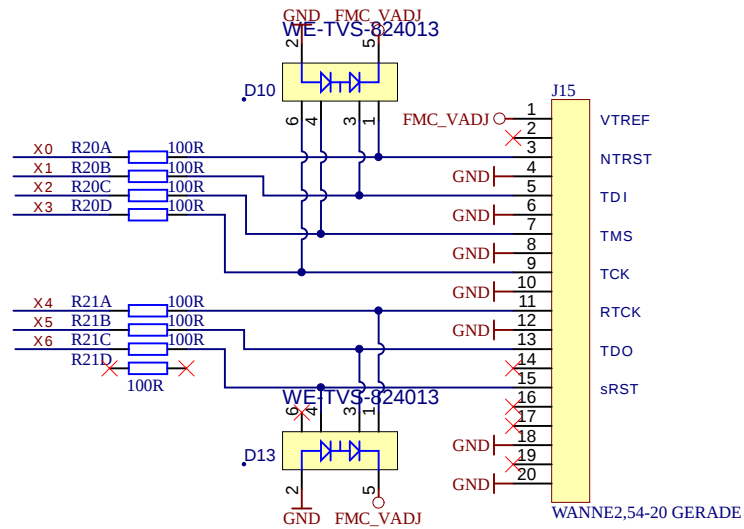
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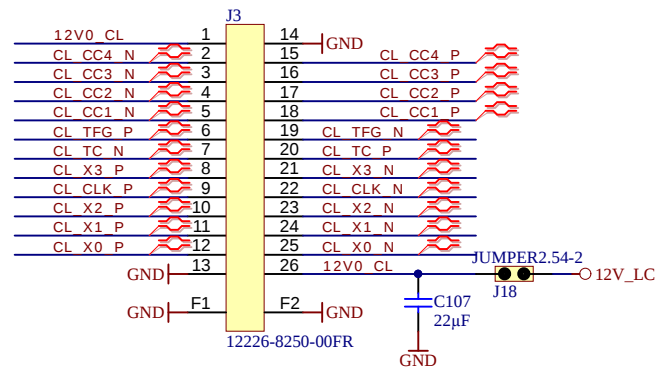
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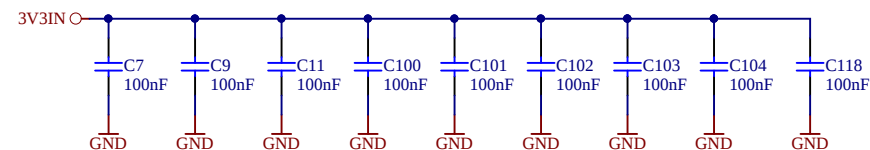
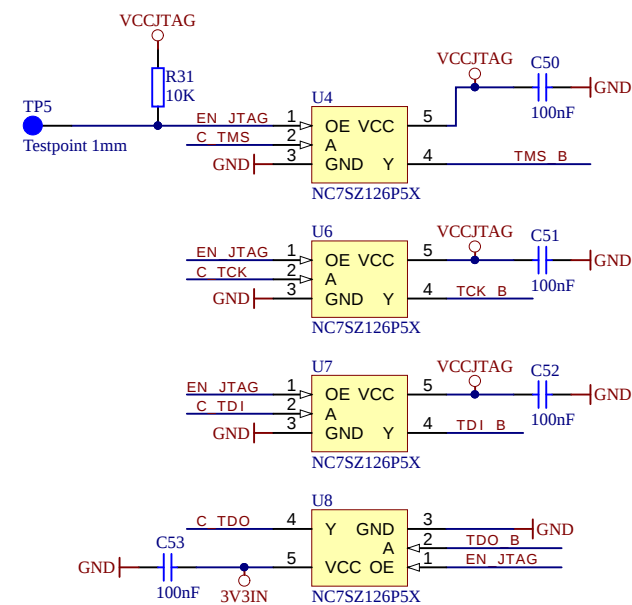
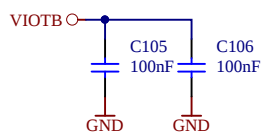
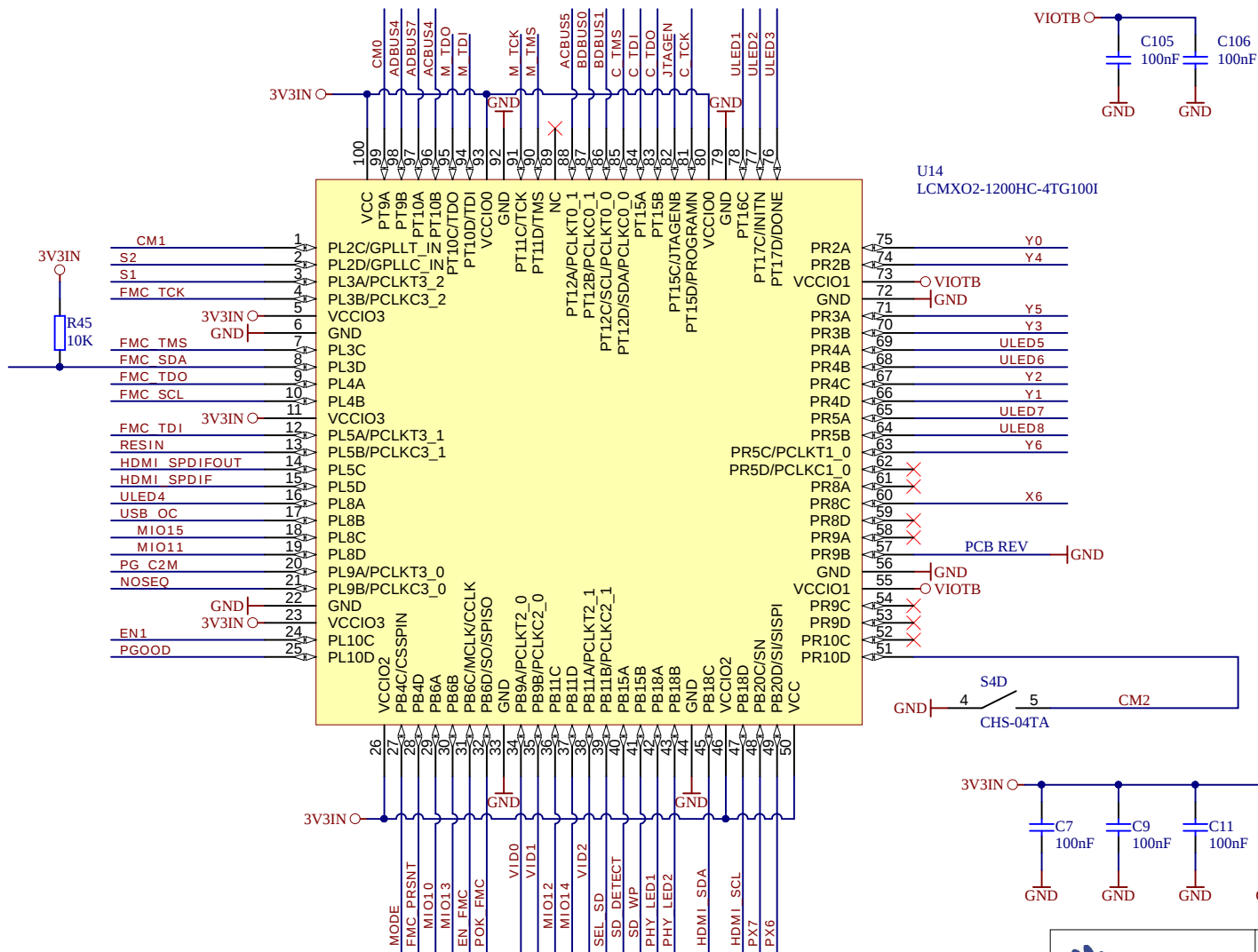
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A4	Number: TE0701 default	Page1 of 19	
Date: 2016-10-14	Copyright: Trenz Electronic GmbH		
Filename: TE0701.SchDoc			



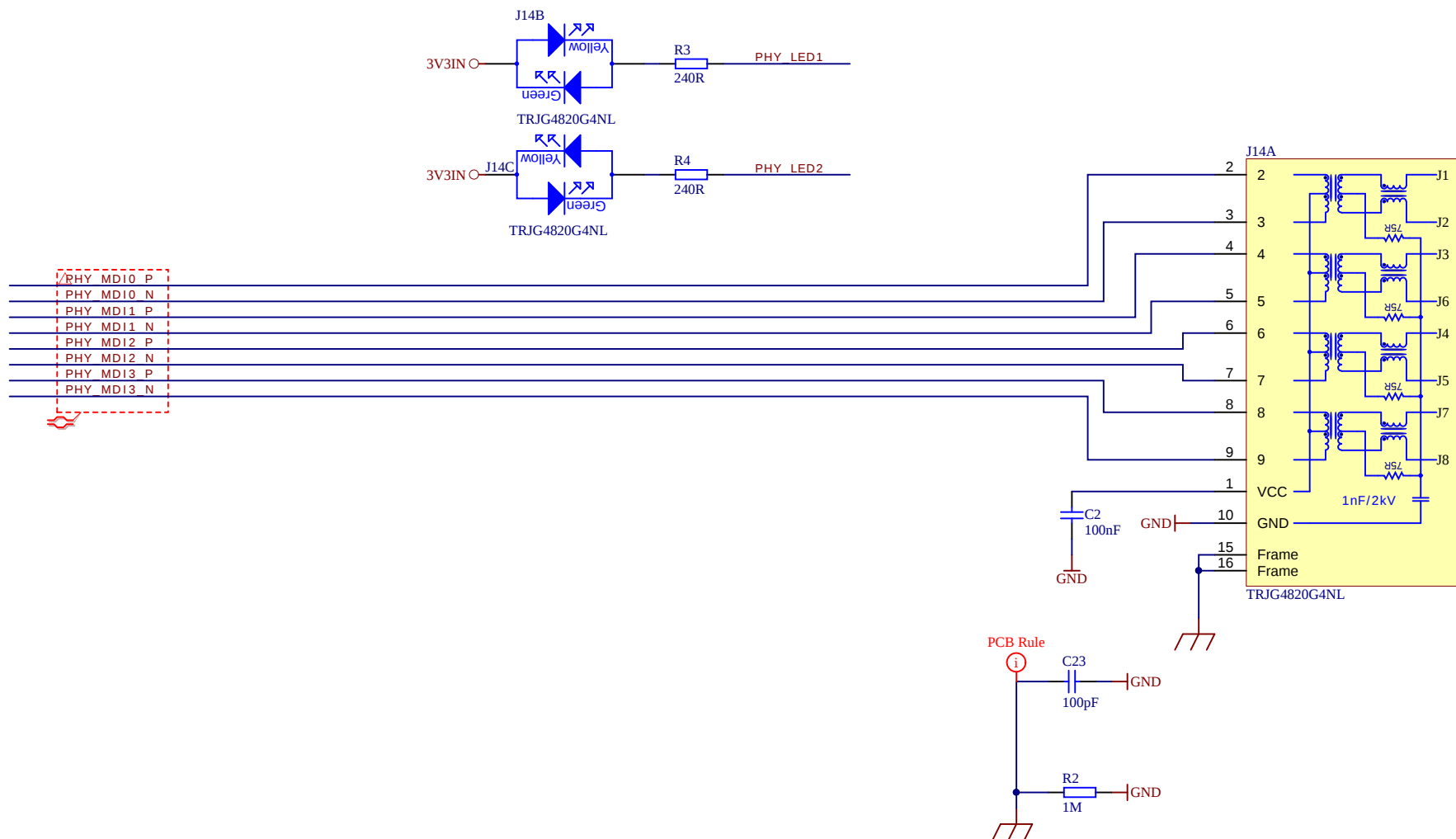
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	Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page2 of 19
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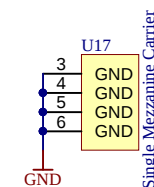
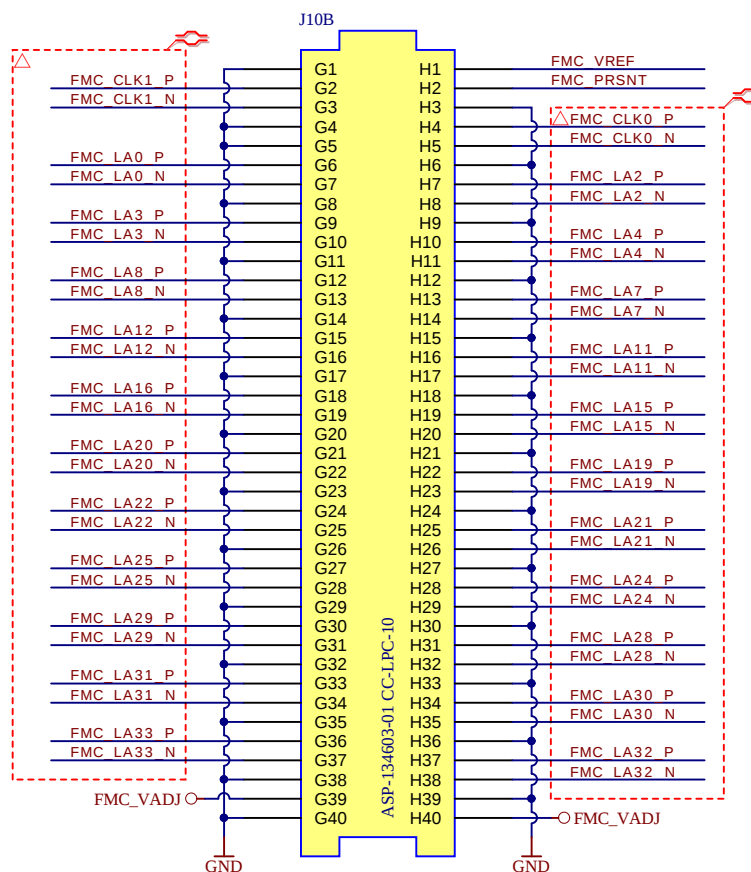
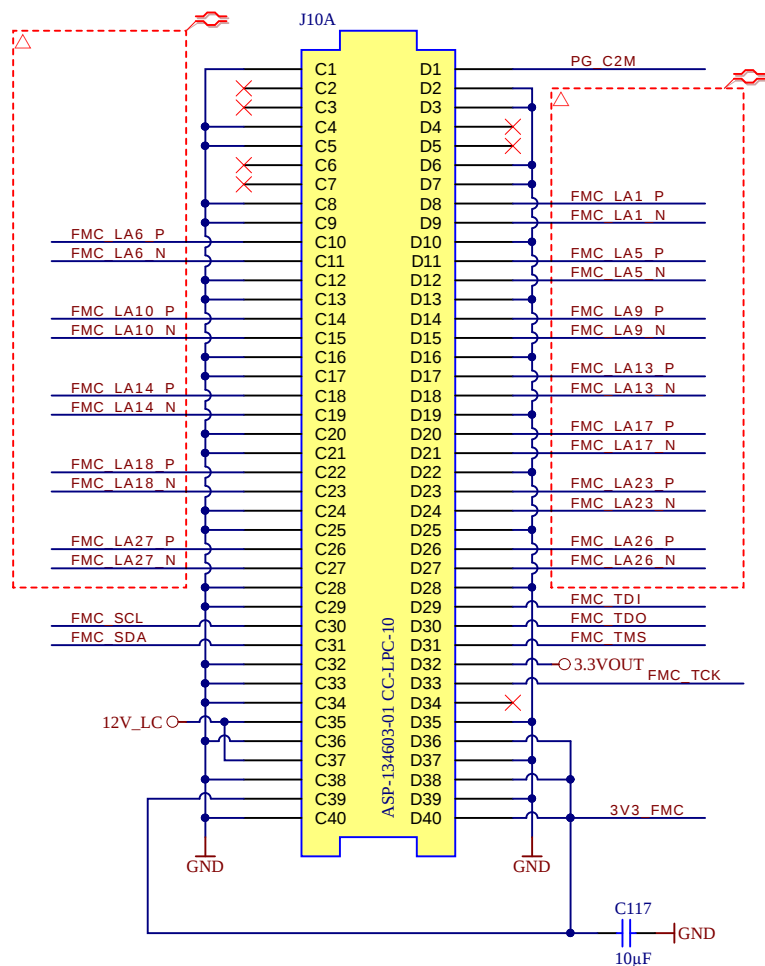
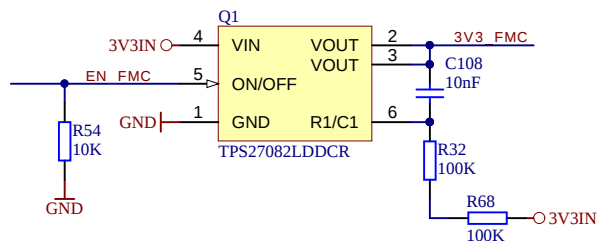
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A4	Number: TE0701 default		Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH		Page3 of 19
Filename: CameraLink.SchDoc			



Title: TE0701- CPLD		
A4	Number: TE0701 default	Rev. 06
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	Title: TE0701 - Ethernet		
	A4	Nummer: TE0701 default	Rev. 06
	Datum: 2016-10-14	Zeichner: Trenz Electronic GmbH	Blatt 5 von 19
	Filename: Ethernet.SchDoc		



Title: TE0701 - FMC		
A4	Number: TE0701 default	Rev. 06
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Filename: FMC.SchDoc		

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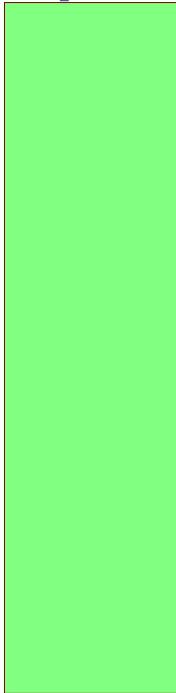
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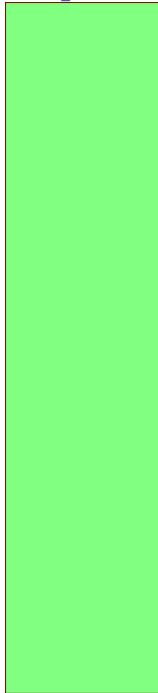
U_TE0720_JB1
TE0720_JB1.SchDoc



U_TE0720_JB3
TE0720_JB3.SchDoc



U_TE0720_JB2
TE0720_JB2.SchDoc



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		A4	Rev. 06
		Number: TE0701 default	
		Date: 2016-10-14	Page7 of 19
Filename:		FPGA_B2B_Connectors.SchDoc	

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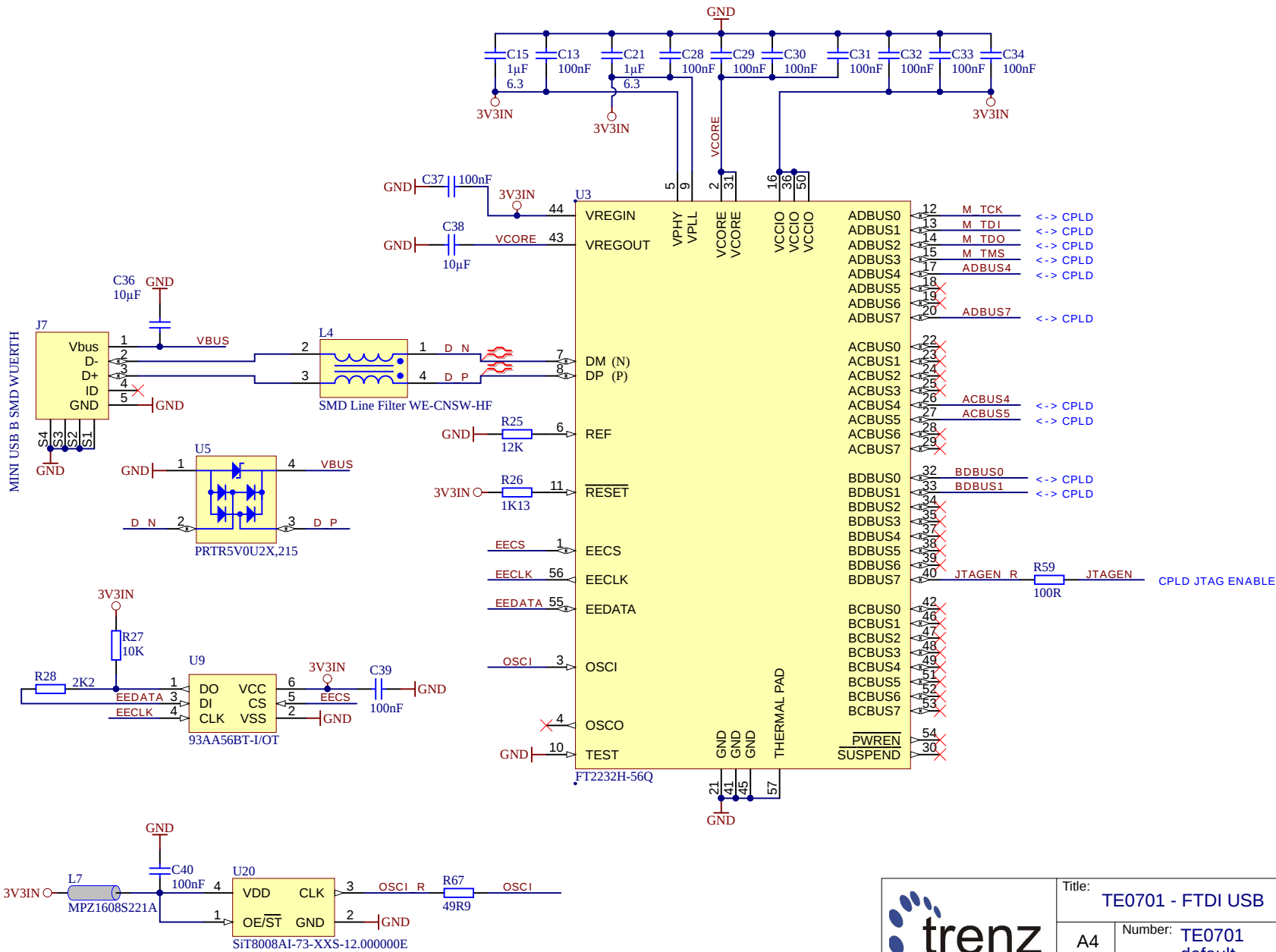
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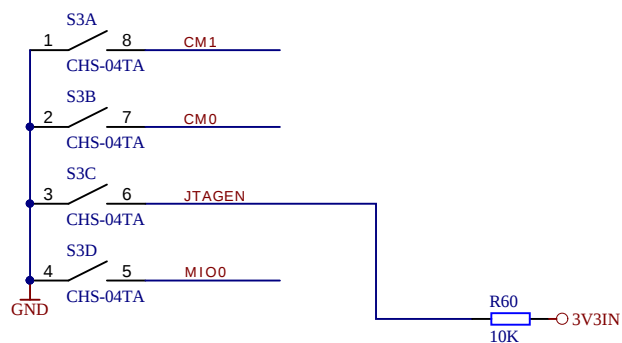
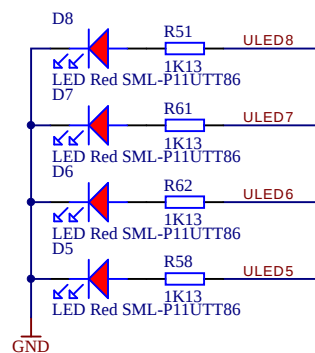
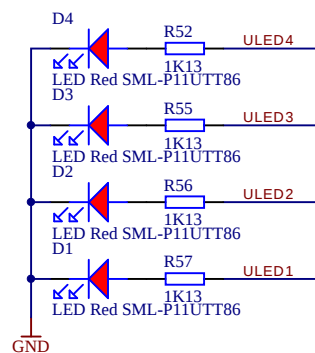
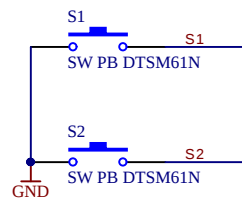


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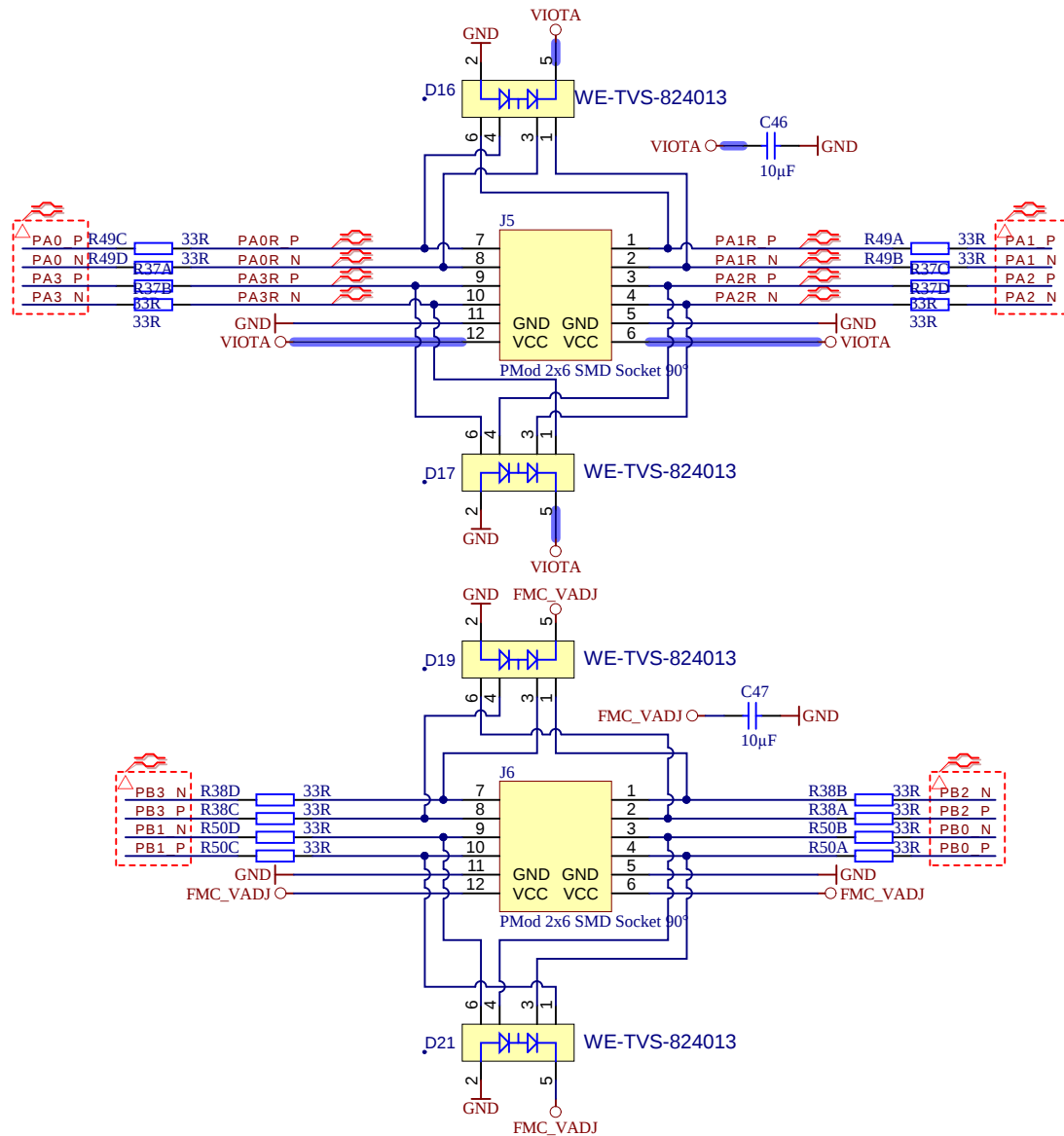
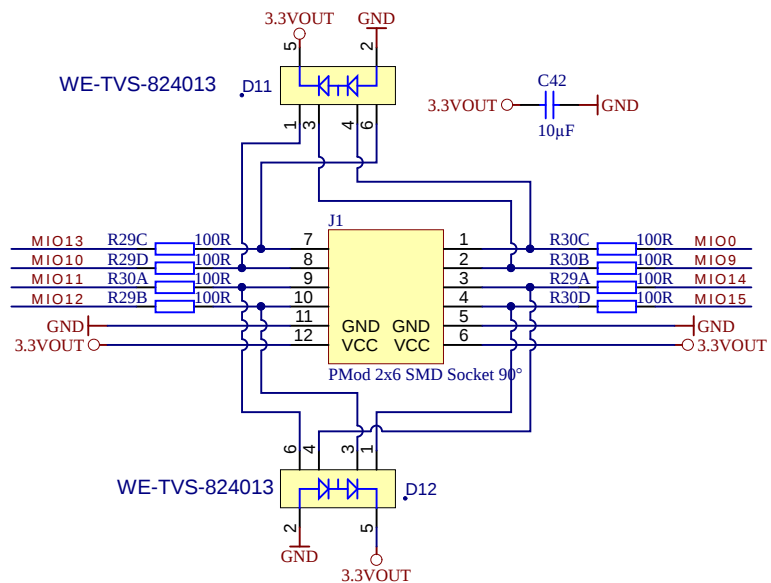


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Date: 2016-10-14		Copyright: Trenz Electronic GmbH		Page11 of 19	
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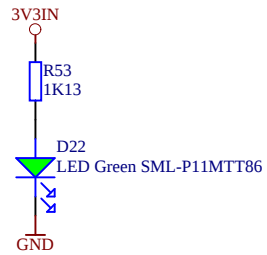
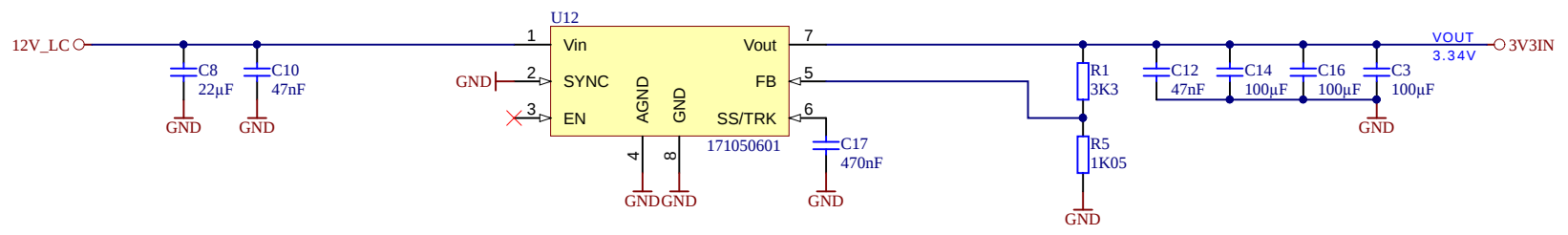




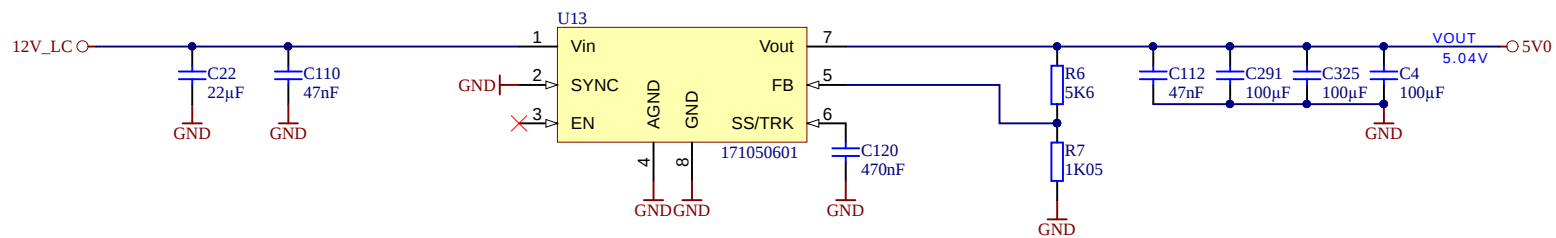
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Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page13 of 19
Filename: LED_SW_BUT.SchDoc		



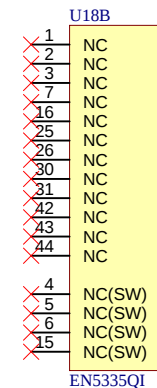
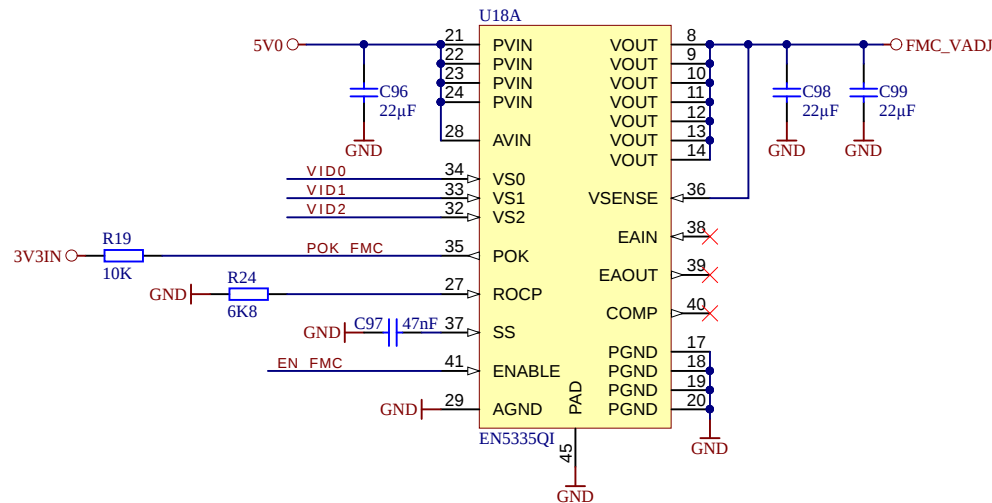
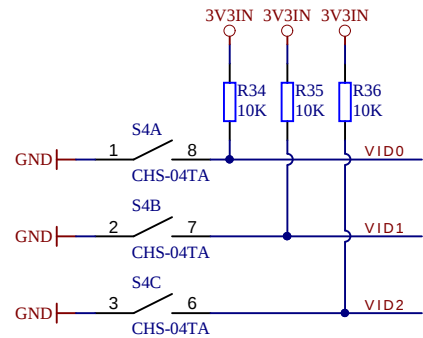
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Date: 2016-10-14	Copyright: Trenz Electronic GmbH	
Filename: PMODs.SchDoc		Page14 of 19



		Title: TE0701 - 3.3V POWER		
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		Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page15 of 19
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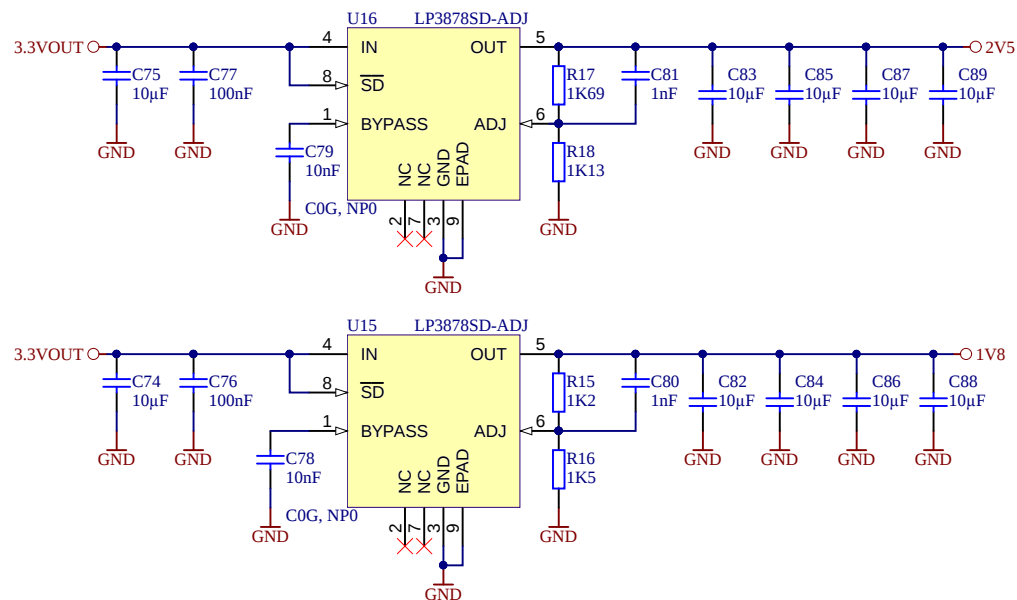


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	Filename: POWER_5V0.SchDoc		



VS2 | VS1 | VS0 | Output Voltage

0	0	0	3.3V
0	0	1	2.5V
0	1	0	1.8V
0	1	1	1.5V
1	0	0	1.25V



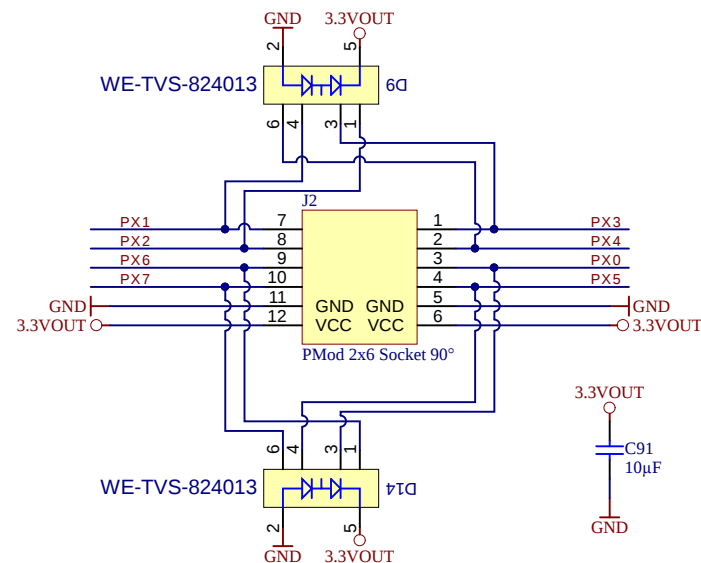
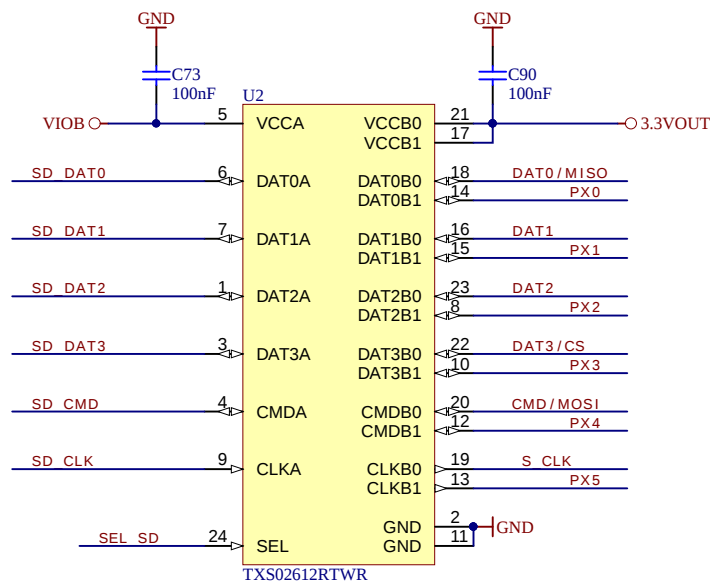
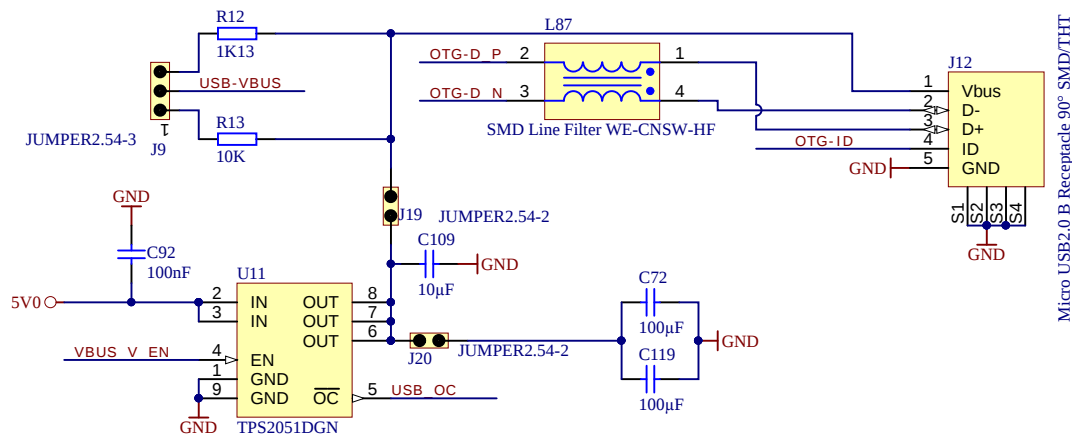
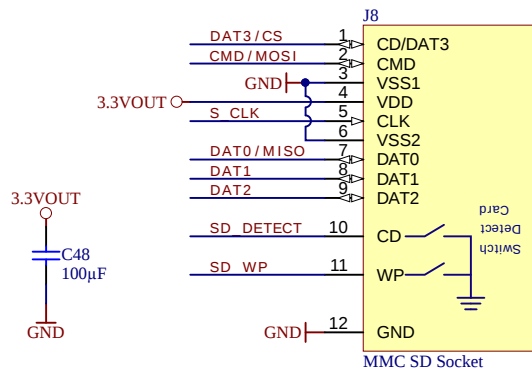
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Date: 2016-10-14	Copyright: Trenz Electronic GmbH	
Filename: POWER_MISC.SchDoc		Page17 of 19

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		Title: TE0701 - SD Card	
		Number: TE0701 default	Rev. 06
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Filename: SDCard.SchDoc		Page18 of 19	

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CHANGES REV05 to REV06:

- 1) Removed holes for Xilinx JTAG Flying Wire Adaptor (1 row header)
- 2) Added jumper J16. Set a power supply on VIOTA to FMC_VADJ
- 3) Added feature to select Vadj voltage by dip switch S4
- 4) U3 FT2232H; new package -56Q
- 5) Changed RJ45 Ethernet connector (J14) 7499111121A to TRJG4820G4NL
- 6) Changed micro USB connector (J12)
- 7) Changed power supply on PMOD connector (J5) 3.3VOUT->VIOTA
- 8) Added resistors R9..R11,R33 for FMC_CLK as swap option
- 9) Added thermal vias to mount holes
- 10) Added serial visual serial number (traceability pad)
- 11) Lib components update
- 12) Changed PMOD connectors THT to SMT
- 13) Removed resistor (R43) on CMD (SD card). R43 is not needed and caused problems with some SD Cards

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Title:	
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TE0701 - Revision history

A4

Number:	TE0701 default
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Rev.	
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Date: 2016-10-14

4 Copyright: Trenz Electronic GmbH

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Revision_Changes.SchDoc

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