

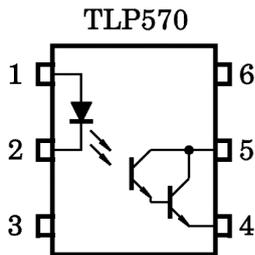
# TLP570, TLP571

Programmable Controllers  
 AC / DC-Input Module  
 Solid State Relay

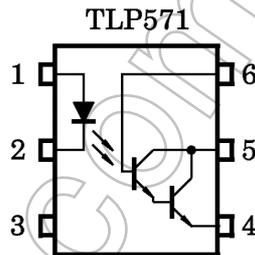
The TOSHIBA TLP570 and TLP571 consist of a Darlington connected photo-transistor optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.  
 TLP570 has no-base internal connection for high-EMI environments.

- Collector-emitter voltage: 35 V (min)
- Current transfer ratio: 1000 % (min)
- Isolation voltage: 2500 Vrms (min)
- UL recognized: UL1577, file no. E67349

### Pin Configurations (top view)

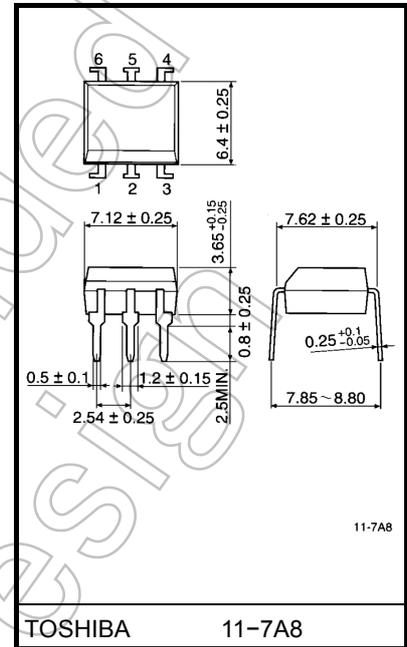


- 1 : ANODE
- 2 : CATHODE
- 3 : NC
- 4 : EMITTER
- 5 : COLLECTOR
- 6 : NC



- 1 : ANODE
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- 6 : BASE

Unit: mm



TOSHIBA 11-7A8

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	$I_F$	70	mA
	Forward current derating (Ta ≥ 25°C)	$\Delta I_F / ^\circ\text{C}$	-0.7	mA / °C
	Peak forward current (100 μs pulse, 100 pps)	$I_{FP}$	1	A
	Reverse voltage	$V_R$	5	V
	Junction temperature	$T_j$	125	°C
Detector	Collector-emitter voltage	$V_{CEO}$	35	V
	Collector-base voltage (TLP571)	$V_{CBO}$	80	V
	Emitter-collector voltage	$V_{ECO}$	7	V
	Emitter-base voltage (TLP571)	$V_{EBO}$	7	V
	Collector current	$I_C$	150	mA
	Power dissipation	$P_C$	150	mW
	Power dissipation derating (Ta ≥ 25°C)	$\Delta P_C / ^\circ\text{C}$	-1.5	mW / °C
	Junction temperature	$T_j$	125	°C
Storage temperature range		$T_{stg}$	-55~125	°C
Operating temperature range		$T_{opr}$	-55~100	°C
Lead soldering temperature (10s)		$T_{sol}$	260	°C
Total package power dissipation		$P_T$	250	mW
Total package power dissipation derating (Ta ≥ 25°C)		$\Delta P_T / ^\circ\text{C}$	-2.5	mW / °C
Isolation voltage (AC, 1 minute, R.H. ≤ 60%) (Note 1)		$BV_S$	2500	$V_{rms}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Device considered a two terminal: Pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

## Recommends Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	$V_{CC}$	—	5	24	V
Forward current	$I_F$	—	16	25	mA
Collector current	$I_C$	—	—	50	mA
Operating temperature	$T_{opr}$	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

**Individual Electrical Characteristics (Ta = 25°C)**

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	$V_F$	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	$I_R$	$V_R = 5 \text{ V}$	—	—	10	$\mu\text{A}$
	Capacitance	$C_T$	$V = 0, f = 1 \text{ MHz}$	—	30	—	pF
Detector	Collector–emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 1 \text{ mA}$	35	—	—	V
	Emitter–collector breakdown voltage	$V_{(BR)ECO}$	$I_E = 0.1 \text{ mA}$	7	—	—	V
	Collector–base breakdown voltage (TLP571)	$V_{(BR)CBO}$	$I_C = 0.1 \text{ mA}$	80	—	—	V
	Emitter–base breakdown voltage (TLP571)	$V_{(BR)EBO}$	$I_E = 0.1 \text{ mA}$	7	—	—	V
	Collector dark current	$I_{CEO}$	$V_{CE} = 24 \text{ V}$	—	10	200	nA
			$V_{CE} = 24 \text{ V}, T_a = 85^\circ\text{C}$	—	—	300	$\mu\text{A}$
	Collector dark current (TLP571)	$I_{CER}$	$V_{CE} = 24 \text{ V}, T_a = 85^\circ\text{C}$ $R_{BE} = 10 \text{ M}\Omega$	—	0.5	10	$\mu\text{A}$
	Collector dark current (TLP571)	$I_{CBO}$	$V_{CB} = 10 \text{ V}$	—	0.01	—	nA
	DC forward current gain (TLP571)	$h_{FE}$	$V_{CE} = 5 \text{ V}, I_C = 10 \text{ mA}$	—	50k	—	—
	Capacitance (collector to emitter)	$C_{CE}$	$V = 0, f = 1 \text{ MHz}$	—	10	—	pF

**Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	$I_C / I_F$	$I_F = 1 \text{ mA}, V_{CE} = 1 \text{ V}$	1000	2000	—	%
Saturated CTR	$I_C / I_F (\text{sat})$	$I_F = 10 \text{ mA}, V_{CE} = 1 \text{ V}$	500	—	—	%
Base photo–current (TLP571)	$I_{PB}$	$I_F = 1 \text{ mA}, V_{CB} = 1 \text{ V}$	—	2	—	$\mu\text{A}$
Collector–emitter saturation voltage	$V_{CE} (\text{sat})$	$I_C = 10 \text{ mA}, I_F = 1 \text{ mA}$	—	—	1.0	V
		$I_C = 100 \text{ mA}, I_F = 10 \text{ mA}$	0.3	—	1.2	

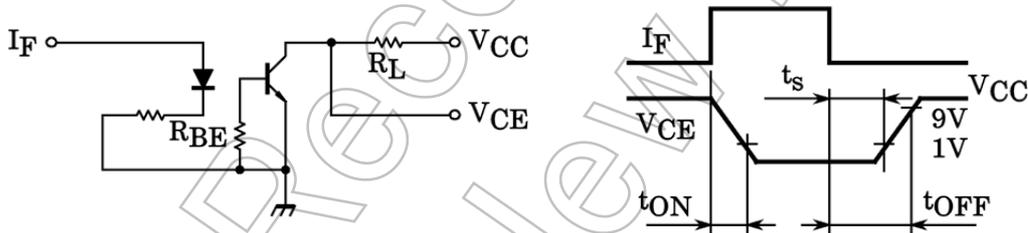
**Isolation Characteristics (Ta = 25°C)**

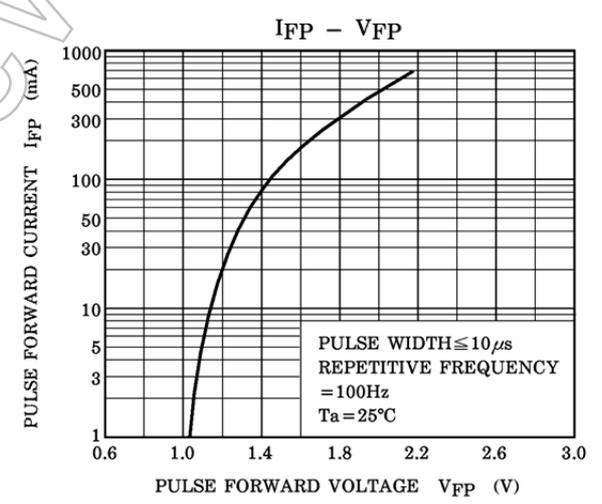
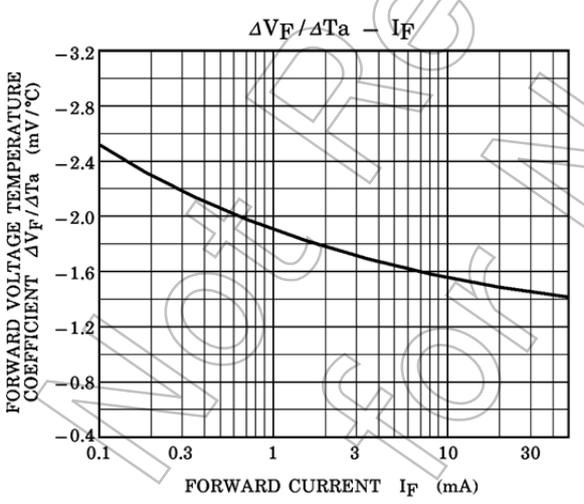
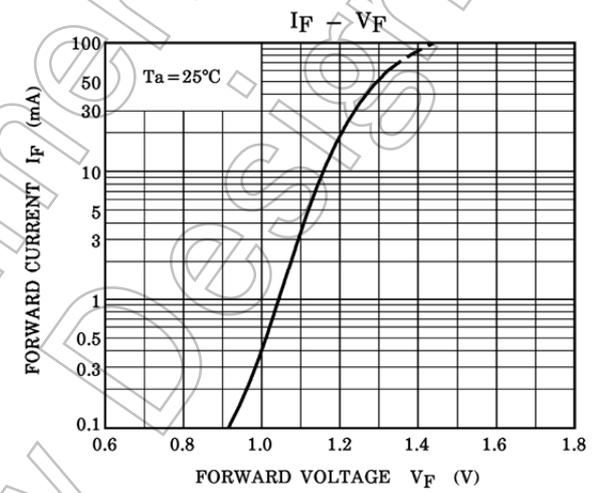
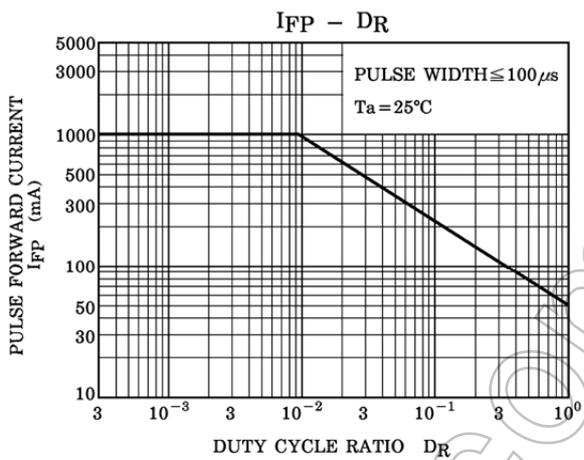
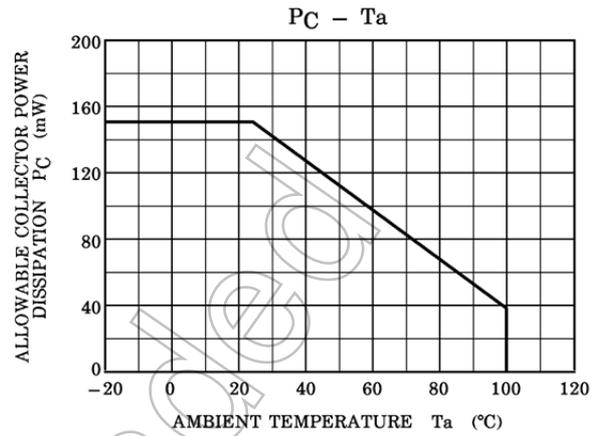
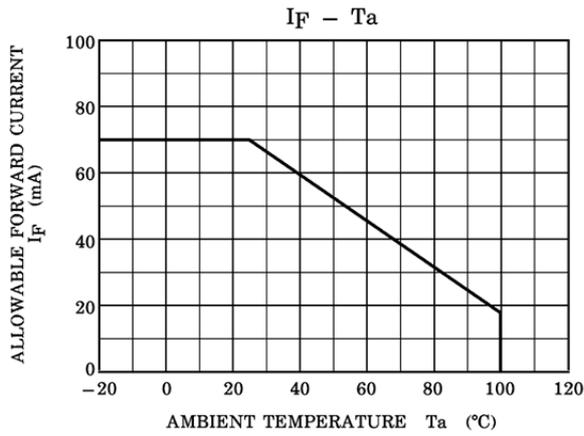
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance (input to output)	C <sub>S</sub>	V <sub>S</sub> = 0, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	—	Ω
Isolation voltage	BV <sub>S</sub>	AC, 1 minute	2500	—	—	V <sub>rms</sub>
		AC, 1 second, in oil	—	5000	—	V <sub>rms</sub>
		DC, 1 minute, in oil	—	5000	—	V <sub>dc</sub>

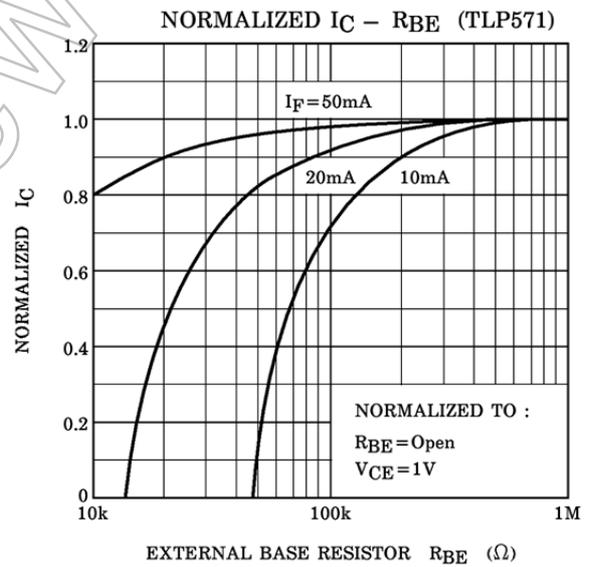
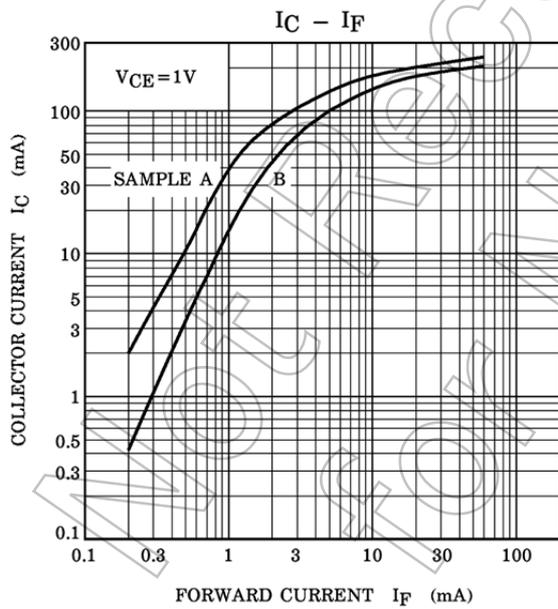
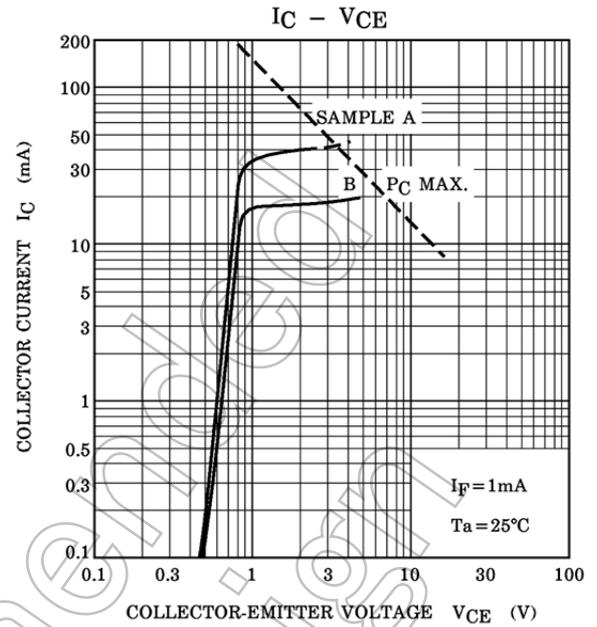
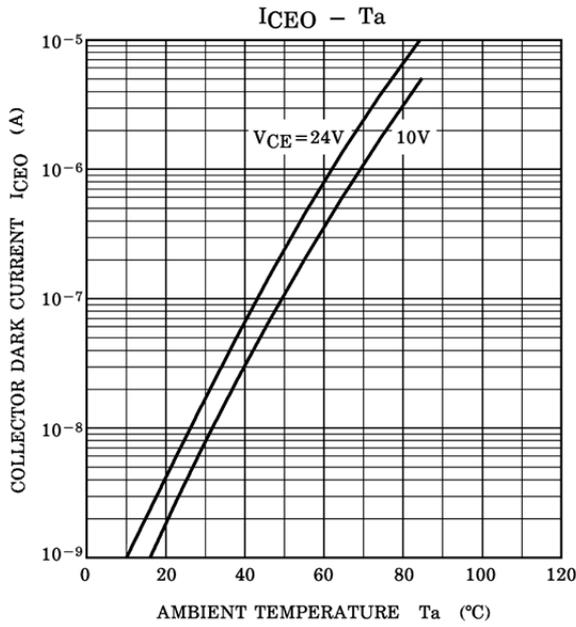
**Switching Characteristics (Ta = 25°C)**

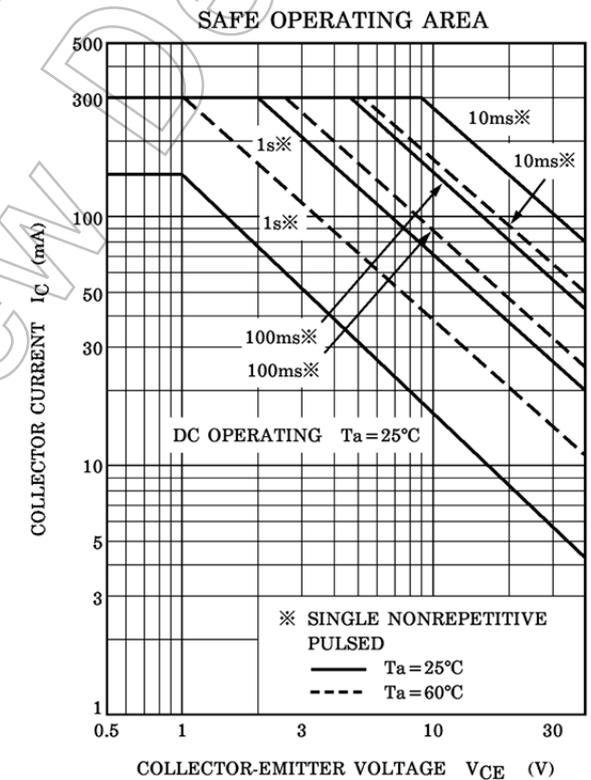
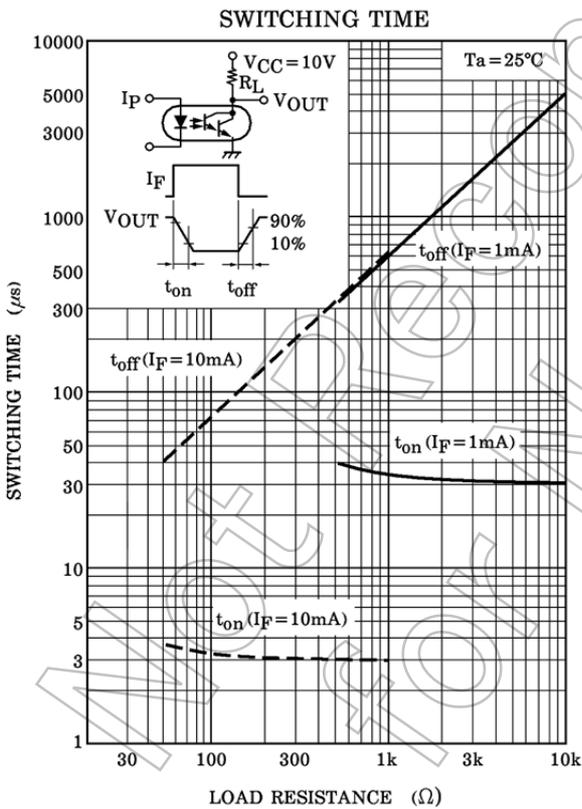
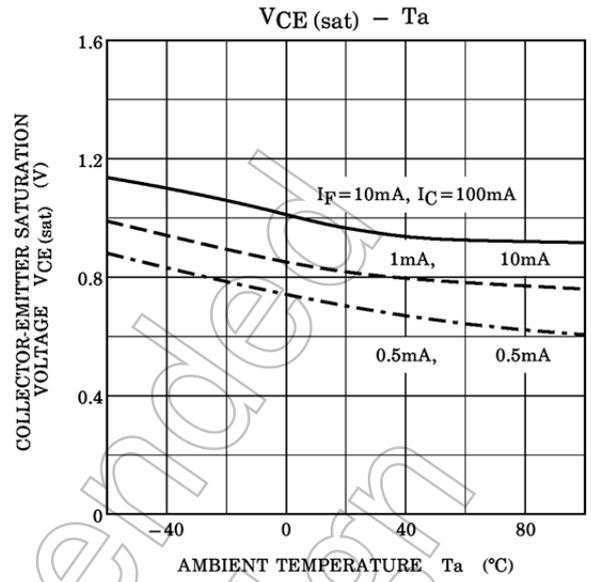
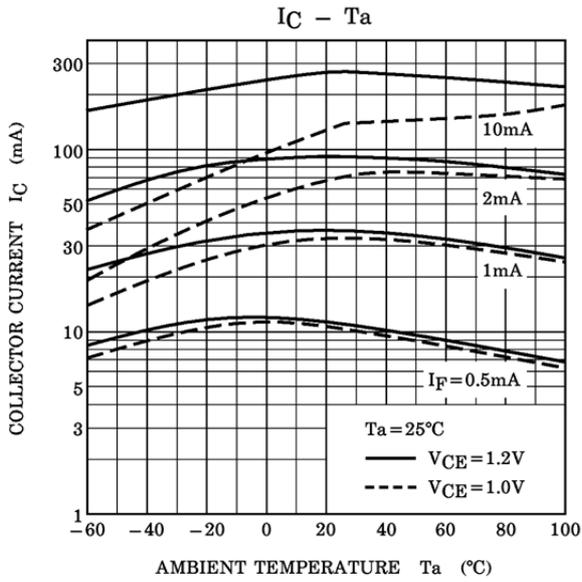
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	t <sub>r</sub>	V <sub>CC</sub> = 10 V I <sub>C</sub> = 10 mA R <sub>L</sub> = 100Ω	—	40	—	μs
Fall time	t <sub>f</sub>		—	30	—	
Turn-on time	t <sub>ON</sub>		—	45	—	
Turn-off time	t <sub>OFF</sub>		—	35	—	
Turn-on time	t <sub>ON</sub>	R <sub>L</sub> = 180Ω R <sub>BE</sub> = open V <sub>CC</sub> = 10 V, I <sub>F</sub> = 10 mA (Fig.1)	—	5	—	μs
Storage time	t <sub>s</sub>		—	20	—	
Turn-off time	t <sub>OFF</sub>		—	100	—	
Turn-on time	t <sub>ON</sub>	R <sub>L</sub> = 180Ω R <sub>BE</sub> = 10MΩ (TLP571) V <sub>CC</sub> = 10 V, I <sub>F</sub> = 10 mA (Fig.1)	—	5	—	μs
Storage time	t <sub>s</sub>		—	15	—	
Turn-off time	t <sub>OFF</sub>		—	60	—	

Fig. 1 Switching time test circuit









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