

PCI Express Solutions

Comprehensive Portfolio from the Leader in PCI Express Solutions

PCI Express Timing Solutions

Industry's broadest offering of PCIe Gen1, Gen2, Gen3 and Gen4 clock generation and buffering solutions

- Clock generators
- Zero delay buffers
- Fanout buffers and multiplexers

PCI Express Signal Retimers and Repeaters

Active signal conditioning for applications up to 8 Gbps PCIe Gen 3

- Four, eight and sixteen channels
- Compensates for cable and PCB trace attenuation and ISI jitter
- Configurable receiver equalization
- Configurable transmitter de-emphasis
- On-chip diagnostics support
- Leading edge power minimization in active and shutdown modes

PCI Express Switches

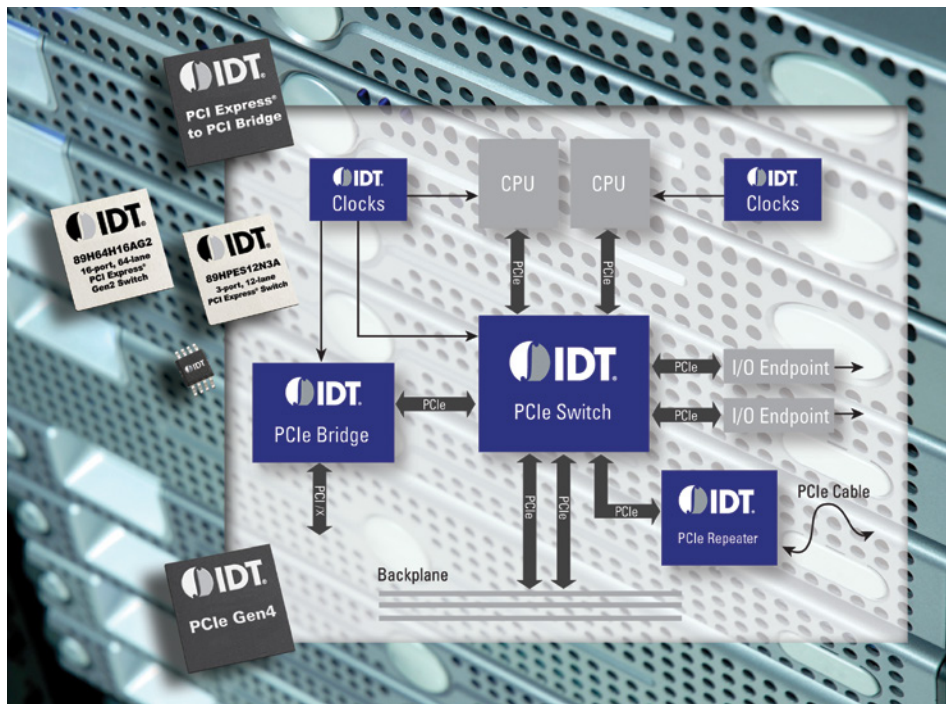
Industry's most comprehensive family of high-performance, scalable PCIe switching solutions

- Extensive portfolio
- Up to 64 lane and 24 port devices
- Highly flexible port configurations
- Unprecedented 8 non-transparent bridging (NTB) functions to enable multi-root applications

PCI Express Bridges

High performance PCIe bridging to legacy PCI and PCI-X protocols

- Ultra-low power version for consumer applications
- Forward mode buffer optimization
- The only PCI Express bridges with Short Term Caching for significant improvement in PCI Read performance
- Pin-compatible with competitive offerings for dual source solution



PCI Express® (PCIe®) is globally recognized as the general purpose I/O that unifies the component interconnect across many applications including desktop computing, servers, workstations, storage, networking, enterprise router, industrial test and control equipment, defense, aerospace and many more.

IDT provides an extensive product portfolio that tackles design requirements needed to build an entire PCI Express network, including timing solutions, switches, signal integrity and bridges.

Timing

- Clock synthesizers and spread spectrum clock generators
- PLL zero-delay buffers (ZDB)
- Non-PLL fanout buffers and multiplexers

Signal Integrity Products

- Retimers
- Repeaters

Switches

- I/O expansion switches
- System interconnect switches

Bridges

- PCIe to PCI / PCI-X Bridges
- PCI-X to PCI-X Bridges
- PCI to PCI Bridges

PCI Express timing provides the reference-clock while maintaining tight jitter specifications for all components.

PCI Express signal integrity is signal conditioning to remove signal noise and correct for trace/cable attenuation.

PCI Express switches provide the switching capacity for the entire PCI Express network.

PCI Express bridges provide connectivity between PCI Express and a different interconnect protocol.



Learn more:
idt.com/go/PCIe



Comprehensive Portfolio from the Leader in PCI Express Solutions

PCI Express® Timing Solutions

9FG Clock (Frequency) Generators

- Eliminate up to 32 resistors
- Save up to 55 mm² of area[†]
- As small as 6.25 mm²
- 85 Ω and 100 Ω system support
- SSC generation

9DB Zero-delay Fanout Buffers

- Eliminate up to 76 resistors
- Save up to 130 mm² of area[†]
- As small as 16 mm²
- 85 Ω and 100 Ω system support
- SSC compatible

9DM Fanout Multiplexers

- Eliminate up to 48 resistors
- Save up to 82 mm² of area[†]
- As small as 9 mm²
- 85 Ω and 100 Ω system support
- SSC compatible

Full-Featured Low Power PCIe® Timing Solutions

PCIe Clock Generators									
Part Number				Ref Output	PCIe Gen Compliance	PCIe Architecture Support*	Spread Spectrum Generation	Package Type	Package Dimensions (mm)
Prefix	Operating Voltage (V)	PCIe Outputs	Zout (W)						
9FG	V = 1.8 U = 1.5	02 04 06 08	31 = 33 41 = 100 41 = 100 51 = 85 P1 = Prog.	Yes	1, 2, 3	CC	0% -0.25% -0.5%	QFN-24 QFN-32 QFN-40 QFN-48	4 x 4 5 x 5 5 x 5 6 x 6
	L = 3.3**				1, 2, 3, 4	CC SRNS SRIS			
	V = 1.8	02	42 = 100	No	1, 2, 3	CC		QFN-16	3 x 3
PCIe Clock Zero-Delay/Fan-out Buffers***									
Part Number				Pin Control of PLL Mode	PCIe Gen Compliance	PCIe Architecture Support*	Spread Spectrum Compatible	Package Type	Package Dimensions (mm)
Prefix	Operating Voltage (V)	PCIe Outputs	Zout (W)						
9DB	V = 1.8 U = 1.5	02 04 06 08	31 = 33 41 = 100 41/42 = 100 51/52 = 85	Yes	1, 2, 3	CC	Yes	QFN-24 QFN-32 QFN-40 QFN-48	4 x 4 5 x 5 5 x 5 6 x 6
	L = 3.3**				1, 2, 3, 4	CC SRNS SRIS			
PCIe Clock Fan-out Buffers***									
Part Number				Pin Control of PLL Mode	PCIe Gen Compliance	PCIe Architecture Support*	Spread Spectrum Compatible	Package Type	Package Dimensions (mm)
Prefix	Operating Voltage (V)	PCIe Outputs	Zout (W)						
9DB	V = 1.8 U = 1.5	05 07 09	31 = 33 41 = 100	N/A	1, 2, 3, 4	CC	Yes	QFN-32 QFN-40 QFN-48	5 x 5 5 x 5 6 x 6
	L = 3.3**	07 09	41 = 100 51 = 85			CC SRNS SRIS		QFN-40 QFN-48	5 x 5 6 x 6
PCIe Clock Multiplexers									
Part Number				Sync/Async Switch Mode	PCIe Gen Compliance	PCIe Architecture Support*	Spread Spectrum Compatible	Package Type	Package Dimensions (mm)
Prefix	Operating Voltage (V)	PCIe Outputs	Zout (W)						
9DM	V = 1.8 U = 1.5	01 04	31 = 33 41 = 100	Yes	1, 2, 3	CC	Yes	QFN-16 QFN-24	3 x 3 4 x 4
	L = 3.3**	04	41 = 100 51 = 85		1, 2, 3, 4	CC SRNS SRIS		QFN-24	4 x 4

[†] Compared to traditional HCSL outputs

* CC = Common Clock, SRNS = Separate Reference No Spread, SRIS = Separate Reference Independent Spread

** L devices are factory customizable

*** For applications requiring more than 9 outputs, see IDT's 9ZXL line of PCIe Buffers

PCI Express® Switches

Additional products and information available — idt.com/go/PCleSwitches

PCIe® to PCI and PCI-X Bus Standards

To complement the switch products, IDT offers bridges to connect PCI® to the PCI and PCI-X bus standards. A PCIe bridge is used for bridging devices that use the PCI/X interface to provide a PCIe connection to a host processor or root complex. Applications include PCIe adapter cards, embedded computing, and motherboards to provide connection to PCI/X devices or additional PCI/X expansion slots.

- Compliant to PCIe 1.1 specification
- Low latency & high throughput features
- Proven interoperability
- Small footprint packages
- Simple power supply requirements
- Comprehensive design tools

Contact an IDT representative for details on pin-compatibility with comparable solutions.



Additional products and information available — idt.com/go/PCleBridges

Mouser Electronics

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IDT (Integrated Device Technology):

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[89KTPES48H12BG2](#) [89KTPES48H12G3](#) [89KTPES32NT8AG2](#) [89KTPES6T5](#) [89H32H8G3YAHL](#)
[89H32H8G3YAHLG](#) [89KTPES48T12G2](#) [89H32H8G3YAHLG18](#) [TSI381-RDK1 V2.1](#) [89H32H8G3YAHLI](#)
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[89H32H8G3YAHL8](#) [89KTPES4T4QFN](#) [89KTPES32NT8BG2](#) [89KTPES48H12AG2](#) [89KTPES32T8G2](#)
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