

REV. Status

PRELIMINARY
04/26/00 JC

PRELIMINARY 1
DEL. INSERTION LOSS
08/11/00 LL

PRELIMINARY 2
ADDED INSERTION
LOSS, CHANGED
MARKING, A.3,
A.5, A.7-A.9,
A.12, & SAFETY.
05/10/02 MP

ADSL transformer designed for use with Texas Instrument ADSL IC chip sets
TNETD2000R, TNETD3000R, TNETD300D, TNETD4000R (CPE side)

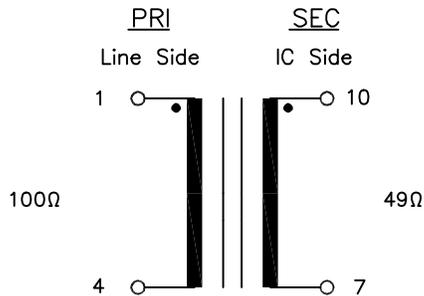
A. Electrical Specifications (@ 25° C)

1. Primary (Line side) Impedance; 100Ω
2. Secondary (IC side) Impedance; 49Ω
3. Frequency Response; ±1.0dB MAX @ 30KHz to 1.1MHz 0dBm
4. Insertion Loss; 0.5dB MAX @ 300KHz, 0dBm
5. Longitudinal Balance; 50dB MIN @ 1.1MHz
(Per ITU method pin 10 grounded)
6. Primary Open Circuit Inductance; 1.5mH±5% @ 100KHz, 0.1Vrms
Measured (1-4)
7. Leakage Inductance; 18.0μH MAX @ 100KHz, 0.1Vrms
Measured (1-4) with 7 & 10 shorted
8. Interwinding Capacitance; 35pF MAX @ 100KHz, 0.1Vrms
Measured (1-10)
9. DC Resistance;
(1-4) = 4.00Ω
(10-7) = 0.70Ω
10. Turns Ratio; (1-4):(7-10)=1.42:1±2%
11. Dielectric Strength; 1875V, 1 second @ Pri to Sec
12. Total Harmonic Distortion; -80dB MAX @ 100KHz, 15Vpp (5.3Vrms)

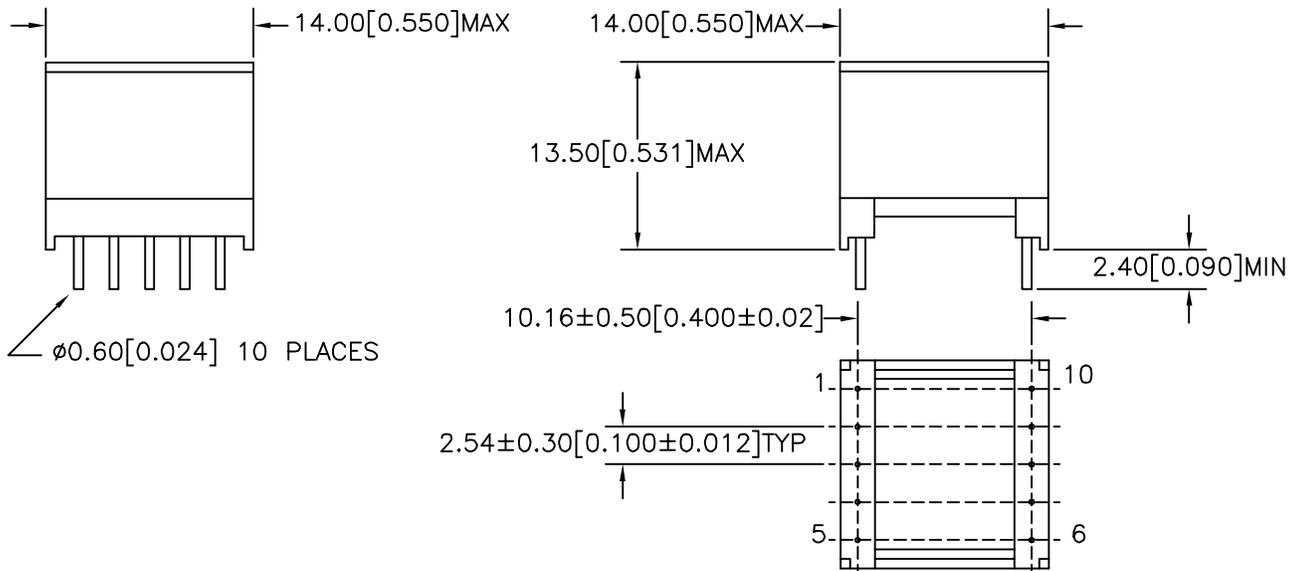
B. Marking; TTC-4054D, TAMURA, date code and country of origin
"D" designates UL approved family classification

C. Safety: UL1950 3rd Edition, UL60950, EN60950

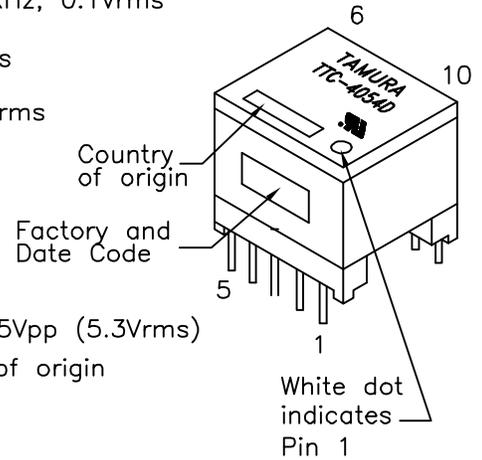
D. Schematic



E. Mechanical Specifications



MODEL NUMBER
TTC-4054



PREPARED BY:

D. Rund

ENGINEER:

M. Pitchai

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.
P-A1-12479
ACAD\TTC\A1124791.DWG

REV
PR2

ADSL THROUGH HOLE
TRANSFORMER

TAMURA CORPORATION OF AMERICA
43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624
(909) 699-1270 FAX 9096769482

TTC-4054

MODEL SPECIFICATION

DIM: mm(in) SCL: 2/1 SH: 1 OF 1

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