Power management (dual digital transistors)

UMC2N / FMC2A

●Features

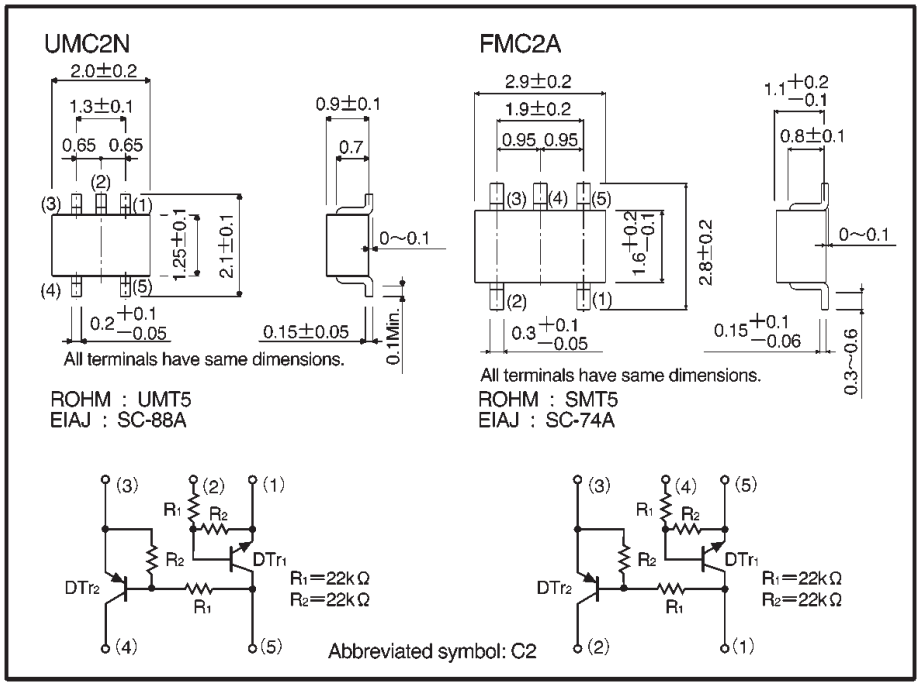
1) Includes a DTA124E and a DTC124E transistor in a single UMT and a SMT package.
2) Ideal for power switch circuits.
3) Mounting cost and area can be cut in half.

●Structure

A PNP and a NPN digital transistor (each with two built in resistors)

The following characteristics apply to both the DTr_1 and DTr_2, however, the "–" sign on DTr_2 values for the PNP type have been omitted.

●External dimensions (Units: mm)



●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Supply voltage		V_{CC}	50	V
Input current		V_{IN}	40	V
			−10	
Output current		I_O	30	mA
		$I_{C(Max.)}$	100	
Power dissipation	UMC2N	Pd	150(TOTAL)	mW ∗1
	FMC2A		300(TOTAL)	mW ∗2
Junction temperature		Tj	150	℃
Storage temperature		Tstg	−55~+150	℃

∗1 120mW per element must not be exceeded.
∗2 200mW per element must not be exceeded.

(96-393-AC124E)

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●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	0.5	V	V_{CC}=5V, I_O=100 μA
	$V_{I(on)}$	3	—	—		V_O=0.2V, I_O=5mA
Output voltage	$V_{O(on)}$	—	0.1	0.3	V	I_O/I_I=10mA/0.5mA
Input current	I_I	—	—	0.36	mA	V_I=5V
Output current	$I_{O(off)}$	—	—	0.5	μA	V_{CC}=50V, V_I=0V
DC current gain	G_I	56	—	—	—	V_O=5V, I_O=5mA
Transition frequency	f_T	—	250	—	MHz	V_{CE}=10mA, I_E=−5mA, f=100MHz *
Input resistance	R_1	15.4	22	28.6	kΩ	—
Resistance ratio	R_2/R_1	0.8	1	1.2	—	—

* Transition frequency of the device

●Packaging specifications

Part No.	Packaging type	Taping	
	Code	TR	T148
	Basic ordering unit (pieces)	3000	3000
UMC2N		○	—
FMC2A		—	○

●Electrical characteristic curves

DTr1

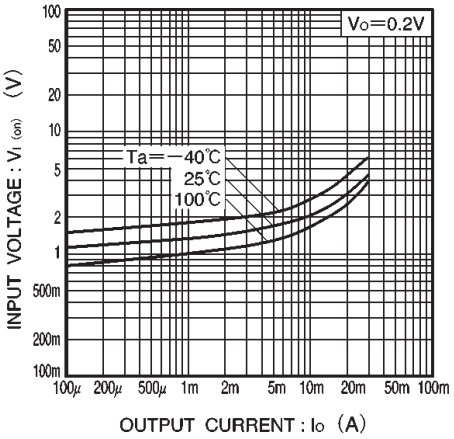

Fig.1 Input voltage vs. output current (ON characteristics)

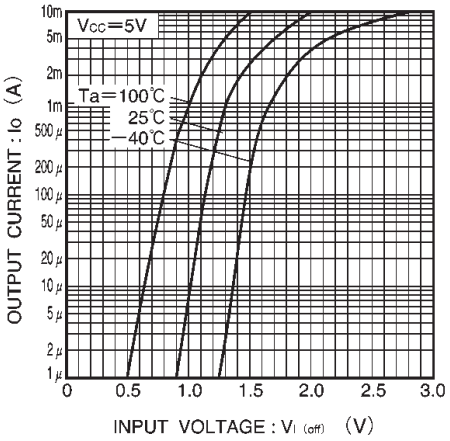

Fig.2 Output current vs. input voltage (OFF characteristics)

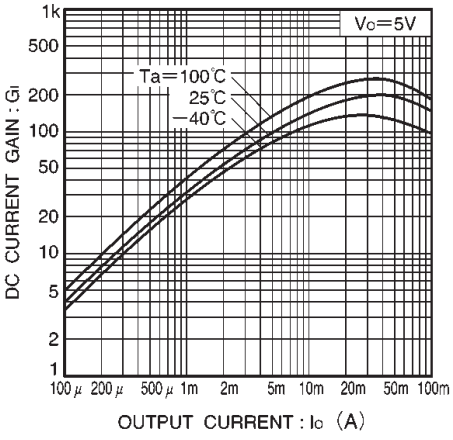

Fig.3 DC current gain vs. output current

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DTr2

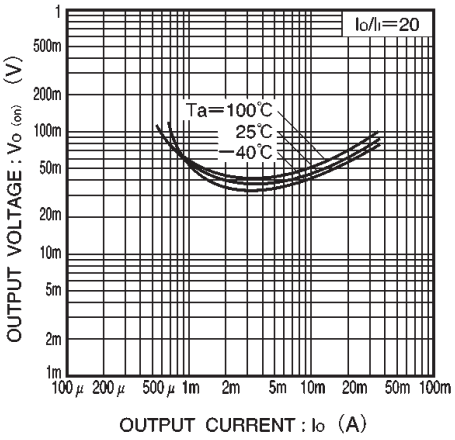

Fig.4 Output voltage vs. output current

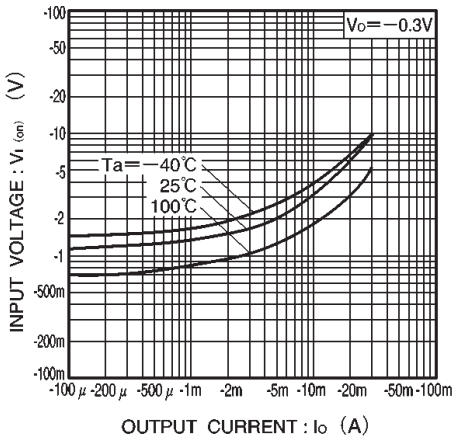

Fig.5 Input voltage vs. output current (ON characteristics)

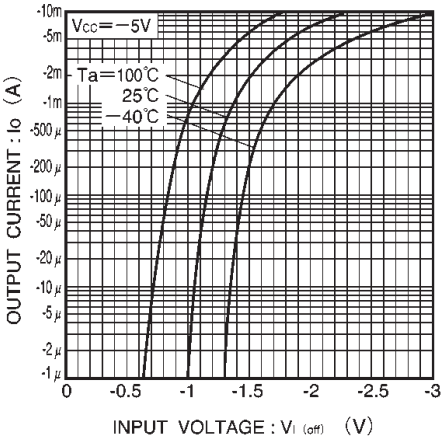

Fig.6 Output current vs. input voltage (OFF characteristics)

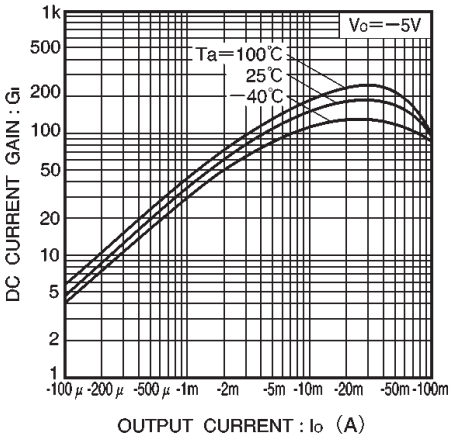

Fig.7 DC current gain vs. output current

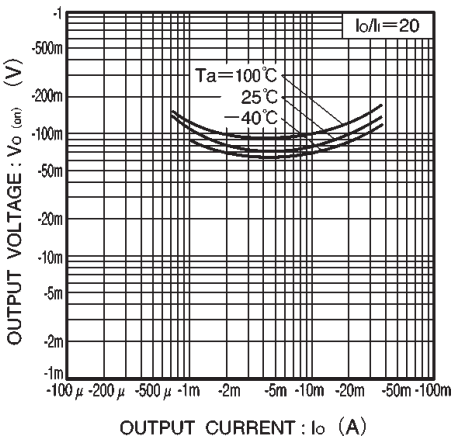

Fig.8 Output voltage vs. output current

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