

## MOS FIELD EFFECT TRANSISTOR $\mu$ PA1912

### P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

### DESCRIPTION

The  $\mu$ PA1912 is a switching device which can be driven directly by a 2.5-V power source.

The  $\mu\text{PA1912}$  features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

### **FEATURES**

- Can be driven by a 2.5-V power source
- · Low on-state resistance

RDS(on)1 = 50 m $\Omega$  MAX. (VGS = -4.5 V, ID = -2.5 A)

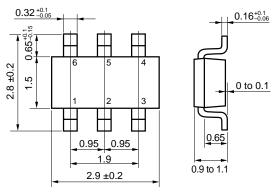
RDS(on)2 = 52 m $\Omega$  MAX. (VGS = -4.0 V, ID = -2.5 A)

RDS(on)3 = 70 m $\Omega$  MAX. (VGS = -2.5 V, ID = -2.5 A)

### **ORDERING INFORMATION**

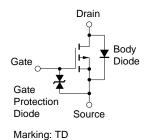
PART NUMBER	PACKAGE		
μPA1912TE	6-pin Mini Mold (Thin Type)		

### PACKAGE DRAWING (Unit: mm)



1, 2, 5, 6 : Drain 3 : Gate : Source

### **EQUIVALENT CIRCUIT**



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C)

Drain to Source Voltage	VDSS	-12	V
Gate to Source Voltage	Vgss	±10	V
Drain Current (DC)	ID(DC)	±4.5	Α
Drain Current (pulse) Note1	D(pulse)	±18	Α
Total Power Dissipation	P <sub>T1</sub>	0.2	W
Total Power Dissipation Note2	P <sub>T2</sub>	2	W
Channel Temperature	Tch	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %

**2.** Mounted on FR-4 board,  $t \le 5$  sec.

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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The mark ★ shows major revised points.

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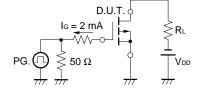
### **ELECTRICAL CHARACTERISTICS (TA = 25 °C)**

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = -12 V, V <sub>GS</sub> = 0 V			-10	μΑ
Gate Leakage Current	Igss	Vgs = ±10 V, Vps = 0 V			±10	μΑ
Gate to Source Cut-off Voltage	VGS(off)	VDS = -10 V, ID = -1 mA	-0.5	-0.90	-1.5	٧
Forward Transfer Admittance	yfs	$V_{DS} = -10 \text{ V}, I_{D} = -2.5 \text{ A}$	3	9.3		S
Drain to Source On-state Resistance	RDS(on)1	$V_{GS} = -4.5  \text{V},  I_{D} = -2.5  \text{A}$		39	50	mΩ
	RDS(on)2	$V_{GS} = -4.0 \text{ V}, I_{D} = -2.5 \text{ A}$		40	52	mΩ
	RDS(on)3	$V_{GS} = -2.5  V,  I_{D} = -2.5  A$		53	70	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = -10 V		810		pF
Output Capacitance	Coss	VGS = 0 V		241		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		122		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = -6 V		304		ns
Rise Time	<b>t</b> r	ID = -2.5 A		532		ns
Turn-off Delay Time	t <sub>d(off)</sub>	V <sub>GS(on)</sub> = -4.0 V		406		ns
Fall Time	tf	$R_G = 10 \Omega$		796		ns
Total Gate Charge	QG	VDD = −10 V		5.6		nC
Gate to Source Charge	Qgs	ID = -4.5 A		2.2		nC
Gate to Drain Charge	Q <sub>GD</sub>	V <sub>GS</sub> = -4.0 V		2.6		nC
Diode Forward Voltage	V <sub>F(S-D)</sub>	IF = 4.5 A, VGS = 0 V		0.86		V
Reverse Recovery Time	trr	IF = 4.5 A, VGS = 0 V		1.1		μs
Reverse Recovery Charge	Qrr	$di/dt = 10 A/\mu s$		4.3		μC

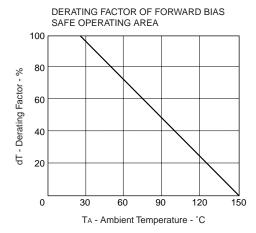
### **TEST CIRCUIT 1 SWITCHING TIME**

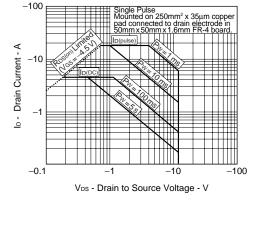
## PG. $\bigcap_{R_G} R_G = 10 \Omega$ $\tau = 1 \mu \text{ S}$ Duty Cycle $\leq 1 \%$

### **TEST CIRCUIT 2 GATE CHARGE**

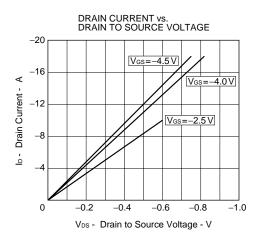


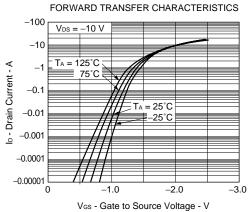
### TYPICAL CHARCTERISTICS (TA = 25 °C)

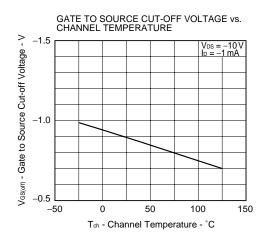


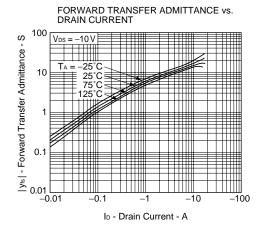


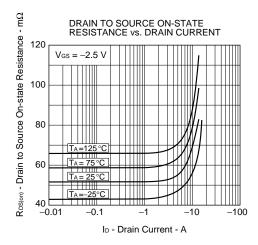
FORWARD BIAS SAFE OPERATING AREA

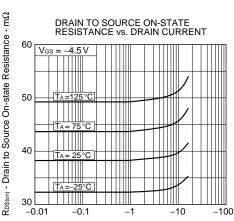




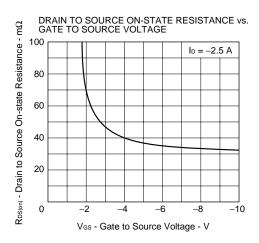


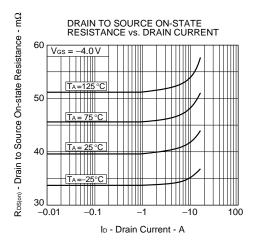


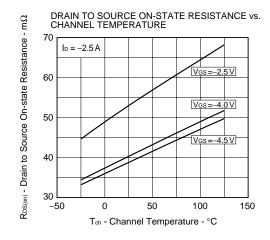


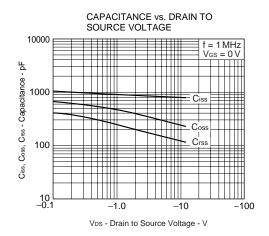


ID - Drain Current - A

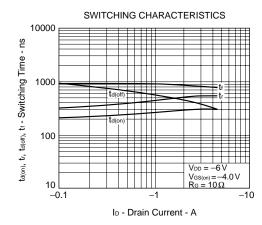








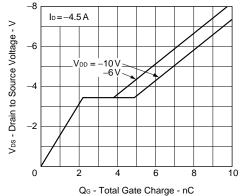




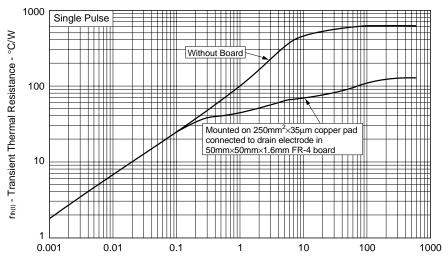
# SOURCE TO DRAIN DIODE FORWARD VOLTAGE 100 4100 900 0.1 0.01 0.01 0.4 0.6 0.8 1.0 1.2

V<sub>F(S-D)</sub> - Source to Drain Voltage - V

### DYNAMIC INPUT CHARACTERISTICS



### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



PW - Pulse Width - s

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