CEL

GaAs INTEGRATED CIRCUIT

μPG2406TK

0.01 to 3.0 GHz SPDT SWITCH

DESCRIPTION

The μPG2406TK is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which were designed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 5.3 V. This device can operate frequency from 0.01 to 3.0 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin lead-less minimold package. And this package is able to high-density surface mounting.

FEATURES

- Switch control voltage : $V_{cont\ (H)}$ = 1.8 to 5.3 V (2.7 V TYP.)
  : $V_{cont\ (L)}$ = −0.2 to +0.2 V (0 V TYP.)
- Low insertion loss : L_{ins} = 0.40 dB TYP. @ f = 1.0 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
  : L_{ins} = 0.47 dB TYP. @ f = 2.5 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
- High isolation : ISL = 27 dB TYP. @ f = 1.0 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
  : ISL = 17 dB TYP. @ f = 2.5 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
- Handling power : $P_{in\ (0.1\ dB)}$ = +29.0 dBm TYP. @ f = 2.0/2.5 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
  : $P_{in\ (1\ dB)}$ = +30.5 dBm TYP. @ f = 0.5 to 3.0 GHz, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V
- High-density surface mounting : 6-pin lead-less minimold package (1.5 × 1.1 × 0.55 mm)

APPLICATIONS

- L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth™ etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2406TK-E2	μPG2406TK-E2-A	6-pin lead-less minimold (1511 PKG) (Pb-Free)	G5K	• Embossed tape 8 mm wide • Pin 1, 6 face the perforation side of the tape • Qty 5 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: μPG2406TK-A

Caution Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

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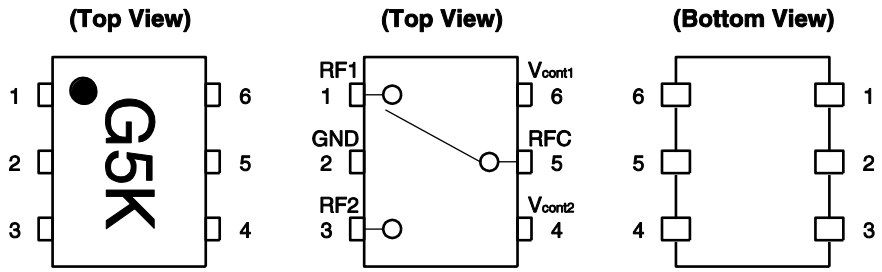
PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	RF1
2	GND
3	RF2
4	V_{cont2}
5	RFC
6	V_{cont1}

SW TRUTH TABLE

ON Path	V_{cont1}	V_{cont2}
RFC-RF1	High	Low
RFC-RF2	Low	High

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Switch Control Voltage		V_{cont}	+6.0 Note	V
Input Power	f = 0.01 to 0.5 GHz	$P_{in}1$	+24.0	dBm
	f = 0.5 to 3.0 GHz	$P_{in}2$	+31.0	
Operating Ambient Temperature		T_A	−45 to +85	°C
Storage Temperature		T_{stg}	−55 to +150	°C

Note $|V_{cont1} - V_{cont2}| \leq 6.0$ V

RECOMMENDED OPERATING RANGE (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	$V_{cont\ (H)}$	1.8	2.7	5.3	V
Switch Control Voltage (L)	$V_{cont\ (L)}$	−0.2	0	0.2	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, $V_{cont\ (H)}$ = 2.7 V, $V_{cont\ (L)}$ = 0 V, DC blocking capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	$L_{ins}1$	f = 0.01 to 0.05 GHz Note 1	–	0.40	–	dB
Insertion Loss 2	$L_{ins}2$	f = 0.05 to 0.5 GHz Note 2	–	0.40	0.45	dB
Insertion Loss 3	$L_{ins}3$	f = 0.5 to 1.0 GHz	–	0.40	0.45	dB
Insertion Loss 4	$L_{ins}4$	f = 1.0 to 2.0 GHz	–	0.45	0.50	dB
Insertion Loss 5	$L_{ins}5$	f = 2.0 to 2.5 GHz	–	0.47	0.55	dB
Insertion Loss 6	$L_{ins}6$	f = 2.5 to 3.0 GHz	–	0.53	0.60	dB
Isolation 1	ISL1	f = 0.01 to 0.05 GHz Note 1	–	27	–	dB
Isolation 2	ISL2	f = 0.05 to 0.5 GHz Note 2	23	27	–	dB
Isolation 3	ISL3	f = 0.5 to 1.0 GHz	23	27	–	dB
Isolation 4	ISL4	f = 1.0 to 2.0 GHz	16	19	–	dB
Isolation 5	ISL5	f = 2.0 to 2.5 GHz	14	17	–	dB
Isolation 6	ISL6	f = 2.5 to 3.0 GHz	14	17	–	dB
Input Return Loss 1	$RL_{in}1$	f = 0.01 to 0.05 GHz Note 1	–	20	–	dB
Input Return Loss 2	$RL_{in}2$	f = 0.05 to 0.5 GHz Note 2	15	20	–	dB
Input Return Loss 3	$RL_{in}3$	f = 0.5 to 3.0 GHz	15	20	–	dB
Output Return Loss 1	$RL_{out}1$	f = 0.01 to 0.05 GHz Note 1	–	20	–	dB
Output Return Loss 2	$RL_{out}2$	f = 0.05 to 0.5 GHz Note 2	15	20	–	dB
Output Return Loss 3	$RL_{out}3$	f = 0.5 to 3.0 GHz	15	20	–	dB
0.1 dB Loss Compression Input Power Note 3	$P_{in\ (0.1\ dB)}$	f = 2.0/2.5 GHz	+26.0	+29.0	–	dBm
		f = 0.5 to 3.0 GHz	–	+29.0	–	dBm
1 dB Loss Compression Input Power Note 4	$P_{in\ (1\ dB)}$	f = 0.5 to 3.0 GHz	–	+30.5	–	dBm
2nd Harmonics	$2f_0$	f = 2.0/2.5 GHz, P_{in} = +20 dBm	65	75	–	dBc
3rd Harmonics	$3f_0$	f = 2.0/2.5 GHz, P_{in} = +20 dBm	65	75	–	dBc
Intermodulation Intercept Point	IIP_3	f = 0.5 to 3.0 GHz, 2 tone, 5 MHz spicing	–	+60	–	dBm
Switch Control Current	I_{cont}	No RF input	–	0.2	20	μA
Switch Control Speed	t_{SW}	50% CTL to 90/10% RF	–	50	500	ns

Notes 1. DC blocking capacitors = 10 000 pF at f = 0.01 to 0.05 GHz

2. DC blocking capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

3. $P_{in\ (0.1\ dB)}$ is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

4. $P_{in\ (1\ dB)}$ is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

Caution This device is used it is necessary to use DC blocking capacitors.
The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF.

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, $V_{cont\ (H)}$ = 1.8 V, $V_{cont\ (L)}$ = 0 V, DC blocking capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 7	$L_{ins}7$	f = 0.01 to 0.05 GHz Note 1	–	0.40	–	dB
Insertion Loss 8	$L_{ins}8$	f = 0.05 to 0.5 GHz Note 2	–	0.40	0.46	dB
Insertion Loss 9	$L_{ins}9$	f = 0.5 to 1.0 GHz	–	0.40	0.47	dB
Insertion Loss 10	$L_{ins}10$	f = 1.0 to 2.0 GHz	–	0.46	0.52	dB
Insertion Loss 11	$L_{ins}11$	f = 2.0 to 2.5 GHz	–	0.48	0.57	dB
Insertion Loss 12	$L_{ins}12$	f = 2.5 to 3.0 GHz	–	0.54	0.62	dB
Isolation 7	ISL7	f = 0.01 to 0.05 GHz Note 1	–	27	–	dB
Isolation 8	ISL8	f = 0.05 to 0.5 GHz Note 2	23	27	–	dB
Isolation 9	ISL9	f = 0.5 to 1.0 GHz	23	27	–	dB
Isolation 10	ISL10	f = 1.0 to 2.0 GHz	16	19	–	dB
Isolation 11	ISL11	f = 2.0 to 2.5 GHz	14	17	–	dB
Isolation 12	ISL12	f = 2.5 to 3.0 GHz	14	17	–	dB
Input Return Loss 4	$RL_{in}4$	f = 0.01 to 0.05 GHz Note 1	–	20	–	dB
Input Return Loss 5	$RL_{in}5$	f = 0.05 to 0.5 GHz Note 2	15	20	–	dB
Input Return Loss 6	$RL_{in}6$	f = 0.5 to 3.0 GHz	15	20	–	dB
Output Return Loss 4	$RL_{out}4$	f = 0.01 to 0.05 GHz Note 1	–	20	–	dB
Output Return Loss 5	$RL_{out}5$	f = 0.05 to 0.5 GHz Note 2	15	20	–	dB
Output Return Loss 6	$RL_{out}6$	f = 0.5 to 3.0 GHz	15	20	–	dB
0.1 dB Loss Compression Input Power Note 3	$P_{in\ (0.1\ dB)}$	f = 2.0/2.5 GHz	+19.0	+22.0	–	dBm
		f = 0.5 to 3.0 GHz	–	+22.0	–	dBm
1 dB Loss Compression Input Power Note 4	$P_{in\ (1\ dB)}$	f = 0.5 to 3.0 GHz	–	+25.0	–	dBm
Switch Control Current	I_{cont}	No RF input	–	0.2	20	μA
Switch Control Speed	t_{SW}	50% CTL to 90/10% RF	–	50	500	ns

Notes 1. DC blocking capacitors = 10 000 pF at f = 0.01 to 0.05 GHz

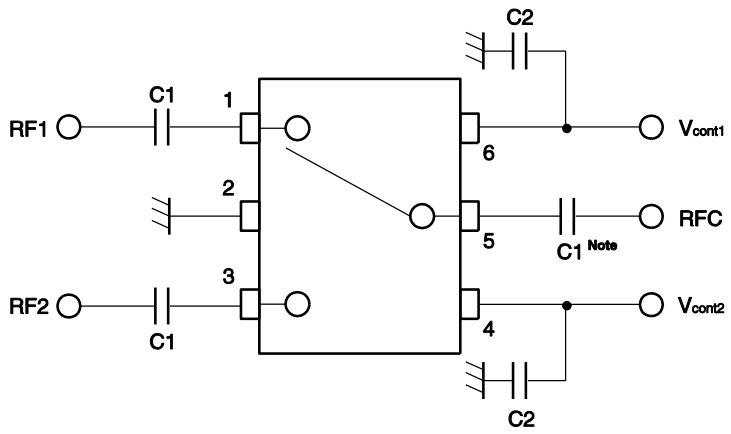
2. DC blocking capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

3. $P_{in\ (0.1\ dB)}$ is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

4. $P_{in\ (1\ dB)}$ is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

Caution This device is used it is necessary to use DC blocking capacitors.
The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF.

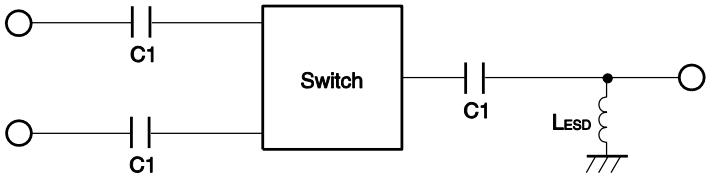
EVALUATION CIRCUIT



Note C1 : 0.01 to 0.05 GHz 10 000 pF
: 0.05 to 0.5 GHz 1 000 pF
: 0.5 to 3.0 GHz 56 pF
C2 : 1 000 pF

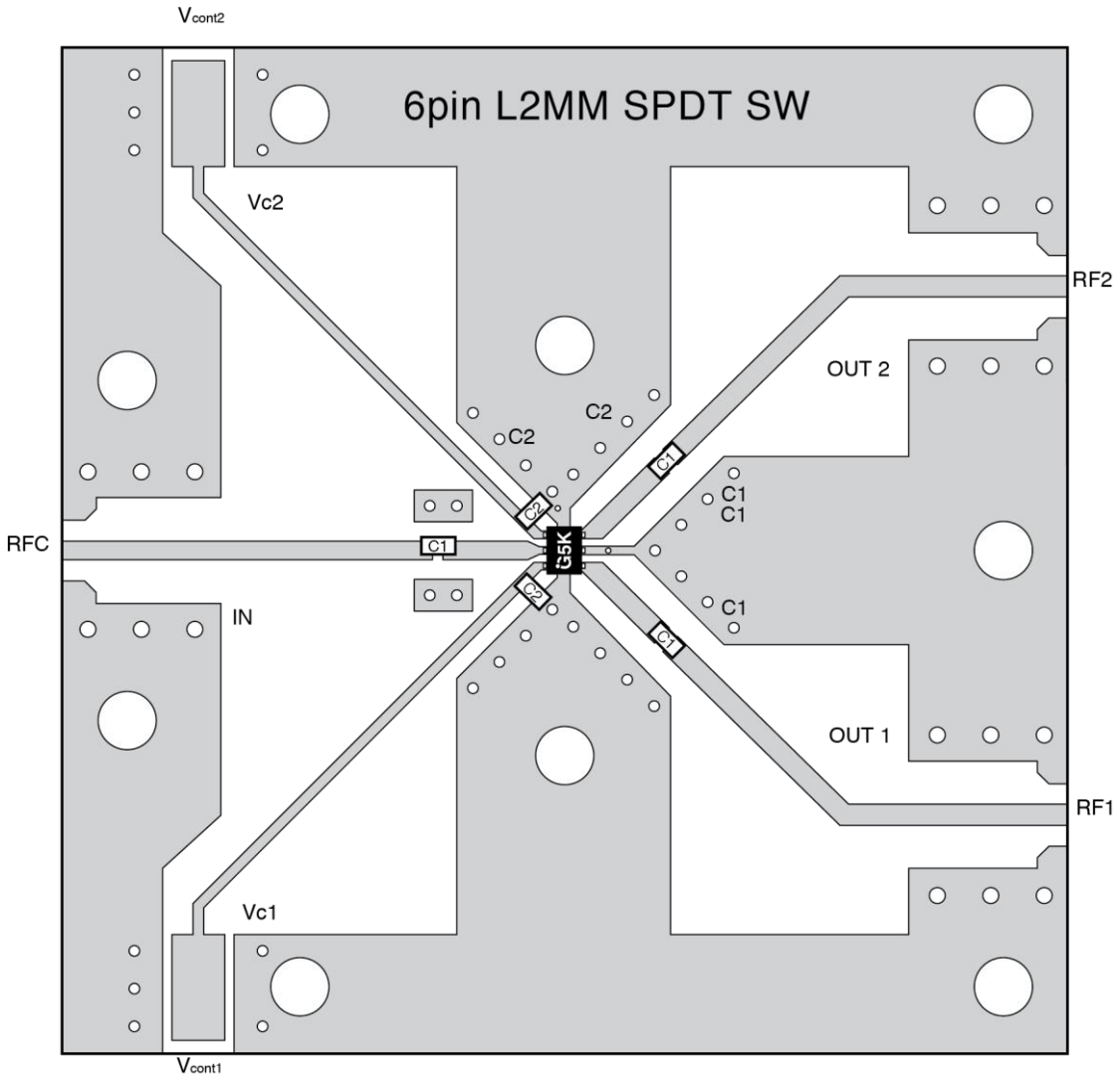
The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

APPLICATION INFORMATION



- L_{ESD} provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.
- The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

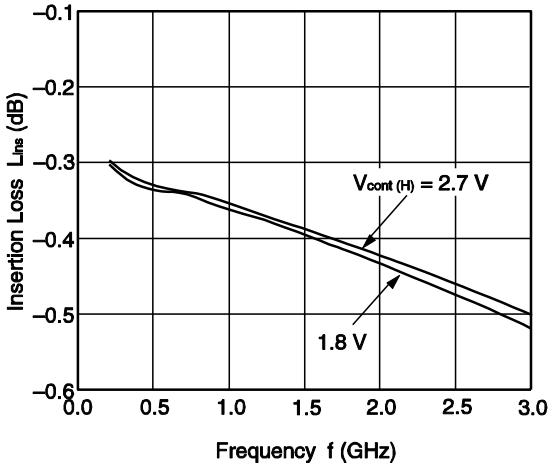


USING THE EVALUATION BOARD

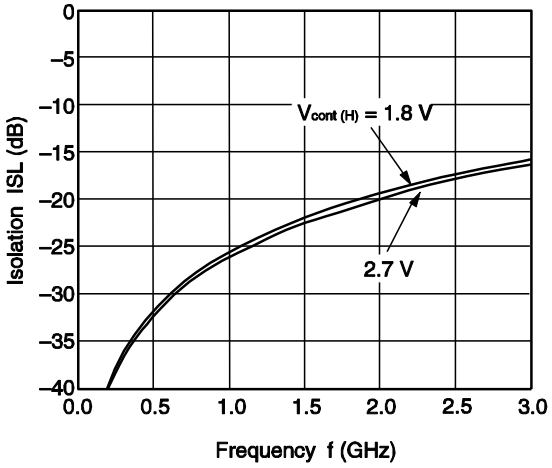
Symbol	Test Conditions	Values
C1	f = 0.01 to 0.05 GHz	10 000 pF
	f = 0.05 to 0.5 GHz	1 000 pF
	f = 0.5 to 3.0 GHz	56 pF
C2		1 000 pF

TYPICAL CHARACTERISTICS (T_A = +25°C, DC blocking capacitors = 56 pF, unless otherwise specified)

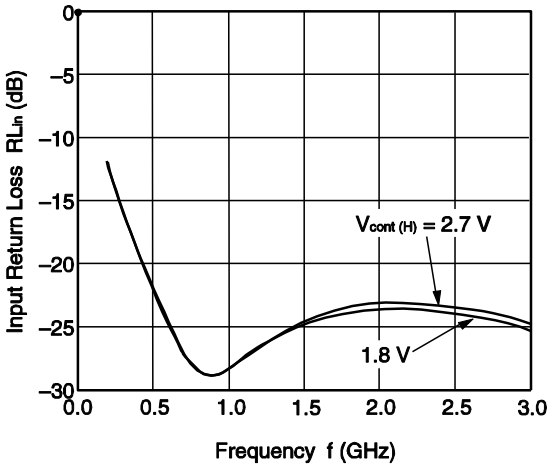
RFC-RF1/RF2
INSERTION LOSS vs. FREQUENCY



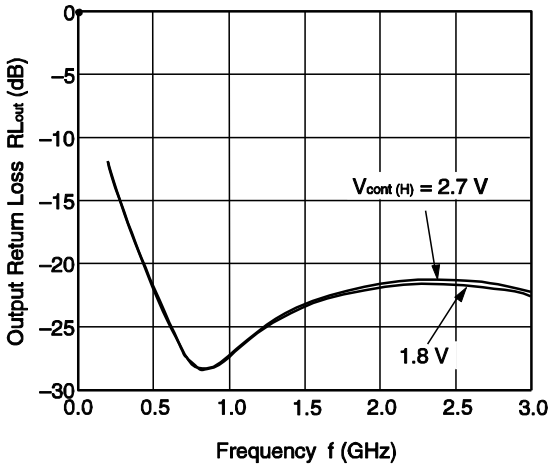
RFC-RF1/RF2
ISOLATION vs. FREQUENCY



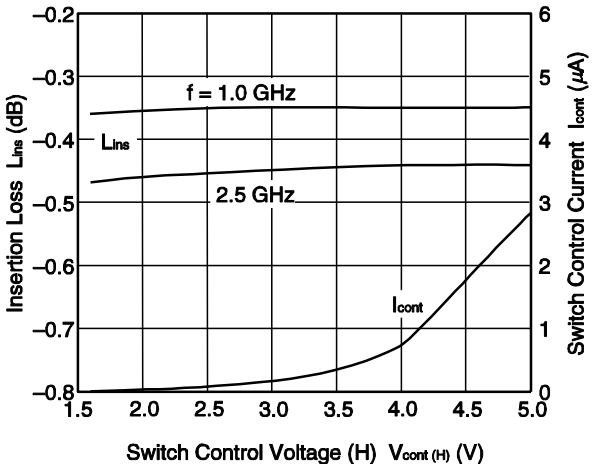
RFC-RF1/RF2
INPUT RETURN LOSS vs. FREQUENCY



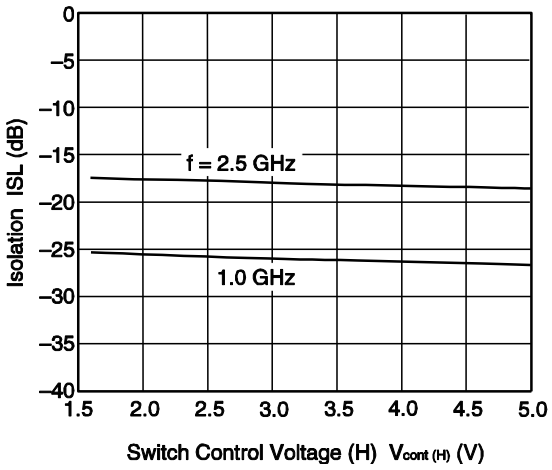
RFC-RF1/RF2
OUTPUT RETURN LOSS vs. FREQUENCY



RFC-RF1/RF2 INSERTION LOSS,
I_{cont} vs. SWITCH CONTROL VOLTAGE (H)

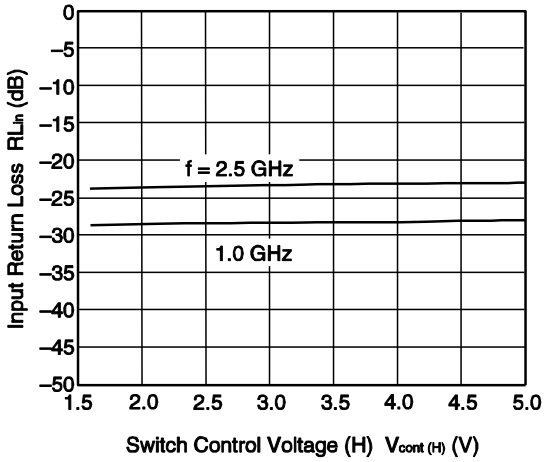


RFC-RF1/RF2 ISOLATION vs.
SWITCH CONTROL VOLTAGE (H)

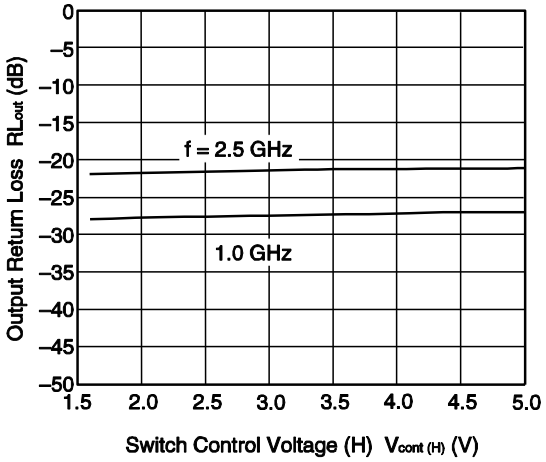


Remark The graphs indicate nominal characteristics.

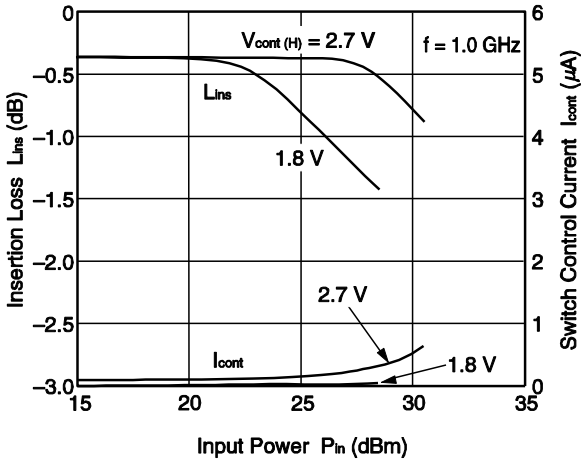
RFC-RF1/RF2 INPUT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)



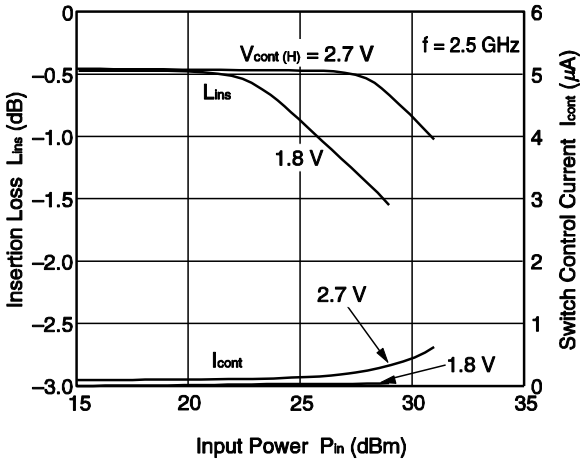
RFC-RF1/RF2 OUTPUT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)



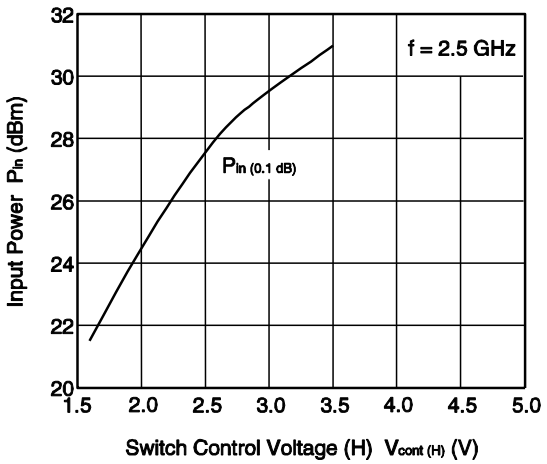
RFC-RF1/RF2 INSERTION LOSS, Icont vs. INPUT POWER



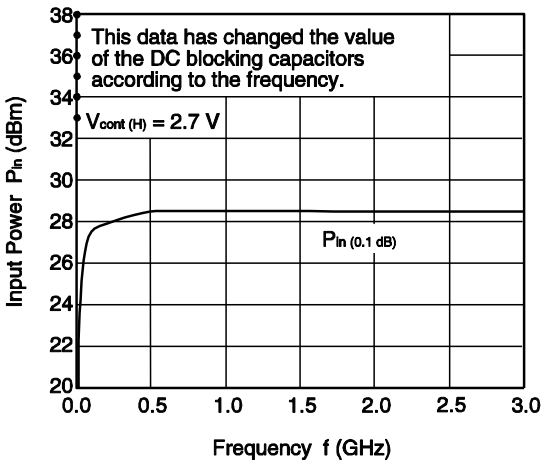
RFC-RF1/RF2 INSERTION LOSS, Icont vs. INPUT POWER



RFC-RF1/RF2 INPUT POWER vs. SWITCH CONTROL VOLTAGE (H)



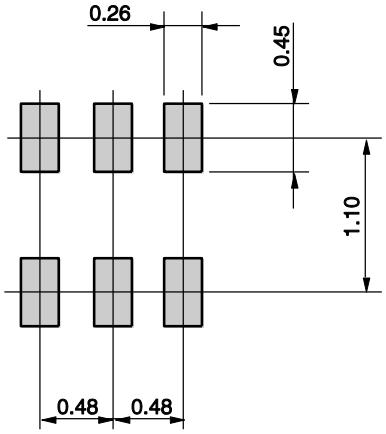
RFC-RF1/RF2 INPUT POWER vs. FREQUENCY



Remark The graphs indicate nominal characteristics.

MOUNTING PAD LAYOUT DIMENSIONS

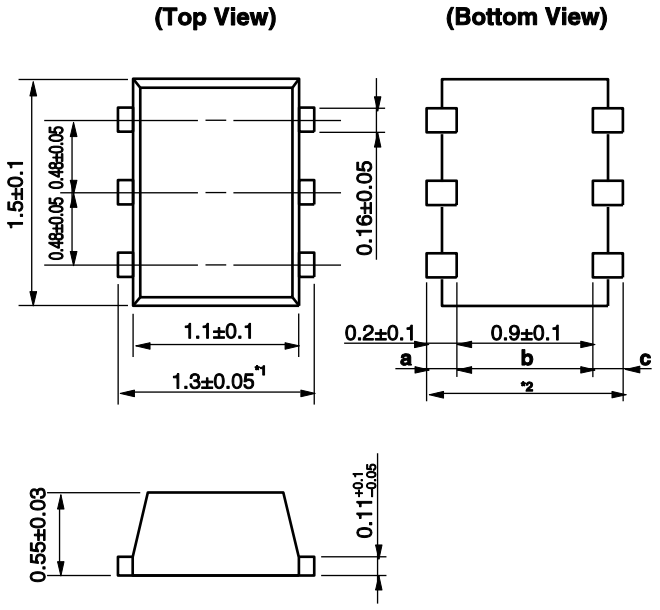
6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)



Remark The mounting pad layout in this document is for reference only.

PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)



Remark Dimension*1 is bigger than dimension*2 (dimension*2 = a + b + c).

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points. • Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below. 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials. 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal. • Do not burn, destroy, cut, crush, or chemically dissolve the product. • Do not lick the product or in any way allow it to enter the mouth.

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