

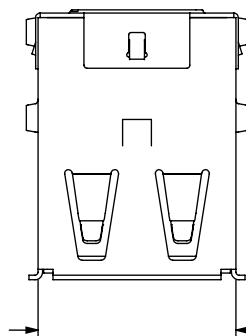
Distributed by:



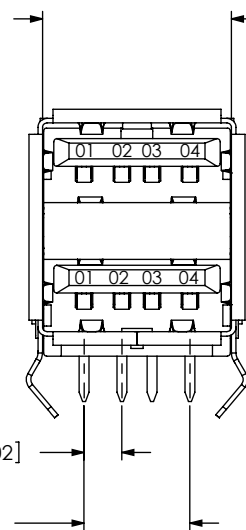
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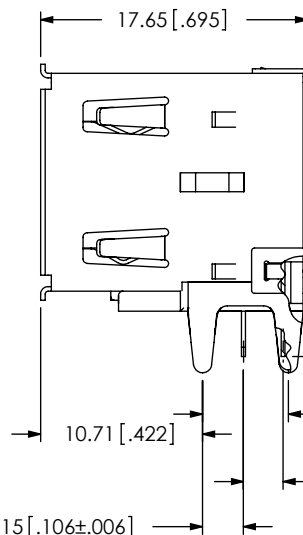
13.10±0.15 [.516±.006]



C1 2.50±0.05 [.098±.002]

C2 7.00±0.15 [.276±.006]

12.50±0.08 [.492±.003]



C3 2.71±0.15 [.106±.006]

10.71 [.422]

17.65 [.695]

C5 3.00±0.15 [.118±.006]

C4 5.68±0.08 [.224±.003]

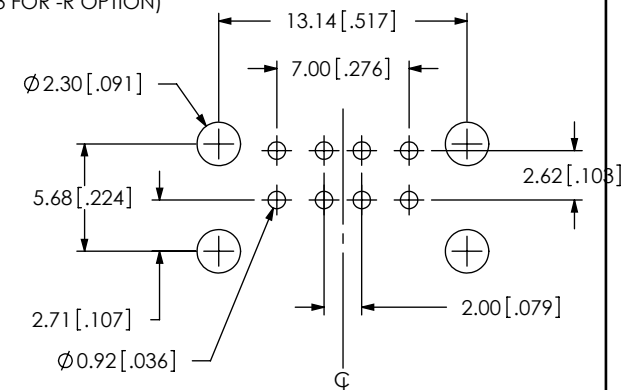
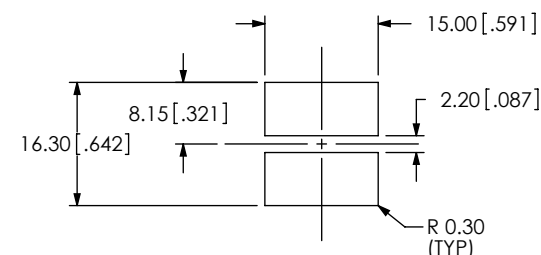
2.62±0.15 [.103±.006]

USB-A-D-X-X-TH-X

TYPE
-ACONFIGURATION
-D: DOUBLEOPTION
-R: ROHS COMPLIANTTERMINATION
-TH: THROUGH HOLECOLOR
-B: BLACK
-W: WHITE

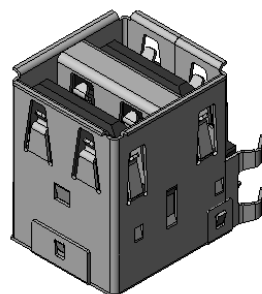
PLATING SPECIFICATION

- C6 -F: GOLD FLASH ON MATING AREA
TIN-LEAD ON TAILS (Sn:60%, Pb:40%)
WITH NICKEL UNDERPLATE
(SEE NOTE 3 FOR -R OPTION)
-S: SELECTIVE, .000030 GOLD ON MATING AREA
TIN-LEAD ON TAILS (Sn:60%, Pb:40%)
WITH NICKEL UNDERPLATE
(SEE NOTE 3 FOR -R OPTION)

RECOMMENDED PCB LAYOUT
TOP VIEWREFERENCE PANEL CUT OUT
DIMENSION

NOTES:

1. C1 REPRESENTS A CRITICAL DIMENSION.
2. DELETED
3. PARTS WITH -R HAVE BRIGHT ACID TIN ON TAILS.

UNLESS OTHERWISE SPECIFIED,
DIMENSIONS ARE IN MM.

TOLERANCES ARE:

DECIMALS	ANGLES
.X: ±.3 [.01]	2°
.XX: ±.13 [.005]	
.XXX: ±.051 [.0020]	

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MATERIAL: DO NOT SCALE DRAWING

INSULATOR: PBT
CONTACT: PHOS BRONZE

SHEET SCALE: 2:1

DESCRIPTION:

USB A, DOUBLE, THROUGH HOLE

DWG. NO.

USB-A-D-X-X-TH-X

BY: B.HARPENAU 2/06/06 SHEET 1 OF 1

520 PARK EAST BLVD, NEW ALBANY, IN 47150
PHONE: 812-944-6733 FAX: 812-948-5047
e-Mail: info@SAMTEC.com code: 55322