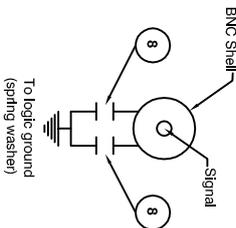
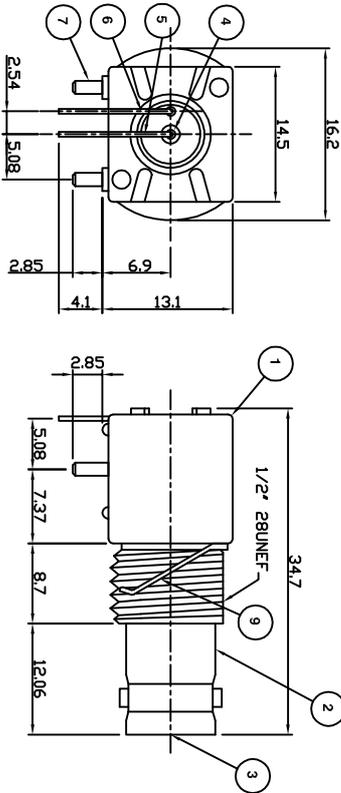
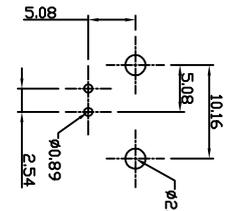


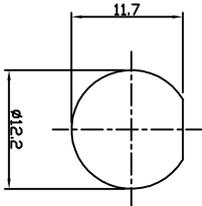
ROHS COMPLIANT



Capacitor Circuit Diagram



Recommended PCB Layout



Panel Cutout

Electrical Characteristics

Nominal Impedance: 50 ohms
 Frequency Range: DC to 1 GHz
 Operating Voltage (rms): 500 V maximum at sea level
 Dielectric Withstand Voltage (rms): 500 V maximum at sea level
 Contact Resistance: 3.0 milliohms maximum
 Insulation Resistance: 5000 megohms minimum

Mechanical Characteristics

Mating Cycles: 500 cycles minimum
 Interface Dimensions: Conform to MIL-C-39012

Environmental Characteristics

Temperature Range: -65 °C to +165 °C

PART	DESCRIPTION
1 Body	Zinc Alloy, nickel plated
2 Dielectric	Polypropylene UL 94HB
3 Contact	Phosphor bronze, gold plated (intraface)
4 Spring Contact	Copper clad steel, tin/lead plated (CDS)
5 Insulation Bush	Beeswax, tin plated
6 Ceramic Chip Capacitor	4700 pF ± 10%
7 Spring Washer	Phosphor bronze, tin plated

DESCRIPTION OF REVISION	APPVD	ISS	DATE
CAD Issue	SN	6	04 Feb 02
New format	DW	5	22 Feb 99
Mounting hole size changed	IS	4	27 Aug 92
Capacitor circuit diagram added	IS	3	19 May 92
Capacitor values added	IS	2	10 April 92
First Issue	IS	1	01 March 92

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 Fax: +44 (0) 1263 899309

SCALE: Not To Scale
 DIMENSIONS: mm
 TOLERANCES: ± 0.2mm unless otherwise stated

DRAWN BY: S Nash
 CHECKED BY: S Nash
 APPROVED BY: M Nielsen
 DATE: 04 Feb 02

TITLE: Low Profile BNC Right Angle PCB Mount Jack Filter

PART NUMBER: VBF211

PAGE: 1 of 1

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