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Surface Mount Multilayer Ceramic Chip Capacitors Array for Commodity Applications



FEATURES

· Mounting cost saving

High density mounting due to mounting space saving



RoHS COMPLIAN

HALOGEN FREE GREEN

Increased throughputDry sheet manufacturing technology

• Base Metal Electrode system (BME)

Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- For use as a bypass for digital and analog signal line noise
- · Computer motherboards and peripherals
- · The common electronic circuits

ELECTRICAL SPECIFICATION				
Size	4 x 0603			
Dielectric	C0G (NP0)	X7R	Y5V	
Capacitance (1)	10 pF to 470 pF	180 pF to 100 nF	10 nF to 100 nF	
Capacitance Tolerance (2)	J (± 5 %), K (± 10 %)	K (± 10 %), M (± 20 %)	M (± 20 %) Z (-20 %/+80 %)	
Rated Voltage (V _{DC})	50 V	16 V, 50 V	50 V	
tan δ/Q ⁽¹⁾	Cap. < 30 pF: Q ≥ 400 + 20 C Cap. ≥ 30 pF: Q ≥ 1000	$U_R = 50 \text{ V: } \le 2.5 \%$ $U_R = 16 \text{ V: } \le 3.5 \%$	≤ 5 %	
Insulation Resistance at U _R	\geq 10 G Ω \geq 10 G Ω or R x C \geq 500 Ω x F, whichever is le		Ω x F, whichever is less	
Operating Temperature	-55 °C to +125 °C		-25 °C to +85 °C	
Capacitance Change	± 30 ppm	± 15 %	+30 %/-80 %	
Termination	Ni/Sn lead (Pb)-free termination			

Notes

NP0: apply 1.0 V_{RMS} ± 0.2 V_{RMS} , 1.0 MHz ± 10 % at the conditions of 25 °C ambient temperature

X7R: apply 1.0 V_{RMS} \pm 0.2 $V_{RMS},$ 1.0 kHz \pm 10 % at the conditions of 25 °C ambient temperature

Y5V: apply 1.0 V_{RMS} ± 0.2 V_{RMS} , 1.0 kHz ± 10 % at the conditions of 20 °C ambient temperature

 $^{^{(1)}}$ Measured at 30 % ~ 70 % related humidity

⁽²⁾ Preconditioning for X7R, Y5V MLCC: Perform a heat treatment at 150 °C ± 10 °C for 1 h, then leave in ambient condition for 24 h ± 2 h before measurement.



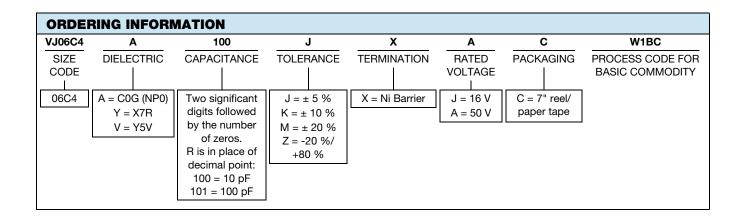
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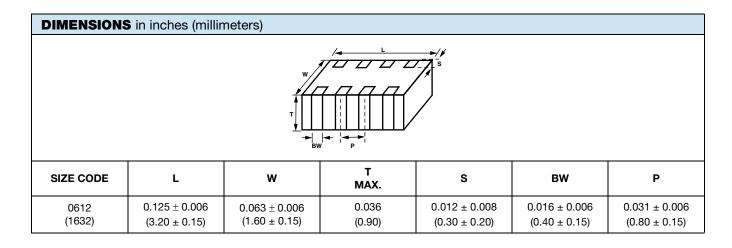
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QUICK REFERENCE DATA				
DIELECTRIC	CASE	MAXIMUM VOLTAGE (V)	CAPACITANCE	
			MINIMUM	MAXIMUM
C0G (NP0)	0612	50	10 pF	470 pF
X7R	0612	50	180 pF	100 nF
Y5V	0612	50	10 nF	100 nF

Note

• Detail ratings see "Selection Chart"







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SELECTION	- JIAIII	000 (1/20)		70	10-10
DIELECTRIC		COG (NP0) X7R			Y5V
STYLE		VJ06C4			
SIZE CODE		0612 (4 x 0603)			
VOLTAGE V _{DC}		50 V	16 V 50 V 50 V		
VOLTAGE COD		Α	J	Α	Α
CAP. CODE	CAP.				
100	10 pF	В			
150	15 pF	В			
220	22 pF	В			
330	33 pF	В			
470	47 pF	В			
680	68 pF	В			
101	100 pF	В			
151	150 pF	В			
181	180 pF	В		В	
221	220 pF	В		В	
271	270 pF	В		В	
331	330 pF	В		В	
391	390 pF	В			
471	470 pF	В		В	
102	1.0 nF			В	
152	1.5 nF			В	
222	2.2 nF			В	
332	3.3 nF			В	
472	4.7 nF			В	
682	6.8 nF			В	
103	10 nF			В	В
153	15 nF		В	В	В
223	22 nF		В	В	В
333	33 nF		В		В
473	47 nF		В		В
683	68 nF		В		
104	100 nF		В		В

Note

• Letters indicate product thickness, see "Packaging Quantities"

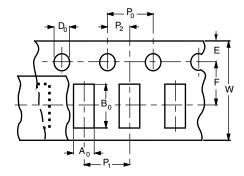


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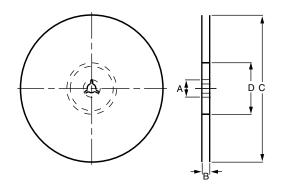
PACKAGING QUANTITIES			
SIZE CODE	THICKNESS (mm)	PAPER TAPE	
SIZE CODE		7" REEL (C)	13" REEL (P)
06C4 (4 x 0603)	0.80 ± 0.10	4K	-

PAPER TAPE SPECIFICATIONS



DIMENSIONS OF PAPER TAPE in millimeters			
SYMBOL	PRODUCT SIZE CODE		
STWIBOL	06C4 (4 x 0603)		
A ₀	2.00 ± 0.10		
B ₀	3.50 ± 0.10		
W	8.00 ± 0.10		
E	1.75 ± 0.05		
F	3.50 ± 0.05		
D ₀	1.50 ± 0.05		
P ₀	4.00 ± 0.10		
P ₁	4.00 ± 0.10		
P ₂	2.00 ± 0.05		

REEL SPECIFICATION

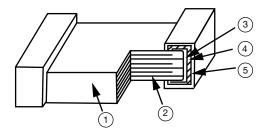


REEL DIMENSIONS AND TAPE WIDTH in millimeters			
SYMBOL	Ø 180 mm; 7"	Ø 330 mm; 13"	
А	13.0 ± 0.5	13.0 ± 0.5	
В	9.0 ± 1.0	9.0 ± 1.0	
С	178.0 ± 1.0	330.0 ± 1.0	
D	60.0 ± 1.0	100.0 ± 1.0	

VJ....W1BC MLCC Chip Array

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CONSTRUCTION				
NO.	NAME		C0G (NP0), X7R, Y5V	
1	Ceramic material		BaTiO ₃ based	
2	Inner electrode		Ni	
3		Inner layer	Cu	
4	Termination	Middle layer	Ni	
5		Outer layer	Sn (matt)	



STORAGE AND HANDLING CONDITIONS

- (1) To store products at 5 $^{\circ}$ C to 40 $^{\circ}$ C ambient temperature and 20 % to 70 % relative humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. Do not store products in a corrosive environment such as sulfide, chloride gas, or acid. It may cause oxidization of electrode, which easily be resulted in poor soldering.
- b. To store products on the shelf and avoid exposure to moisture.
- c. Do not expose products to excessive shock, vibration, direct sunlight and so on.



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