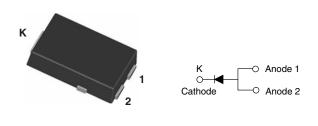


TO-277A (SMPC)

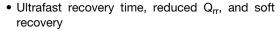
### Vishay Semiconductors

# Ultrafast Rectifier, 6 A FRED Pt®



PRODUCT SUMMARY				
Package	TO-277A (SMPC)			
I <sub>F(AV)</sub>	6 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	0.95 V			
t <sub>rr (typ.)</sub>	42 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			

#### **FEATURES**





RoHS

COMPLIANT

• 175 °C maximum operating junction temperature

• For PFC, CRM snubber operation

Low forward voltage drop

Low leakage current

peak of 260 °C

**HALOGEN** FREE • Meets MSL level 1, per J-STD-020, LF maximum

AEC-Q101 qualified, meets JESD 201 class 2 whisker test

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	$V_{RRM}$		600	V		
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 150 °C	6	۸		
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	Α		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	
Forward voltage	voltage V <sub>F</sub>	I <sub>F</sub> = 6 A	-	1.10	1.30	V
Forward voltage		I <sub>F</sub> = 6 A, T <sub>J</sub> = 150 °C	-	0.95	1.15	
Reverse leakage current I <sub>R</sub>	$V_R = V_R$ rated	-	-	5		
	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	25	150	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	8	-	pF



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			$I_F = 1 A$ , $dI_F/dt = 50 A/\mu s$ , $V_R = 30 V$		42	-	
Poverse recovery time	Reverse recovery time t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	60	
neverse recovery time		T <sub>J</sub> = 25 °C		-	58	-	ns
		T <sub>J</sub> = 125 °C		-	85	-	
Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 6 A$ $dI_F/dt = 500 A/\mu s$ $V_R = 400 V$	-	10	-	А	
	T <sub>J</sub> = 125 °C		-	15	-		
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	290	-	nC
	T <sub>J</sub> = 125 °C		-	620	-	110	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to solder pad	R <sub>thJ-Sp</sub>		-	2.4	3.5	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style TO-277A (SMPC)		NE	:U6	

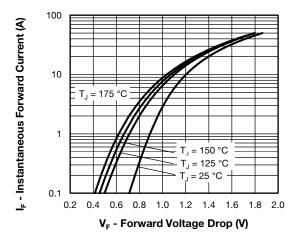


Fig. 1 - Typical Forward Voltage Drop Characteristics

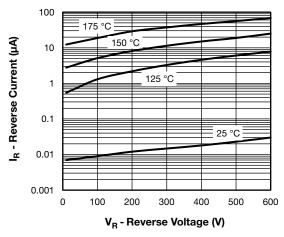


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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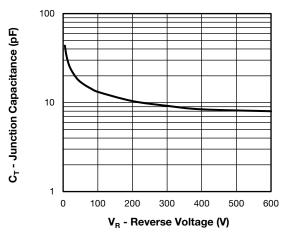


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

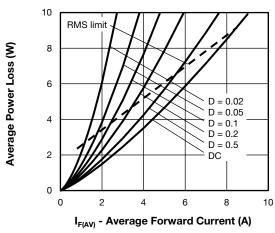


Fig. 5 - Forward Power Loss Characteristics

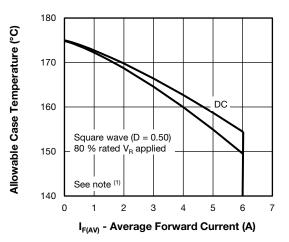


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

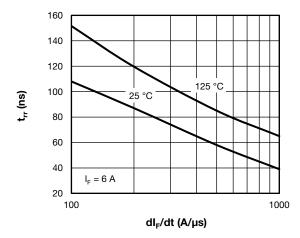


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

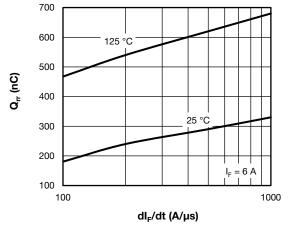


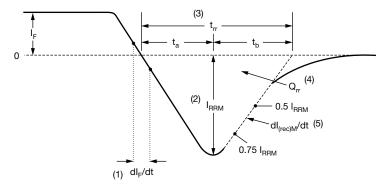
Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = \text{rated} \ V_R \\ \end{array}$ 



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- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

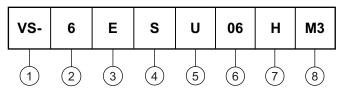
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 8 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

#### **Device code**



- 1 Vishay Semiconductors product
- 2 Current rating (6 = 6 A)
- Circuit configuration:
  - E = single diode
- 4 S = SMPC package
- 5 Process type,
  - U = ultrafast recovery
- 6 Voltage code (06 = 600 V)
- 7 H = AEC-Q101 qualified
- 8 M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-6ESU06HM3/86A	1500	1500	7" diameter plastic tape and reel	
VS-6ESU06HM3/87A	6500	6500	13" diameter plastic tape and reel	

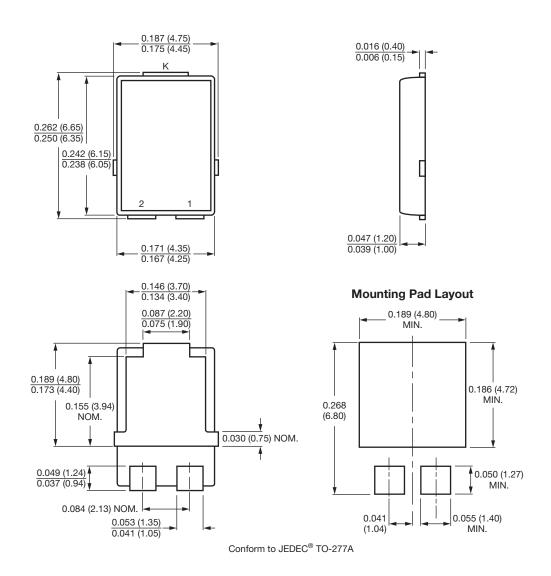
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95570</u>				
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			



Vishay Semiconductors

# **TO-277A (SMPC)**

### **DIMENSIONS** in inches (millimeters)





## **Legal Disclaimer Notice**

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