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Vishay Semiconductors

Insulated Gate Bipolar Transistor (Ultrafast IGBT), 90 A



9
SOT-227

PRODUCT SUMMARY					
V _{CES}	1200 V				
V _{CE(on)} typical at 75 A, 25 °C	3.3 V				
I _C DC	90 A at 90 °C				
Package	SOT-227				
Circuit	Single Switch no Diode				

FEATURES

- NPT Generation V IGBT technology
- Square RBSOA
- Positive V_{CE(on)} temperature coefficient
- · Fully isolated package
- Speed 8 kHz to 60 kHz
- Very low internal inductance (≤ 5 nH typical)
- · Industry standard outline
- UL approved file E78996
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- · Easy to assemble and parallel
- · Direct mounting on heatsink
- Plug-in compatible with other SOT-227 packages
- Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Continuous collector current I _C ⁽¹⁾		T _C = 25 °C	149		
Continuous collector current	IC (")	T _C = 90 °C	90	^	
Pulsed collector current	I _{CM}		200	А	
Clamped inductive load current	I _{LM}		200		
Gate to emitter voltage	V _{GE}		± 20	V	
Davis diasiastica IODT		T _C = 25 °C	862	W	
Power dissipation, IGBT	P _D	T _C = 90 °C	414		
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	

⁽¹⁾ Maximum collector current admitted is 100 A, to do exceed the maximum temperature of terminals



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 250 μA	1200	-	-	
		V _{GE} = 15 V, I _C = 75 A	-	3.3	3.8	
Collector to emitter voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 75 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	3.6	3.9	V
		$V_{GE} = 15 \text{ V}, I_{C} = 75 \text{ A}, T_{J} = 150 ^{\circ}\text{C}$	-	3.7	-	
Gate threshold voltage		$V_{CE} = V_{GE}$, $I_C = 250 \mu A$	4	5	6	
Gate threshold voltage V _{GE(th)}		$V_{CE} = V_{GE}$, $I_C = 250 \mu A$, $T_J = 125 ^{\circ}C$	-	3.2	-	
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	$V_{CE} = V_{GE}$, $I_{C} = 1$ mA (25 °C to 125 °C)	-	-12	-	mV/°C
		V _{GE} = 0 V, V _{CE} = 1200 V	-	7	250	μA
Collector to emitter leakage current	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	1.4	10	mA
		$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	6.5	20	IIIA
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 250	nA

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	690	-	
Gate to emitter charge (turn-on)	Q _{ge}	$I_C = 50 \text{ A}, V_{CC} = 600 \text{ V},$	V _{GE} = 15 V	-	65	-	nC
Gate to collector charge (turn-on)	Q _{gc}			-	250	-	
Turn-on switching loss	E _{on}			-	1.2	-	
Turn-off switching loss	E _{off}			-	2.1	-	mJ
Total switching loss	E _{tot}	$I_C = 75 \text{ A}, V_{CC} = 600 \text{ V},$		-	3.3	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}, R_g = 5 \Omega,$	-	250	-		
Rise time	t _r	L = 500 µH, T _J = 25 °C	Energy losses include tail and diode	-	38	-	ns
Turn-off delay time	t _{d(off)}			-	280	-	
Fall time	t _f			-	90	-	
Turn-on switching loss	E _{on}		recovery Diode used HFA16PB120	-	1.7	-	mJ
Turn-off switching loss	E _{off}			-	4.08	-	
Total switching loss	E _{tot}	$I_C = 75 \text{ A}, V_{CC} = 600 \text{ V},$		-	5.78	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}, R_g = 5 \Omega,$		-	245	-	
Rise time	t _r	$L = 500 \mu H, T_J = 125 °C$		-	48	-	
Turn-off delay time	t _{d(off)}			-	280	-	ns
Fall time	t _f			-	140	-	
Reverse bias safe operating area	RBSOA	$T_{J} = 150 ^{\circ}\text{C}, I_{C} = 200 \text{A}, R_{g} = 22 \Omega, \\ V_{GE} = 15 \text{V to 0 V}, V_{CC} = 900 \text{V}, \\ V_{P} = 1200 \text{V}, L = 500 \mu\text{H}$ Fullsquare					

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL		MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-40	-	150	°C
Thermal resistance junction to case	R _{thJC}		-	-	0.145	°C/W
Thermal resistance case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	C/VV
Weight			=-	30	-	g
Mounting torque			-	-	1.3	Nm
Case style		SOT-227				



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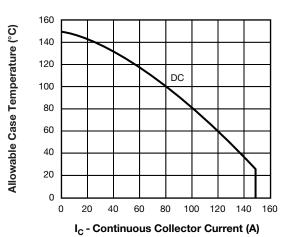


Fig. 1 - Maximum DC IGBT Collector Current vs.

Case Temperature

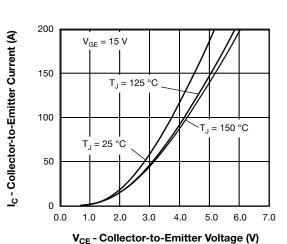


Fig. 2 - Typical Collector to Emitter Current Output Characteristics of IGBT

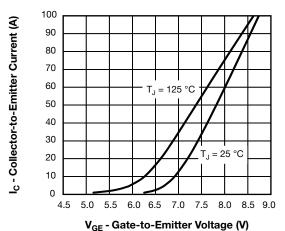


Fig. 3 - Typical IGBT Transfer Characteristics

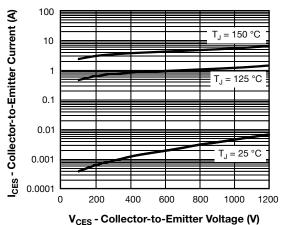


Fig. 4 - Typical IGBT Zero Gate Voltage Collector Current

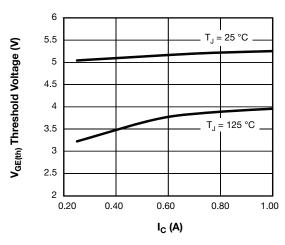


Fig. 5 - Typical IGBT Threshold Voltage

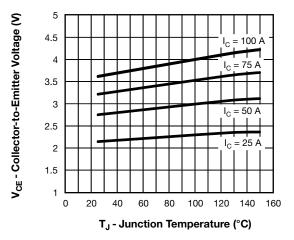


Fig. 6 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15 \text{ V}$

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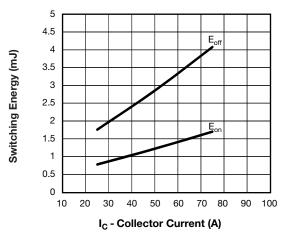


Fig. 7 - Typical IGBT Energy Losses vs. I $_{C}$ T $_{J}$ = 125 °C, L = 500 μ H, V $_{CC}$ = 600 V, R $_{q}$ = 5 Ω , V $_{GE}$ = 15 V, Diode used HFA16PB120

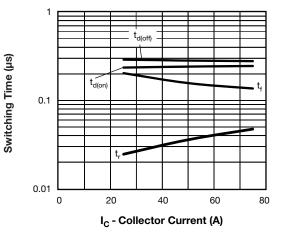


Fig. 8 - Typical IGBT Switching Time vs. I_C T_J = 125 °C, L = 500 $\mu\text{H},~V_{CC}$ = 600 V, R_g = 5 $\Omega,~V_{GE}$ = 15 V, Diode used HFA16PB120

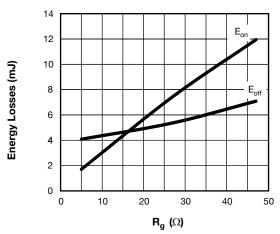


Fig. 9 - Typical IGBT Energy Loss vs. Rg, TJ = 125 °C, IC = 75 A, L = 500 μ H, VCC = 600 V, VGE = 15 V, Diode used HFA16PB120

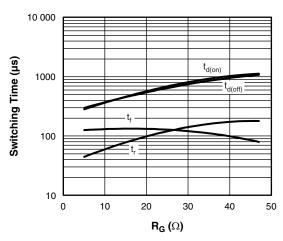


Fig. 10 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, R_g = 5 Ω , V_{GE} = 15 V

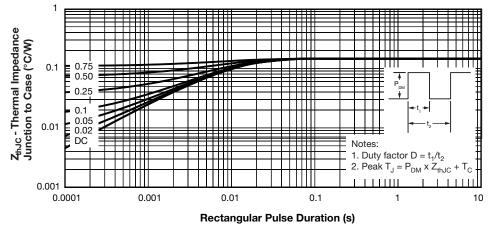


Fig. 11 - Maximum Thermal Impedance Z_{thJC} Characteristics (IGBT)

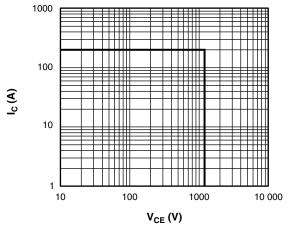
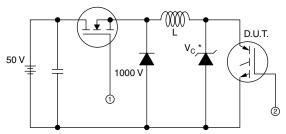


Fig. 12 - IGBT Reverse Bias SOA, TJ = 150 °C, V_{GE} = 15 V



- * Driver same type as D.U.T.; V $_{\rm C}$ = 80 % of V $_{\rm ce(max.)}$ * Note: Due to the 50 V power supply, pulse width and inductor
- will increase to obtain Id

Fig. 13a - Clamped Inductive Load Test Circuit

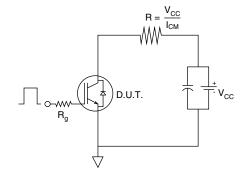


Fig. 13b - Pulsed Collector Current Test Circuit

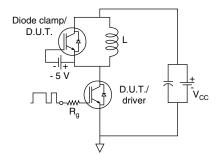


Fig. 14a - Switching Loss Test Circuit

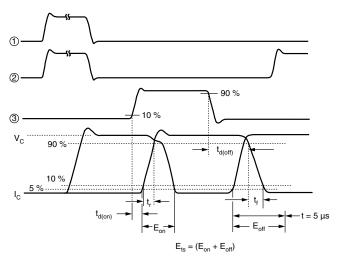
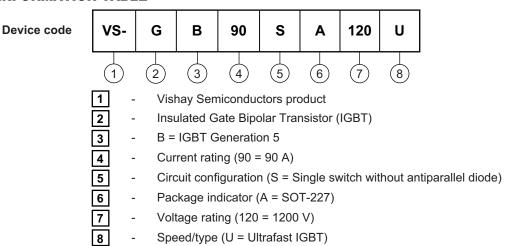


Fig. 14b - Switching Loss Waveforms Test Circuit

ORDERING INFORMATION TABLE



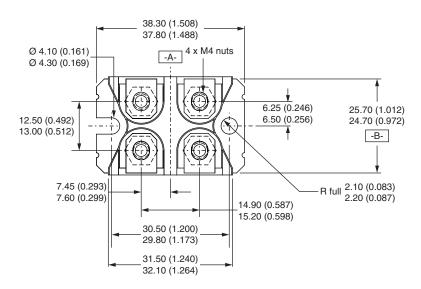
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CI	RCUIT DRAWING		
Single switch, no antiparallel diode	S	2 (G) O 1, 4 (E)	Lead Assignment 4 1 2		

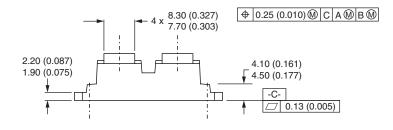
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

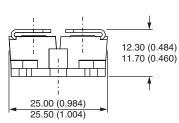


SOT-227 Generation II

DIMENSIONS in millimeters (inches)







Note

Controlling dimension: millimeter



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