



# CONNOR—WINFIELD CORPORATION

AURORA, IL. 60505  
PHONE (630) 851-4722  
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## 4 PIN J-LEAD SURFACE MOUNT VCXO

SPECIFICATIONS	VSM61**4	VSM62**4	VSM64**4
Center Frequency Range (Fo)	2MHz to 100MHz		
Frequency Stability (Vcontrol=2.50Vdc)	±25ppm	±50ppm	±20ppm
Aging (10 yrs)	±10ppm		
Temperature Range	-40°C to +85°C		
Waveform	CMOS Squarewave , TTL Compatible		
Load	25pF Maximum		
Voltage Voh	4.5V Minimum		
Vol	0.5V Maximum		
Current Ioh	-16mA		
Iol	16mA		
Duty Cycle	40/60 Maximum Measured @2.5Vdc		
Rise/Fall Time	5nS Maximum		
Jitter (BW=10Hz to 20MHz)	5pS RMS Maximum		
Jitter (BW=12kHz to 20MHz)	1pS RMS Maximum		
SSB Phase Noise (Typical)	-95dBc/Hz @ 100Hz , -135dBc/Hz @ 10KHz		
Frequency Control			
Control Voltage Range	0.5 to 4.5Vdc		
Pullability Options (measured @ 25°C, referenced to Fo) (**-- Add to model number)	<b>15</b>	±75ppm Minimum	
	<b>22</b>	±100ppm Minimum      Only available to 80MHz	
Control Voltage for Fo	2.5Vdc ±0.5Vdc		
Slope	Positive		
Monotonic Linearity	< ±10%		
Input Impedance	50K Ohm		
Modulation Bandwidth	3dB cutoff frequency at 10KHz Typical		
Supply Voltage (Vcc)	+5Vdc ±5%		
Supply Current	60mA Maximum		
Package	Hermetically sealed, ceramic package with gold "J" leads		

### EXAMPLE MODEL NUMBER:

V S M 6 1 1 5 4 / 7 7 . 7 6 M H z

SURFACE MOUNT  
VCXO SERIES

CENTER FREQUENCY

MINIMUM DEVIATION OF  
±75ppm OVER  
CONTROL VOLTAGE RANGE

Specifications subject to change without notice

BULLETIN #: VX245  
PAGE 1 OF 2

REV: 03  
DATE: 9/11/03

ISSUED BY: \_\_\_\_\_

Dimensional Tolerance: ±.02"  
±.005"

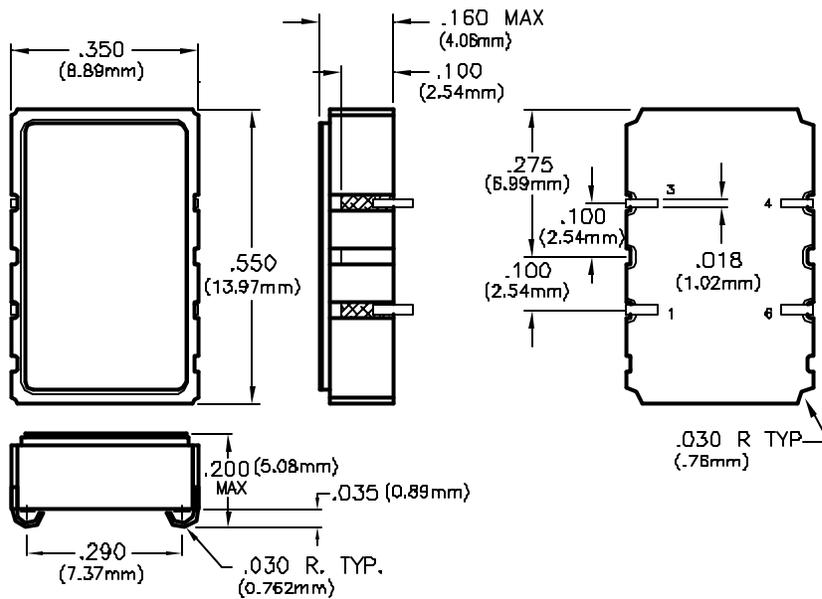


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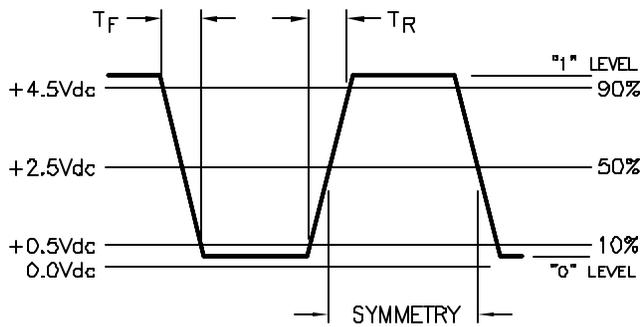
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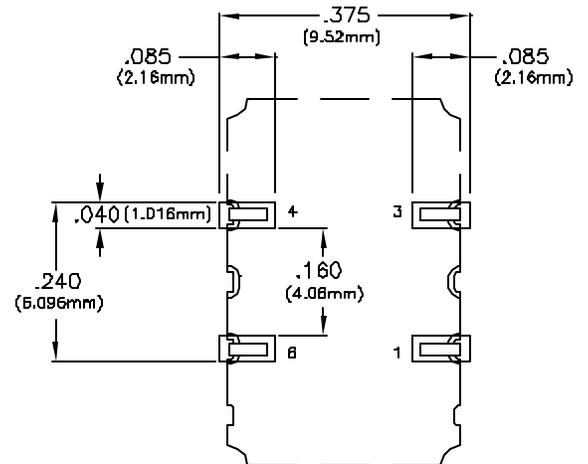


PAD	CONNECTION
1	CONTROL VOLTAGE
3	GROUND
4	OUTPUT
6	Vcc

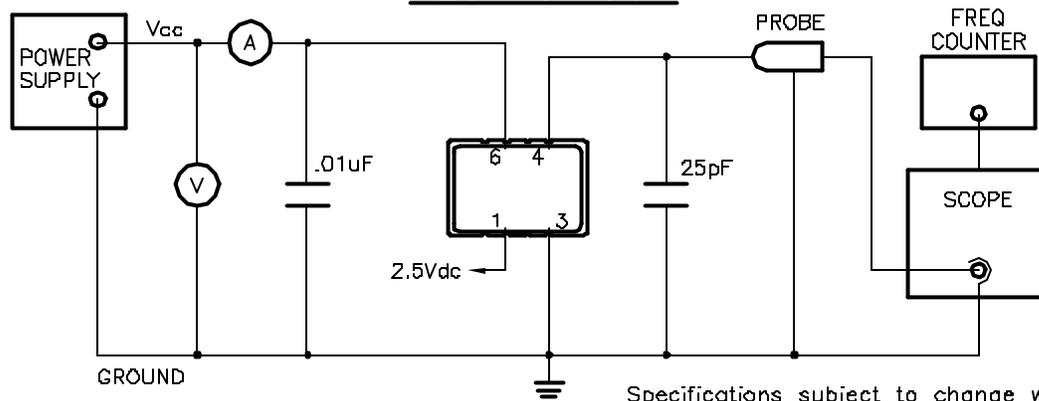
## OUTPUT WAVEFORM



## SUGGESTED PAD LAYOUT



## TEST CIRCUIT



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