

# APPROVAL SHEET

# WA04P

 $\pi$  type chip attenuator

 $50\Omega$ , 1dB to 10dB Size 0402x2

\*Contents in this sheet are subject to change without prior notice.



#### **FEATURE**

- 1. Unbalanced  $\pi$  type attenuator circuit in one chip (1.0mm x 1.0mm)
- 2. Mounting occupation area reduction
- 3. Mounting assembly cost saving

#### **APPLICATION**

- Attenuation, level control, impedance matching of high frequency signals of communication equipment;
- Mobile phone (GSM, CDMA, PDC, etc,...)
- Telecom

#### **DESCRIPTION**

The attenuator is constructed in a high grade ceramic body (aluminum oxide). Internal circuit is applied to the top surface of the substrate, and its design determines the required attenuation value. The attenuation layer is covered with a protective coating and a rectangular marker indicates input pin1 as shown in circuit configuration.

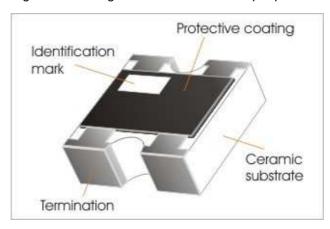
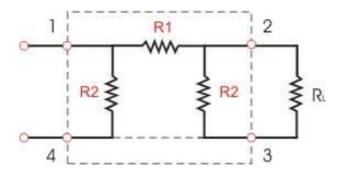


Fig 1. Outline of WA04P Chip attenuator

#### **CIRCUIT CONFIGURATION**



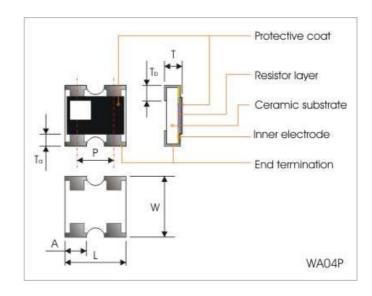


#### **QUICK REFERENCE DATA**

Item	General Specification			
Series No.	WA04P(Convex type)			
Size	0402x2 (1005x2)			
Attenuation Range	1dB, 2dB, 3dB, 4dB, 5dB, 6dB, 7dB, 8dB, 9dB, 10dB			
Attenuation Tolerance				
1dB ~ 5dB	±0.3dB			
6dB ~ 10dB	±0.5dB			
Characteristic impedance	50Ω			
Rated power at T <sub>amb</sub> =70°C	0.04 W / package			
Limiting voltage (DC)	50V			
Frequency range (DC)	Max. 2.2 GHz			
VSWR (Voltage Standing Wave Ratio)	Max. 1.3			
Number of Resistors	3 resistors			
Number of Terminals	4 terminals			
Climatic category (IEC60068)	55/155/56			

## **DIMENSIONS(mm)**

	WA04P
L	1.00 ± 0.10
W	1.00 +0.10 / -0
Т	0.35 ± 0.10
Р	0.65 ± 0.10
Α	$0.33 \pm 0.10$
Та	0.15 ± 0.10
Tb	0.25 ± 0.10



#### **MARKING**

No marking for WA04P chip attenuator

#### **FUNCTIONAL DESCRIPTION**

Product characterization

Standard attenuation values include 1dB to 5dB with a tolerance of  $\pm 0.3$ dB, 6dB to 10dB with a tolerance of  $\pm 0.5$ dB.



#### CATALOGUE NUMBERS AND PACKAGING

The attenuators have a catalogue number starting with .

WA04	P	001	Х	В	Т	L
Size code	Type code	Attenuation code	Impedance	Tolerance	Packaging code	Termination code
•		001 = 1dB	Χ : 50Ω	A : ±0.2dB	T:7" reel taped	L = Sn base (lead
element		002 = 2dB		B : ±0.3dB		free)
		003 = 3dB		C: ±0.5dB		
		004 = 4dB		D:±1.0dB		
		005 = 5dB				
		006 = 6dB				
		007 = 7dB				
		008 = 8dB				
		009 = 9dB				
		010 = 10dB				

Packaging: 8mm width paper taping 10,000pcs per reel.

#### **MOUNTING**

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

#### **SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

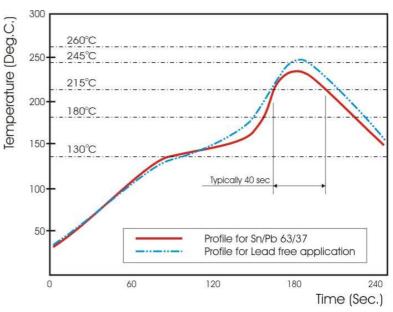


Fig 3. Infrared soldering profile



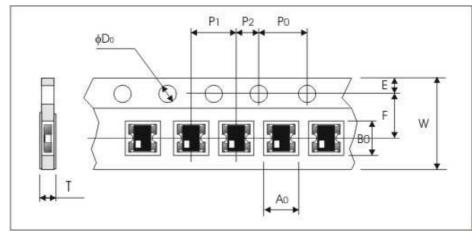
# **TEST AND REQUIREMENTS (JIS C 5201-1: 1998)**

TEST	PROCEDURE	REQUIREMENT
Characteristic Impedance	Measuring circuit  R2  R2  R2  R2  R2  R2  R2  R2  R2  R	50Ω
Insulation resistance	Apply the 50VDC for 1minute	At least 100MΩ
Clause 4.6		
Solderability Clause 4.17	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235 $^{\circ}\!$	good tinning (>95% covered) no visible damage
Resistance to soldering heat(R.S.H) Clause 4.18	Un-mounted chips completely immersed for 10±1 second in a SAC solder bath at $260^{\circ}\text{C} \pm 5^{\circ}\text{C}$	no visible damage Attenuation 1~ 2dB : within ±0.1dB Attenuation 3~ 5dB : within ±0.2dB Attenuation 6~ 10dB : within ±0.3dB
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	Ditto
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	Ditto
Dielectric Withstand Voltage Clause 4.7	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover



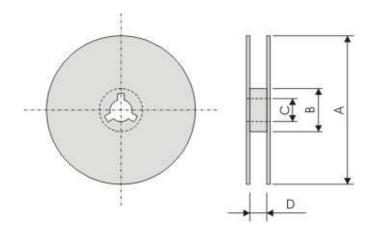
#### **PACKAGING**

## Paper Tape specifications (unit :mm)



	A0	B0	W	F	E
(mm)	1.20±0.05	1.20±0.05	8.00±0.20	3.50±0.05	1.75±0.10
	P1	P2	P0	ØD0	Т
(mm)	2.00±0.10	2.00±0.05	4.00±0.10	$1.50^{+0.10}_{-0}$	0.45±0.10

#### **Reel dimensions**



Symbol	А	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5