

## General Description

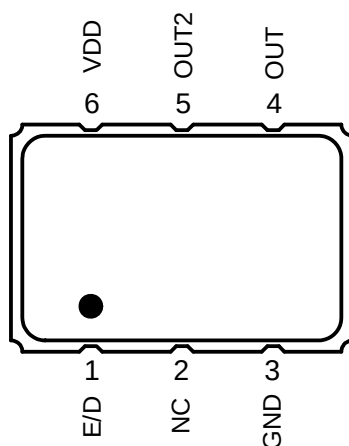
The XLL is an LVDS Crystal Oscillator with 0.89ps typical phase jitter over 12kHz to 20 MHz bandwidth. Available in a wide frequency range from 0.750MHz to 1350MHz, the IDT XLL Series Crystal Oscillator utilizes a family of proprietary ASICs, with a key focus on noise reduction technologies.

The 3rd order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. With short lead-time, low cost, low noise, wide frequency range, excellent ambient performance, the XLL is an excellent choice over the conventional technologies. The XLL has stabilities as tight as  $\pm 20\text{ppm}$  with extremely quick delivery for both standard and custom frequencies

## Features

- Frequency range: 0.750MHz to 1350MHz
- Output Type: LVDS
- Frequency Stability:  $\pm 20\text{ppm}$ ,  $\pm 25\text{ppm}$ ,  $\pm 50\text{ppm}$ , or  $\pm 100\text{ppm}$
- Supply Voltage: 2.5V or 3.3V
- Phase Jitter (1.875MHz to 20MHz): 225fs typical
- Phase Jitter (12kHz to 20MHz): 0.89ps typical
- Package options: 3.2mm x 2.5mm x 1.0mm (JX6)  
5.0mm x 3.2mm x 1.2mm (JS6)  
7.0mm x 5.0mm x 1.3mm (JU6)
- Operating Temperatures:  $-20^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$  or  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$

## Pin Assignment



6-pin CLCC

## Pin Descriptions

| Pin Number | Pin Name | Description                                     |
|------------|----------|-------------------------------------------------|
| 1          | E/D      | Enable/Disable <sup>1</sup> (0=Output Disabled) |
| 2          | NC       | No connect                                      |
| 3          | GND      | Connect to ground                               |
| 4          | OUT      | Output                                          |
| 5          | OUT2     | Complementary Output                            |
| 6          | VDD      | Supply voltage                                  |

1. Pulled high internally.

## Absolute Maximum Ratings

Stresses above the ratings listed below can cause permanent damage to the XLL. These ratings, which are standard values for IDT commercially rated parts, are stress ratings only. Functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods can affect product reliability. Electrical parameters are guaranteed only over the recommended operating temperature range.

| Item                           | Rating                |
|--------------------------------|-----------------------|
| VDD                            | -0.5 to +5.0 V        |
| E/D                            | -0.5 V to VDD + 0.5 V |
| OUT                            | -0.5 V to VDD + 0.5 V |
| Storage Temperature            | -55°C to 125°C        |
| Theta Ja (Junction to Ambient) | 102°C/W – Still Air   |

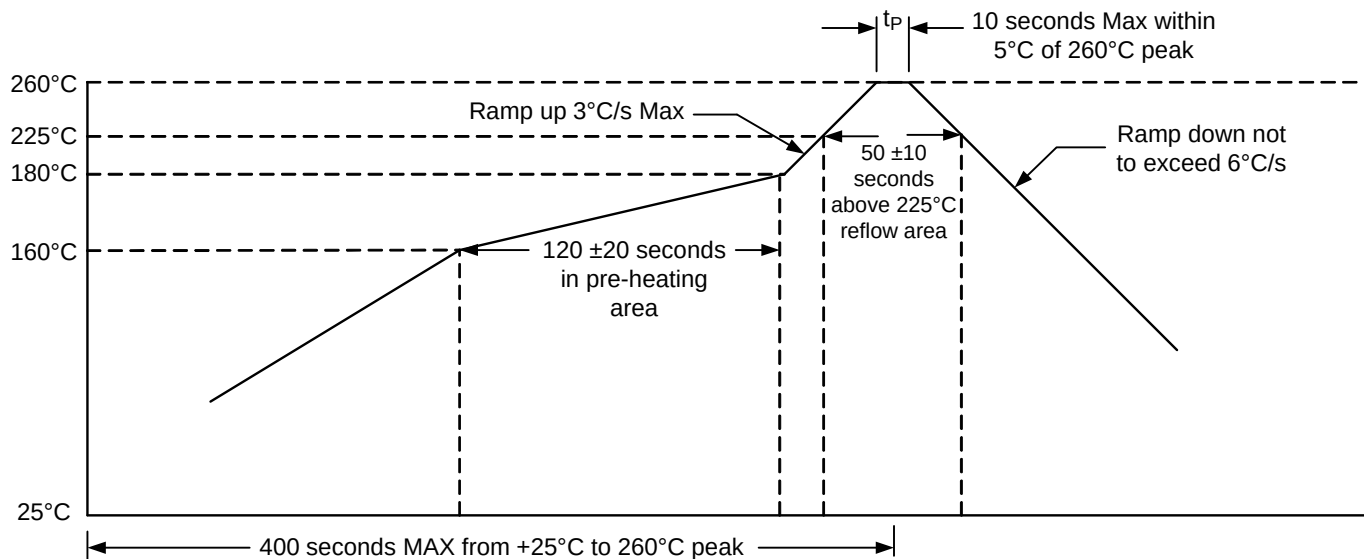
## ESD Compliance

|                        |       |
|------------------------|-------|
| Human Body Model (HBM) | 1000V |
| Machine Model (MM)     | 150V  |

## Mechanical Testing

| Parameter                | Test Method                                                                        |
|--------------------------|------------------------------------------------------------------------------------|
| Mechanical Shock         | Drop from 75cm to hardwood surface–3 times                                         |
| Mechanical Vibration     | 10~55Hz, 1.5mm amplitude, 1 minute sweep<br>2 hours each in 3 directions (X, Y, Z) |
| High Temperature Burn-in | Under power @ 125°C for 2000 hours                                                 |
| Hermetic Seal            | He pressure: 4 ±1kgf/cm <sup>2</sup> 2 hour soak                                   |

## Solder Reflow Profile



## DC Characteristics

( $V_{DD} = 3.3\text{ V} \pm 5\%$ ,  $T_A = -20^\circ\text{C}$  to  $+70^\circ\text{C}$ ;  $-40^\circ$  to  $+85^\circ\text{C}$ )

| Parameter                                           | Symbol   | Condition          | Min          | Typ | Max          | Units |
|-----------------------------------------------------|----------|--------------------|--------------|-----|--------------|-------|
| Power Supply Current                                | $I_{DD}$ | Common Frequencies |              |     | 100          | mA    |
| Differential Output Voltage                         | $V_{OD}$ | Standard LVDS load |              | 0.6 |              | V     |
| Output Offset Voltage                               | $V_{OS}$ | Standard LVDS load |              | 1.3 |              | V     |
| Enable/Disable Input HIGH Voltage (Output enabled)* | $V_{IH}$ |                    | $70\%V_{DD}$ |     |              | V     |
| Enable/Disable Input LOW Voltage (Output disabled)  | $V_{IL}$ |                    |              |     | $30\%V_{DD}$ | V     |

\* A pullup resistor from pin 6 (VDD) to pin 1 (E/D) enables output when pin 1 is left open.

## AC Characteristics

( $V_{DD} = 3.3\text{ V} \pm 5\%$ ,  $T_A = -20^\circ\text{C}$  to  $+70^\circ\text{C}$ ;  $-40^\circ$  to  $+85^\circ\text{C}$ )

| Parameter                                        | Symbol          | Condition                                                                                                                                                 | Min      | Typ  | Max       | Units  |
|--------------------------------------------------|-----------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------|----------|------|-----------|--------|
| Output Frequency Range                           | $F_{OUTR}$      |                                                                                                                                                           | 0.750    |      | 1350      | MHz    |
| Frequency Stability                              |                 | Temperature = $-20^\circ\text{C}$ to $+70^\circ\text{C}$                                                                                                  | $\pm 20$ |      | $\pm 100$ | ppm    |
|                                                  |                 | Temperature = $-40^\circ\text{C}$ to $+85^\circ\text{C}$                                                                                                  | $\pm 25$ |      | $\pm 100$ | ppm    |
| Aging (1 <sup>st</sup> year)                     |                 | $T_a = 25^\circ\text{C}$                                                                                                                                  |          |      | 3         |        |
| Aging (10 years)                                 |                 | $T_a = 25^\circ\text{C}$                                                                                                                                  |          |      | 10        |        |
| Output Load                                      |                 | Differential                                                                                                                                              |          | 100  |           | Ohms   |
| Start-up Time                                    | $T_{ST}$        | Output valid time after VDD meets minimum specified level                                                                                                 |          |      | 10        | ms     |
| Output Rise Time                                 |                 | 20% to 80% $V_{PP}$                                                                                                                                       |          |      | 400       | ps     |
| Output Fall Time                                 |                 | 80% to 20% $V_{PP}$                                                                                                                                       |          |      | 400       | ps     |
| Output Clock Duty Cycle                          | $T_{DTCY}$      | 50% $V_{P-P}$                                                                                                                                             | 45       |      | 55        | %      |
| Output Enable/ Disable Time                      | $T_{OE}$        |                                                                                                                                                           |          |      | 100       | ns     |
| Period Jitter, RMS                               | $J_{PER}$       | Frequency = 156.25MHz                                                                                                                                     |          | 3.0  |           | ps     |
| Random Jitter                                    | $R_J$           | Frequency = 156.25MHz<br>Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications)                                                       |          | 1.3  |           | ps     |
| Deterministic Jitter                             | $D_J$           |                                                                                                                                                           |          | 5.8  |           | ps     |
| Total Jitter                                     | $T_J$           |                                                                                                                                                           |          | 23.6 |           | ps     |
| Phase Jitter (12kHz – 20MHz)                     | $\phi_{JITTER}$ | Common Frequencies                                                                                                                                        |          | 0.89 |           | ps     |
| Phase Noise Performance<br>Frequency = 156.25MHz | $\phi_{NOISE}$  | 100Hz of Carrier                                                                                                                                          |          | -80  |           | dBc/Hz |
|                                                  |                 | 1kHz of Carrier                                                                                                                                           |          | -115 |           | dBc/Hz |
|                                                  |                 | 10kHz of Carrier                                                                                                                                          |          | -118 |           | dBc/Hz |
|                                                  |                 | 100kHz of Carrier                                                                                                                                         |          | -124 |           | dBc/Hz |
|                                                  |                 | 1MHz of Carrier                                                                                                                                           |          | -142 |           | dBc/Hz |
|                                                  |                 | 10MHz of Carrier                                                                                                                                          |          | -151 |           | dBc/Hz |
| Output Frequency (Common)                        | $F_{OUT}$       | 100MHz, 106.25MHz, 125.8MHz, 150MHz, 155.52MHz, 156.25MHz, 200MHz, 212.5MHz, 250MHz, 300MHz, 312.5MHz, 400MHz<br>(Contact IDT for additional frequencies) |          |      |           |        |

Note: Inclusive of initial frequency accuracy, operating temperature range, supply variation, load variation, 3 times solder reflow, shock, vibration and 1 year aging at  $25^\circ\text{C}$ . We do not recommend hand soldering the devices

## DC Characteristics

( $V_{DD} = 2.5\text{ V} \pm 5\%$ ,  $T_A = -20^\circ\text{C}$  to  $+70^\circ\text{C}$ ;  $-40^\circ$  to  $+85^\circ\text{C}$ )

| Parameter                                           | Symbol   | Condition          | Min          | Typ  | Max          | Units |
|-----------------------------------------------------|----------|--------------------|--------------|------|--------------|-------|
| Power Supply Current                                | $I_{DD}$ | Common Frequencies | 26           |      | 65           | mA    |
| Differential Output Voltage                         | $V_{OD}$ | Standard LVDS load |              | 0.4  |              | V     |
| Output Offset Voltage                               | $V_{OS}$ | Standard LVDS load |              | 1.25 |              | V     |
| Enable/Disable Input HIGH Voltage (Output enabled)* | $V_{IH}$ |                    | $70\%V_{DD}$ |      |              | V     |
| Enable/Disable Input LOW Voltage (Output disabled)  | $V_{IL}$ |                    |              |      | $30\%V_{DD}$ | V     |

\* A pullup resistor from pin 6 (VDD) to pin 1 (E/D) enables output when pin 1 is left open.

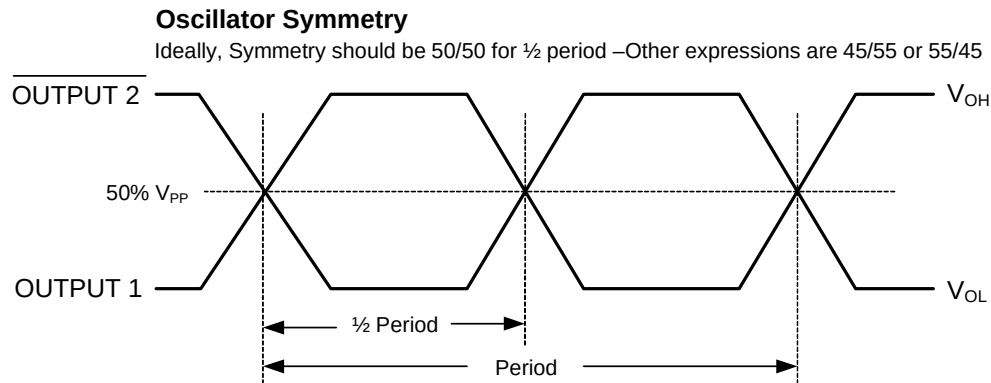
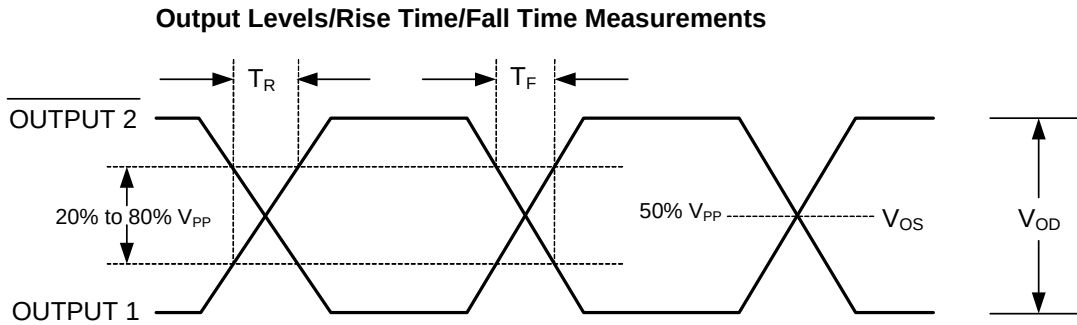
## AC Characteristics

( $V_{DD} = 2.5\text{ V} \pm 5\%$ ,  $T_A = -20^\circ\text{C}$  to  $+70^\circ\text{C}$ ;  $-40^\circ$  to  $+85^\circ\text{C}$ )

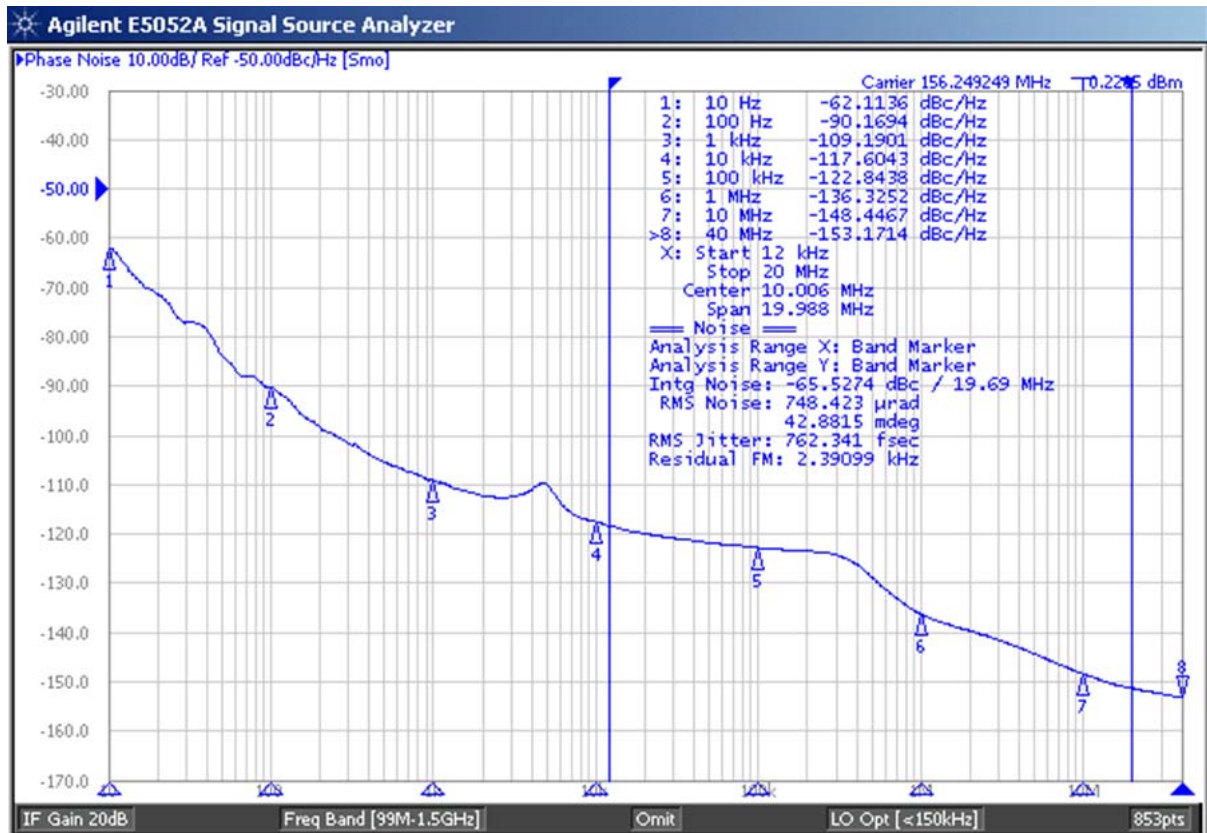
| Parameter                                        | Symbol          | Condition                                                                                                                                                 | Min      | Typ  | Max       | Units  |
|--------------------------------------------------|-----------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------|----------|------|-----------|--------|
| Output Frequency Range                           | $F_{OUTR}$      |                                                                                                                                                           | 0.750    |      | 1000      | MHz    |
| Frequency Stability                              |                 | Temperature = $-20^\circ\text{C}$ to $+70^\circ\text{C}$                                                                                                  | $\pm 20$ |      | $\pm 100$ | ppm    |
|                                                  |                 | Temperature = $-40^\circ\text{C}$ to $+85^\circ\text{C}$                                                                                                  | $\pm 25$ |      | $\pm 100$ | ppm    |
| Output Load                                      |                 | Differential                                                                                                                                              |          | 100  |           | Ohms   |
| Start-up Time                                    | $T_{ST}$        | Output valid time after VDD meets minimum specified level                                                                                                 |          |      | 10        | ms     |
| Output Rise Time                                 |                 | 20% to 80% $V_{PP}$                                                                                                                                       |          |      | 400       | ps     |
| Output Fall Time                                 |                 | 80% to 20% $V_{PP}$                                                                                                                                       |          |      | 400       | ps     |
| Output Clock Duty Cycle                          | $T_{DTCY}$      | 50% $V_{P-P}$                                                                                                                                             | 45       |      | 55        | %      |
| Output Enable/ Disable Time                      | $T_{OE}$        |                                                                                                                                                           |          |      | 100       | ns     |
| Period Jitter, RMS                               | $J_{PER}$       | Frequency = 156.25MHz                                                                                                                                     |          | 4.0  |           | ps     |
| Random Jitter                                    | $R_J$           | Frequency = 156.25MHz<br>Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications)                                                       |          | 1.4  |           | ps     |
| Deterministic Jitter                             | $D_J$           |                                                                                                                                                           |          | 9.2  |           | ps     |
| Total Jitter                                     | $T_J$           |                                                                                                                                                           |          | 29.2 |           | ps     |
| Phase Jitter (12kHz – 20MHz)                     | $\phi_{JITTER}$ | Frequency = 156.25MHz                                                                                                                                     |          | 1.04 |           | ps     |
| Phase Noise Performance<br>Frequency = 156.25MHz | $\phi_{NOISE}$  | 100Hz of Carrier                                                                                                                                          |          | -83  |           | dBc/Hz |
|                                                  |                 | 1kHz of Carrier                                                                                                                                           |          | -105 |           | dBc/Hz |
|                                                  |                 | 10kHz of Carrier                                                                                                                                          |          | -113 |           | dBc/Hz |
|                                                  |                 | 100kHz of Carrier                                                                                                                                         |          | -119 |           | dBc/Hz |
|                                                  |                 | 1MHz of Carrier                                                                                                                                           |          | -137 |           | dBc/Hz |
|                                                  |                 | 10MHz of Carrier                                                                                                                                          |          | -146 |           | dBc/Hz |
| Output Frequency (Standards)                     | $F_{OUT}$       | 100MHz, 106.25MHz, 125.8MHz, 150MHz, 155.52MHz, 156.25MHz, 200MHz, 212.5MHz, 250MHz, 300MHz, 312.5MHz, 400MHz<br>(Contact IDT for additional frequencies) |          |      |           |        |

Note: Inclusive of initial frequency accuracy, operating temperature range, supply variation, load variation, 3 times solder reflow, shock, vibration and 1 year aging at  $25^\circ\text{C}$ . We do not recommend hand soldering the devices

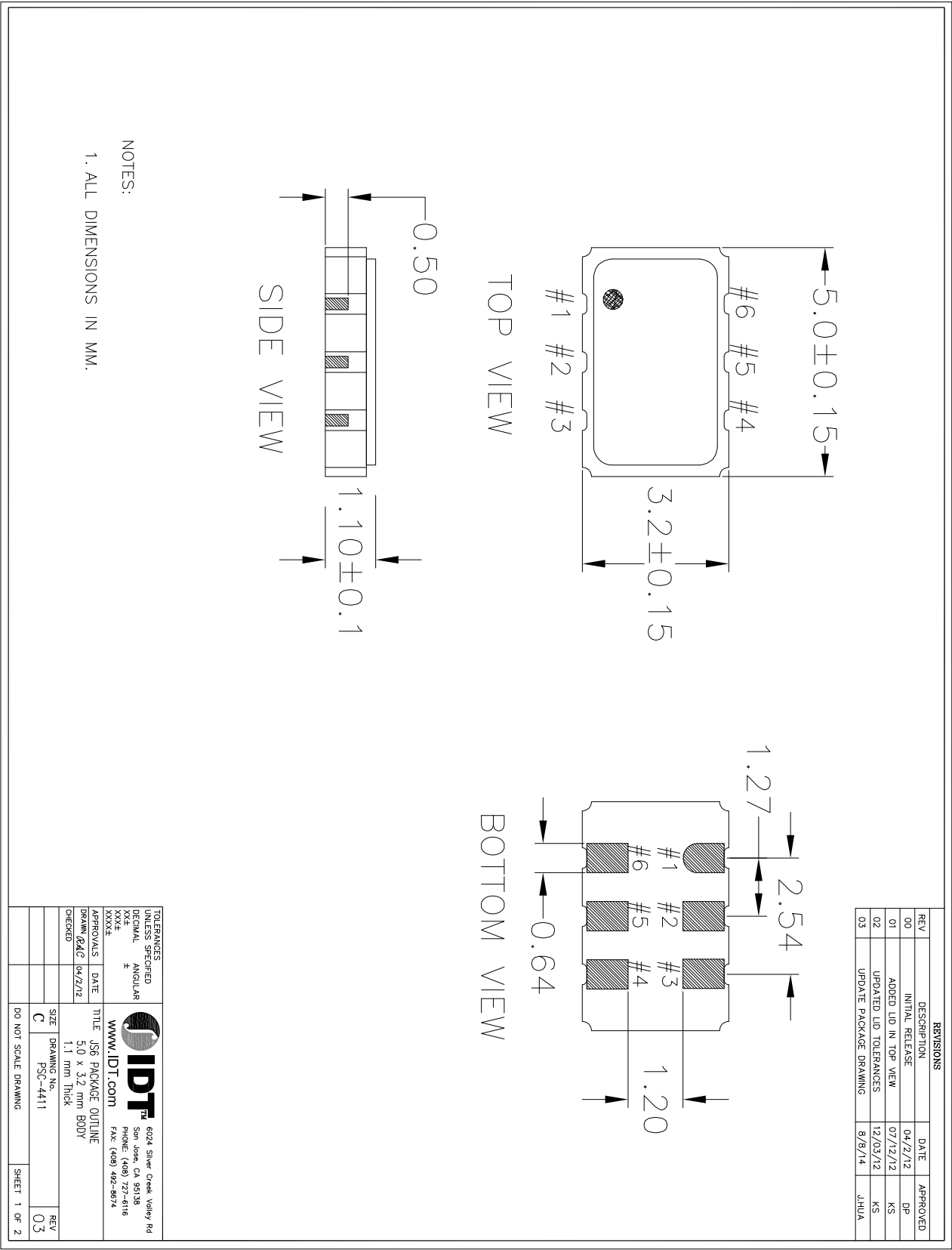
## Output Waveform



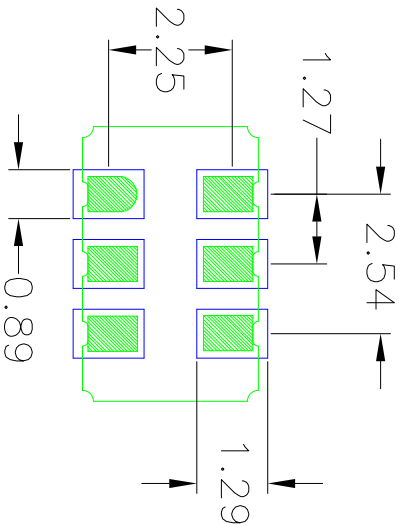
## Typical Phase Noise (3.3V)



JS6 Package Outline and Dimensions



# JS6 Package Outline and Dimensions (cont.)



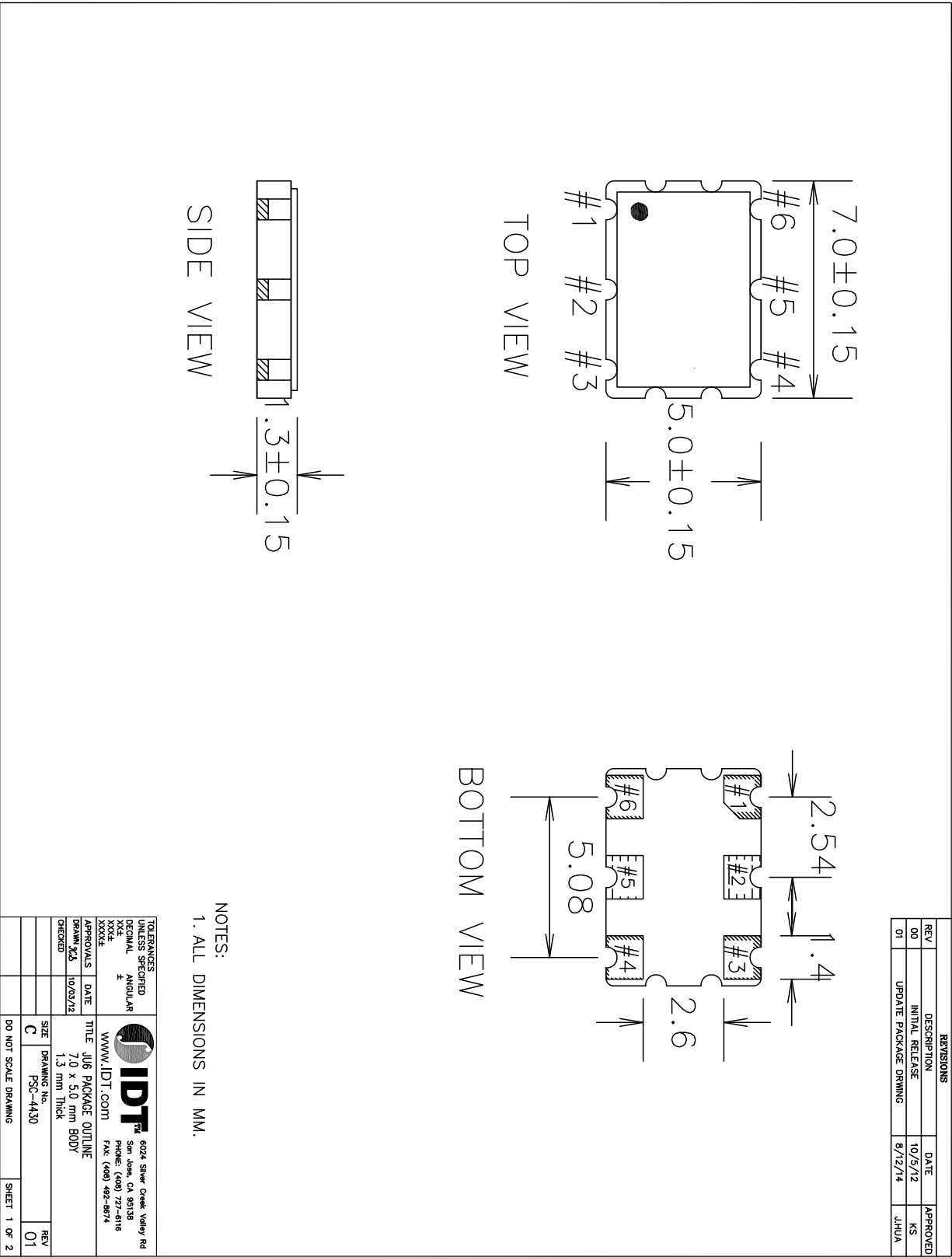
RECOMMENDED LAND PATTERN

- NOTES:
1. ALL DIMENSION ARE IN mm. ANGLES IN DEGREES.
  2. TOP DOWN VIEW, AS VIEWED ON PCB.
  3. COMPONENT OUTLINE SHOW FOR REFERENCE IN GREEN.
  4. LAND PATTERN IN BLUE. NSMD PATTERN ASSUMED.
  5. LAND PATTERN RECOMMENDATION PER IPC-7351B GENERIC REQUIREMENT FOR SURFACE MOUNT DESIGN AND LAND PATTERN.

| REVISIONS |                        |          |          |
|-----------|------------------------|----------|----------|
| REV       | DESCRIPTION            | DATE     | APPROVED |
| 00        | INITIAL RELEASE        | 04/23/12 | DP       |
| 01        | ADDED LID IN TOP VIEW  | 07/12/12 | KS       |
| 02        | UPDATED LID TOLERANCES | 12/03/12 | KS       |
| 03        | UPDATE PACKAGE DRAWING | 8/8/14   | JHUA     |

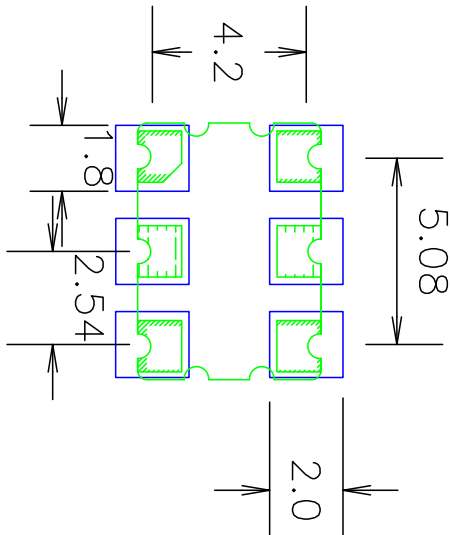
|                             |             |                           |  |                             |  |
|-----------------------------|-------------|---------------------------|--|-----------------------------|--|
| TOLERANCES UNLESS SPECIFIED |             | IDT                       |  | 6024 Silver Creek Valley Rd |  |
| DECIMAL                     | ANGULAR     | www.idt.com               |  | San Jose, CA 95138          |  |
| ±                           |             | PHONE: (408) 727-6116     |  | FAX: (408) 492-8674         |  |
| XX.X                        |             | TITLE JS6 PACKAGE OUTLINE |  | 5.0 X 3.2 mm B00T           |  |
| XX.XX                       |             | DATE 10/23/12             |  | 1.1 mm THICK                |  |
| XX.XXX                      |             | DRAWN/CHK                 |  | GREGOR                      |  |
| SIZE                        | DRAWING NO. | DO NOT SCALE DRAWING      |  | SHEET 2 OF 2                |  |
| C                           | F50-4411    | REV                       |  | 03                          |  |

JU6 Package Outline and Dimensions





# JU6 Package Outline and Dimensions (cont.)



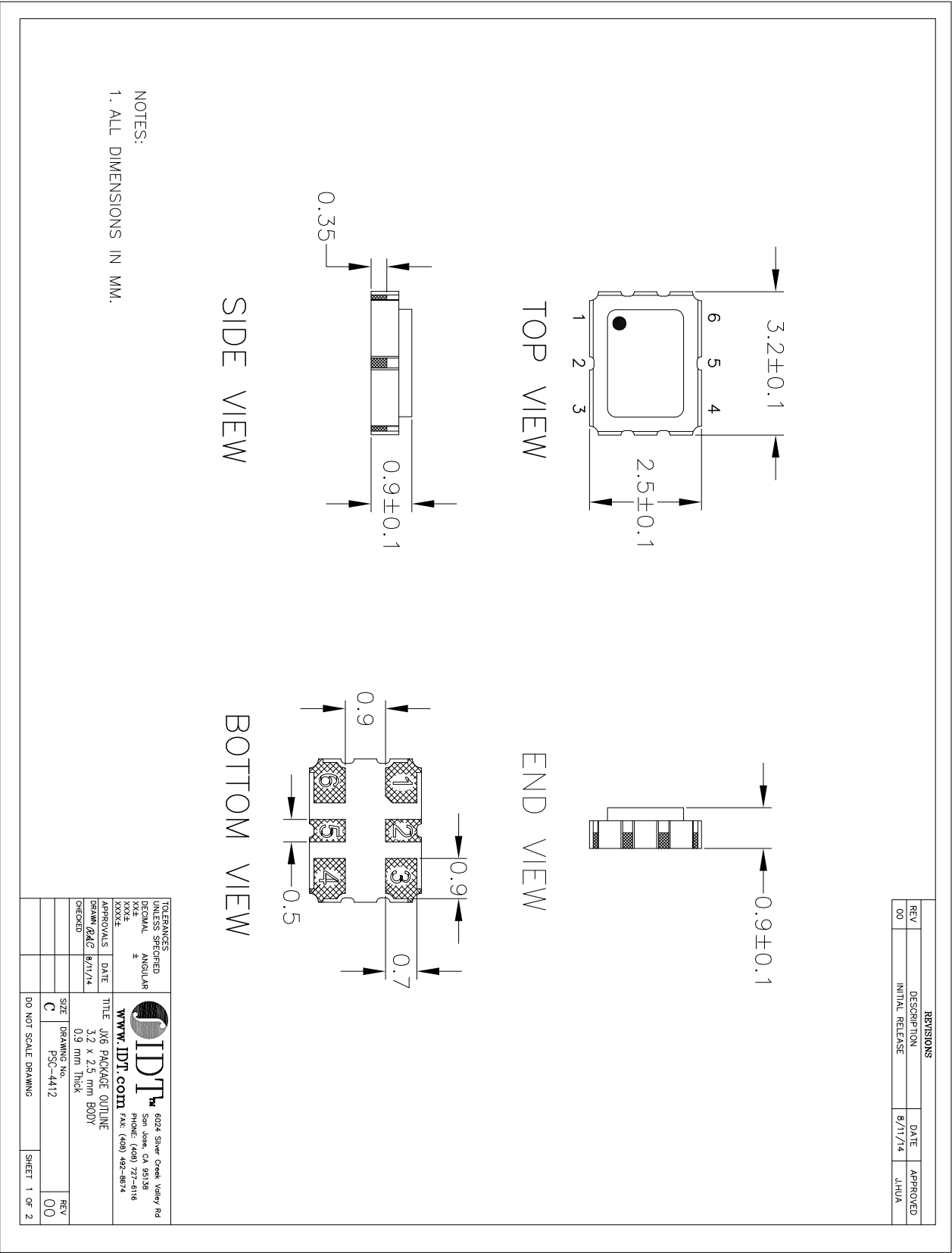
RECOMMENDED LAND PATTERN

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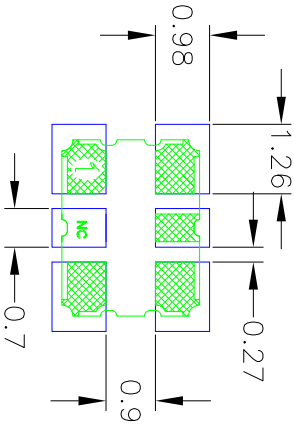
| REVISIONS |                        |         |          |
|-----------|------------------------|---------|----------|
| REV       | DESCRIPTION            | DATE    | APPROVED |
| 00        | INITIAL RELEASE        | 10/5/12 | KS       |
| 01        | UPDATE PACKAGE DRAWING | 8/12/14 | JHUA     |

|                                |          |                      |  |                                                                                                   |  |
|--------------------------------|----------|----------------------|--|---------------------------------------------------------------------------------------------------|--|
| TOLERANCES<br>UNLESS SPECIFIED |          | IDT                  |  | 8024 Silver Creek Valley Rd<br>San Jose, CA 95138<br>PHONE: (408) 727-8118<br>FAX: (408) 486-6919 |  |
| DECIMAL                        | ANGULAR  | www.idt.com          |  | TITLE                                                                                             |  |
| XXXX                           | °        | DATE                 |  | JU6 PACKAGE OUTLINE                                                                               |  |
| DRAWN BY                       | DATE     | SIZE                 |  | DRAWING No.                                                                                       |  |
| 00000                          | 10/03/12 | C                    |  | PSC-4430                                                                                          |  |
| OVERSIZED                      |          | DO NOT SCALE DRAWING |  | REV                                                                                               |  |
|                                |          |                      |  | 01                                                                                                |  |

JX6 Package Outline and Dimensions



JX6 Package Outline and Dimensions (cont.)



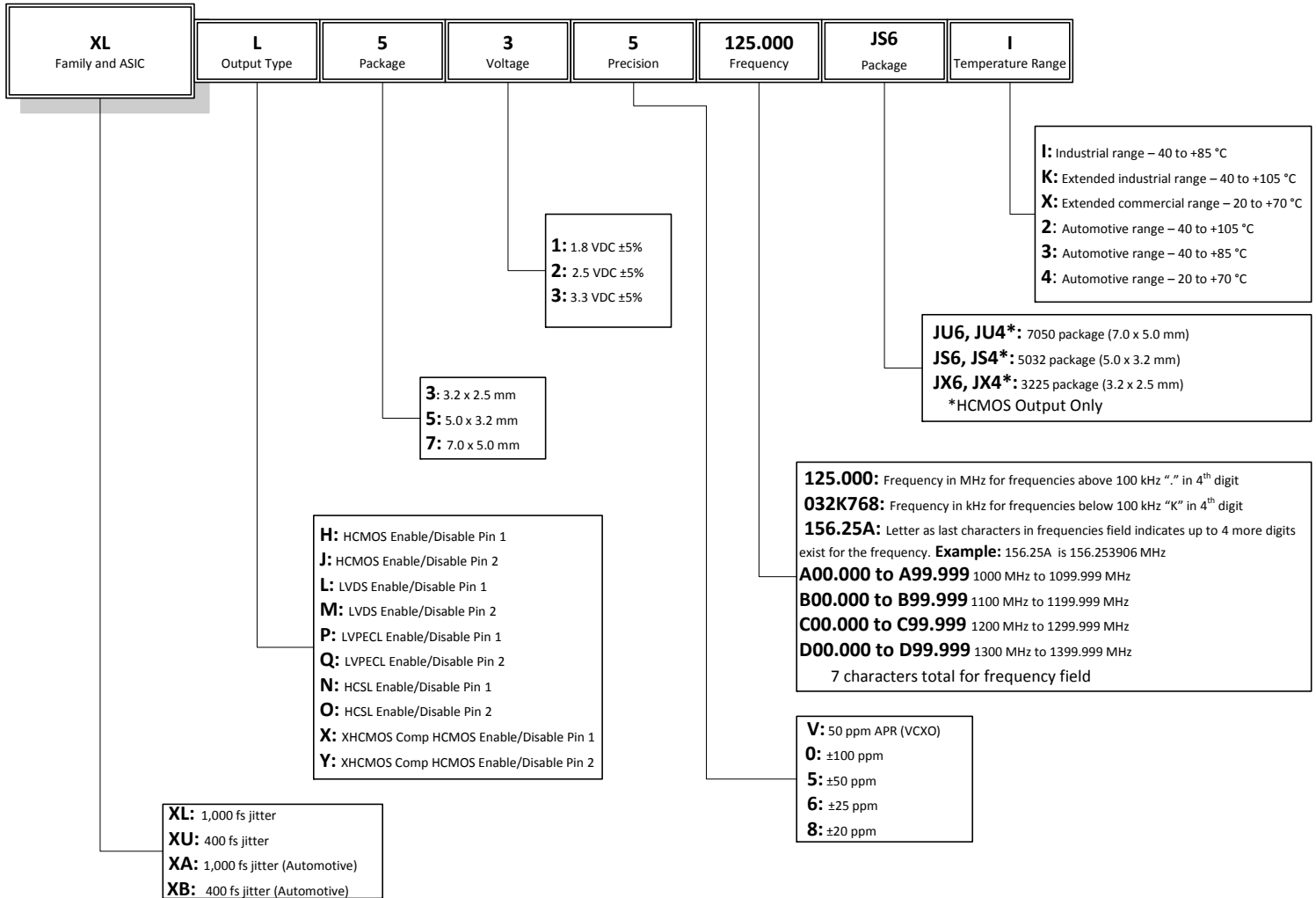
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| REVISIONS |                 |         |          |
|-----------|-----------------|---------|----------|
| REV       | DESCRIPTION     | DATE    | APPROVED |
| 00        | INITIAL RELEASE | 8/11/14 | JHUA     |

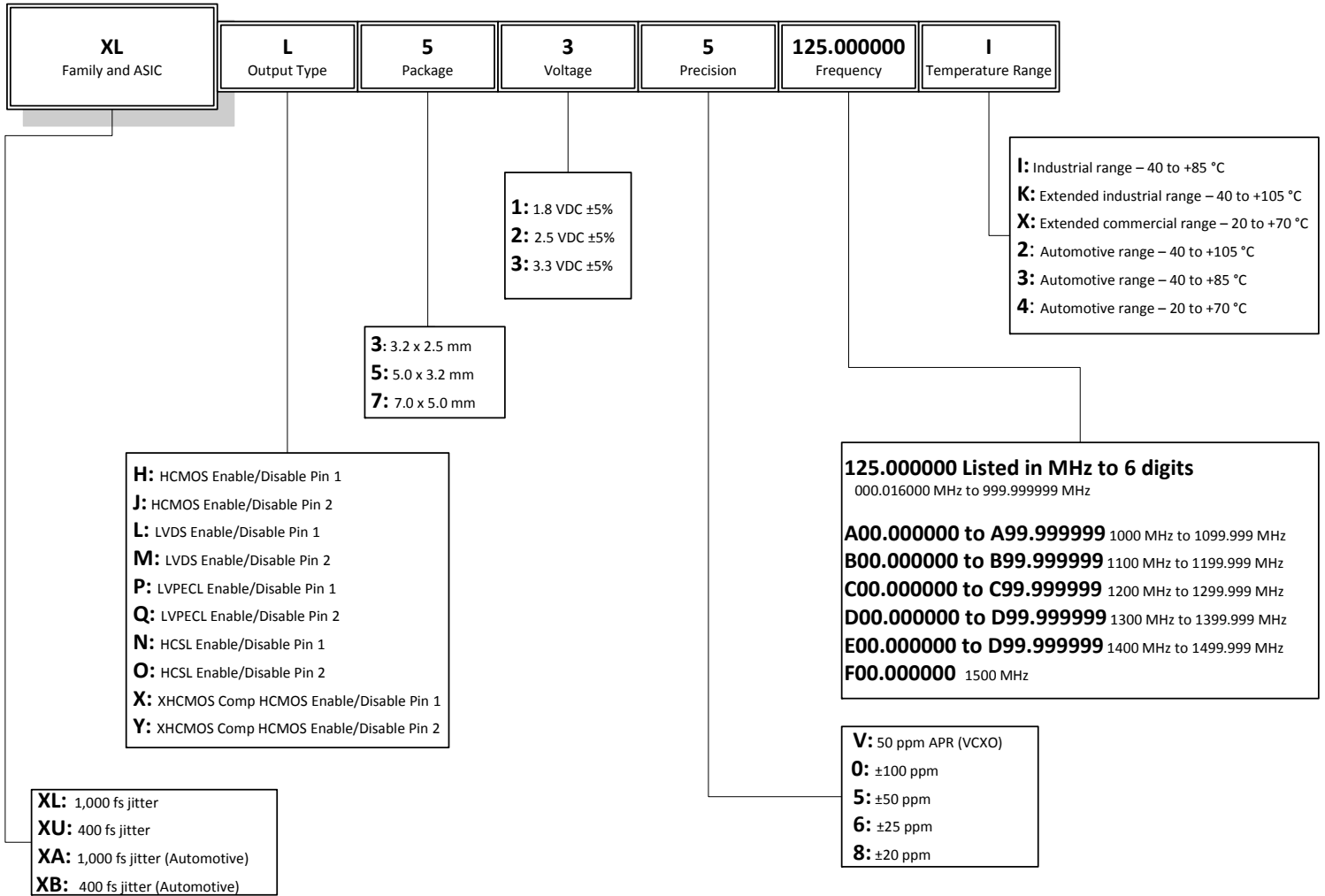
|                             |             |                             |              |
|-----------------------------|-------------|-----------------------------|--------------|
| TOLERANCES UNLESS SPECIFIED |             | IDT                         |              |
| DECIMAL                     | ANGULAR     | 6024 Silver Creek Valley Rd |              |
| ±                           |             | San Jose, CA 95138          |              |
| XXE                         |             | PHONE: (408) 727-6116       |              |
| XXE                         |             | FAX: (408) 492-8674         |              |
| APPROVALS                   | DATE        | TITLE JX6 PACKAGE OUTLINE   |              |
| DRWN 024C                   | 8/11/14     | 3.2 x 2.5 mm BODY           |              |
| ORCGRD                      |             | 0.3 mm THICK                |              |
| SIZE                        | DRAWING No. | REV                         |              |
| C                           | FSC-4412    | 00                          |              |
| DO NOT SCALE DRAWING        |             |                             | SHEET 2 OF 2 |

## IDT Ordering Information Scheme #1 (for reference only)



The **IDT Ordering Information Scheme #1** table above is to be used for reference only. IDT is updating the orderable part number for this device family to support frequency accuracy and ordering down to the 1Hz level. Please see the **IDT Ordering Information Scheme #2** table on the following page for the latest ordering information.

## IDT Ordering Information Scheme #2



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## Revision History

| Rev. | Date     | Originator | Description of Change                                                                                                             |
|------|----------|------------|-----------------------------------------------------------------------------------------------------------------------------------|
| C    | 10/28/16 | P. Jenkins | Update ordering information decoder tables by separating them into Scheme 1 and Scheme 2; add note to distinguish the two tables. |
|      |          |            |                                                                                                                                   |



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**Tech Support**  
[www.IDT.com/go/support](http://www.IDT.com/go/support)

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|                                          |                                          |                                          |                                          |                                          |
|------------------------------------------|------------------------------------------|------------------------------------------|------------------------------------------|------------------------------------------|
| <a href="#"><u>XLL725031.000JU6X</u></a> | <a href="#"><u>XLL730800.000JU6X</u></a> | <a href="#"><u>XLL735081.000JU6X</u></a> | <a href="#"><u>XLL725135.000JU6X</u></a> | <a href="#"><u>XLL535161.13AJS6I</u></a> |
| <a href="#"><u>XLL725800.000JU6X</u></a> | <a href="#"><u>XLL52V297.000JS6I</u></a> | <a href="#"><u>XLL335161.13AJX6I</u></a> | <a href="#"><u>XLL52V296.00AJS6I</u></a> | <a href="#"><u>XLL536012.000JS6X</u></a> |
| <a href="#"><u>XLL336032.000JX6I</u></a> | <a href="#"><u>XLL325148.500JX6I</u></a> | <a href="#"><u>XLL735050.000JU6I</u></a> | <a href="#"><u>XLL336040.000JX6I</u></a> | <a href="#"><u>XLL730001.000JU6X</u></a> |
| <a href="#"><u>XLL32V297.000JX6I</u></a> | <a href="#"><u>XLL32V296.00AJX6I</u></a> | <a href="#"><u>XLL735135.000JU6X</u></a> | <a href="#"><u>XLL735160.000JU6I</u></a> | <a href="#"><u>XLL730156.250000X</u></a> |
| <a href="#"><u>XLL735060.000000I</u></a> | <a href="#"><u>XLL735156.250000X</u></a> | <a href="#"><u>XLL538050.000000X</u></a> | <a href="#"><u>XLL736005.000000X</u></a> | <a href="#"><u>XLL530133.000000I</u></a> |
| <a href="#"><u>XLL735270.000000X</u></a> | <a href="#"><u>XLL730020.000000X</u></a> | <a href="#"><u>XLL738155.520000X</u></a> | <a href="#"><u>XLL735312.500000X</u></a> | <a href="#"><u>XLL73V150.000000I</u></a> |
| <a href="#"><u>XLL73V300.000000I</u></a> | <a href="#"><u>XLL735250.000000I</u></a> | <a href="#"><u>XLL738100.000000X</u></a> | <a href="#"><u>XLL53V622.080000I</u></a> | <a href="#"><u>XLL738125.000000X</u></a> |
| <a href="#"><u>XLL735300.000000I</u></a> | <a href="#"><u>XLL735220.000000I</u></a> | <a href="#"><u>XLL735166.666700X</u></a> | <a href="#"><u>XLL736781.250000X</u></a> | <a href="#"><u>XLL730200.000000X</u></a> |
| <a href="#"><u>XLL735300.000000X</u></a> | <a href="#"><u>XLL73VA90.000000I</u></a> | <a href="#"><u>XLL736024.576000X</u></a> | <a href="#"><u>XLL735166.000000I</u></a> | <a href="#"><u>XLL736700.000000X</u></a> |
| <a href="#"><u>XLL73V148.351600I</u></a> | <a href="#"><u>XLL535250.000000I</u></a> | <a href="#"><u>XLL538100.000000X</u></a> | <a href="#"><u>XLL730050.000000X</u></a> | <a href="#"><u>XLL736100.000000X</u></a> |
| <a href="#"><u>XLL530148.500000X</u></a> | <a href="#"><u>XLL738250.000000X</u></a> | <a href="#"><u>XLL735062.500000X</u></a> | <a href="#"><u>XLL735150.000000I</u></a> | <a href="#"><u>XLL735148.500000X</u></a> |
| <a href="#"><u>XLL73V148.500000I</u></a> | <a href="#"><u>XLL536400.000000X</u></a> | <a href="#"><u>XLL53V877.714285X</u></a> | <a href="#"><u>XLL73V096.000000I</u></a> | <a href="#"><u>XLL735050.000000X</u></a> |
| <a href="#"><u>XLL736106.250000X</u></a> | <a href="#"><u>XLL538001.000000X</u></a> | <a href="#"><u>XLL735106.250000I</u></a> | <a href="#"><u>XLL735150.000000X</u></a> | <a href="#"><u>XLL738200.000000X</u></a> |
| <a href="#"><u>XLL738600.000000X</u></a> | <a href="#"><u>XLL735050.000000I</u></a> | <a href="#"><u>XLL736066.000000X</u></a> | <a href="#"><u>XLL736125.000000X</u></a> | <a href="#"><u>XLL535125.000000X</u></a> |
| <a href="#"><u>XLL735360.000000X</u></a> | <a href="#"><u>XLL738156.250000X</u></a> | <a href="#"><u>XLL530300.000000X</u></a> | <a href="#"><u>XLL538A00.000000X</u></a> | <a href="#"><u>XLL736312.500000I</u></a> |
| <a href="#"><u>XLL736400.000000X</u></a> | <a href="#"><u>XLL536A00.000000I</u></a> | <a href="#"><u>XLL730100.000000X</u></a> | <a href="#"><u>XLL735400.000000X</u></a> | <a href="#"><u>XLL736053.125000I</u></a> |
| <a href="#"><u>XLL735212.500000X</u></a> | <a href="#"><u>XLL735250.000000X</u></a> | <a href="#"><u>XLL335125.000000I</u></a> | <a href="#"><u>XLL726156.250000I</u></a> | <a href="#"><u>XLL736000.750000I</u></a> |
| <a href="#"><u>XLL73V060.000000I</u></a> | <a href="#"><u>XLL536093.600000I</u></a> | <a href="#"><u>XLL536065.536000I</u></a> | <a href="#"><u>XLL526180.000000X</u></a> | <a href="#"><u>XLL335054.000000I</u></a> |
| <a href="#"><u>XLL535180.000000X</u></a> | <a href="#"><u>XLL335100.000000I</u></a> | <a href="#"><u>XLL736062.500000I</u></a> | <a href="#"><u>XLL53V045.000000X</u></a> | <a href="#"><u>XLL326072.000000I</u></a> |
| <a href="#"><u>XLL536050.000000X</u></a> | <a href="#"><u>XLL736060.000000I</u></a> | <a href="#"><u>XLL736200.000000X</u></a> | <a href="#"><u>XLL526166.666666I</u></a> | <a href="#"><u>XLL736062.500000X</u></a> |