

Features

- Generates any Telecom or SyncE frequency independent of the input frequency rate
- Two general purpose synthesizers generate a wide range of digital bus clocks
- Programmable digital PLLs synchronizes to any Telecom ($N * 8$ kHz) or any Synchronized Ethernet (SyncE) frequencies.
- Flexible two-stage architecture translates between arbitrary data rates, line coding rates and FEC rates
- Digital PLLs filter jitter from 14 Hz, 28 Hz, 56 Hz, 112 Hz, 224 Hz, 448 Hz or 896 Hz
- Four programmable Numerically Controlled Oscillators (NCOs) available where two NCOs can be used at the time
- Automatic hitless reference switching and digital holdover on reference fail
- Four reference inputs configurable as single ended

Ordering Information

ZL30150GGG	100 Pin LBGA	Trays
ZL30150GGG2	100 Pin LBGA*	Trays

*Pb Free Tin/Silver/Copper
-40°C to +85°C

or differential

- Eight LVPECL outputs and four LVCMOS outputs
- Eight outputs configurable as LVCMOS or LVDS/LVPECL/HCSL
- Operates from a single crystal resonator or clock oscillator
- Configurable via SPI/I2C interface

Applications

- 10 Gigabit line cards
- Synchronous Ethernet, 10 GBASE-R and 10 GBASE-W
- SONET/SDH

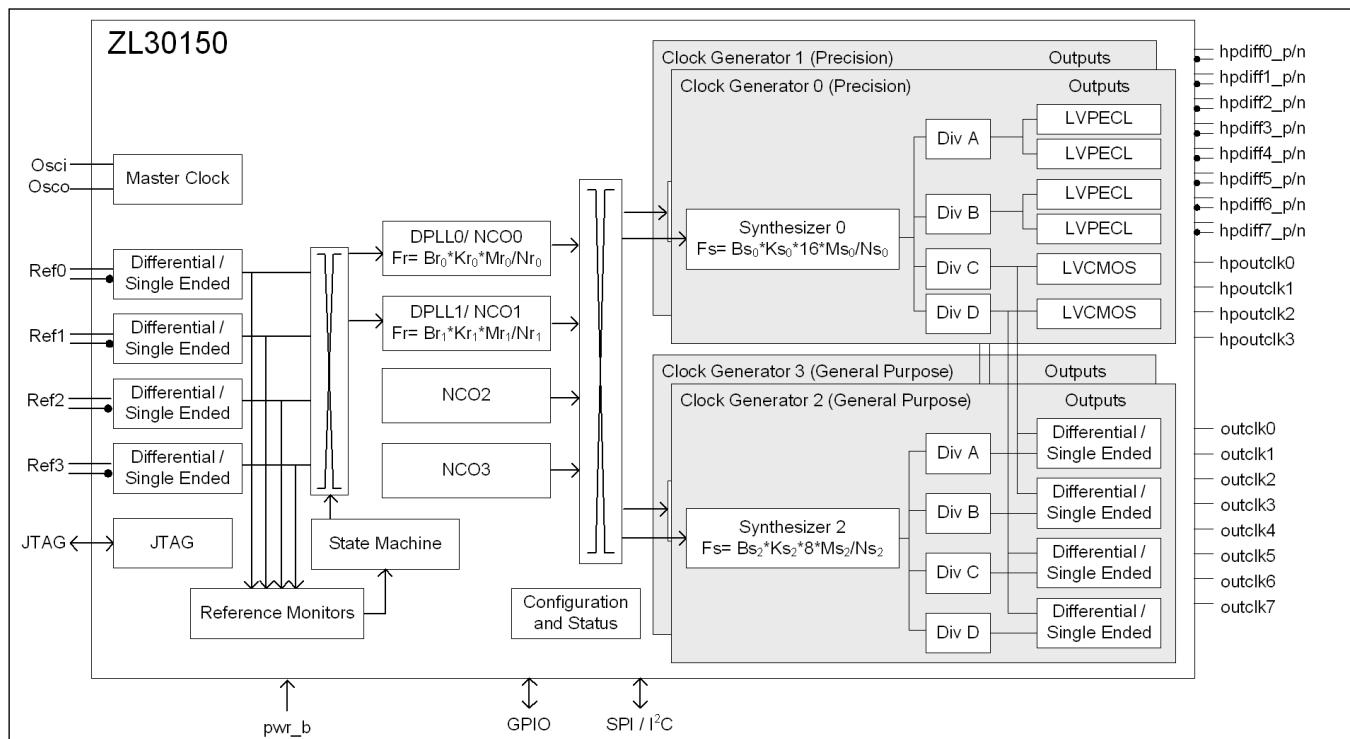
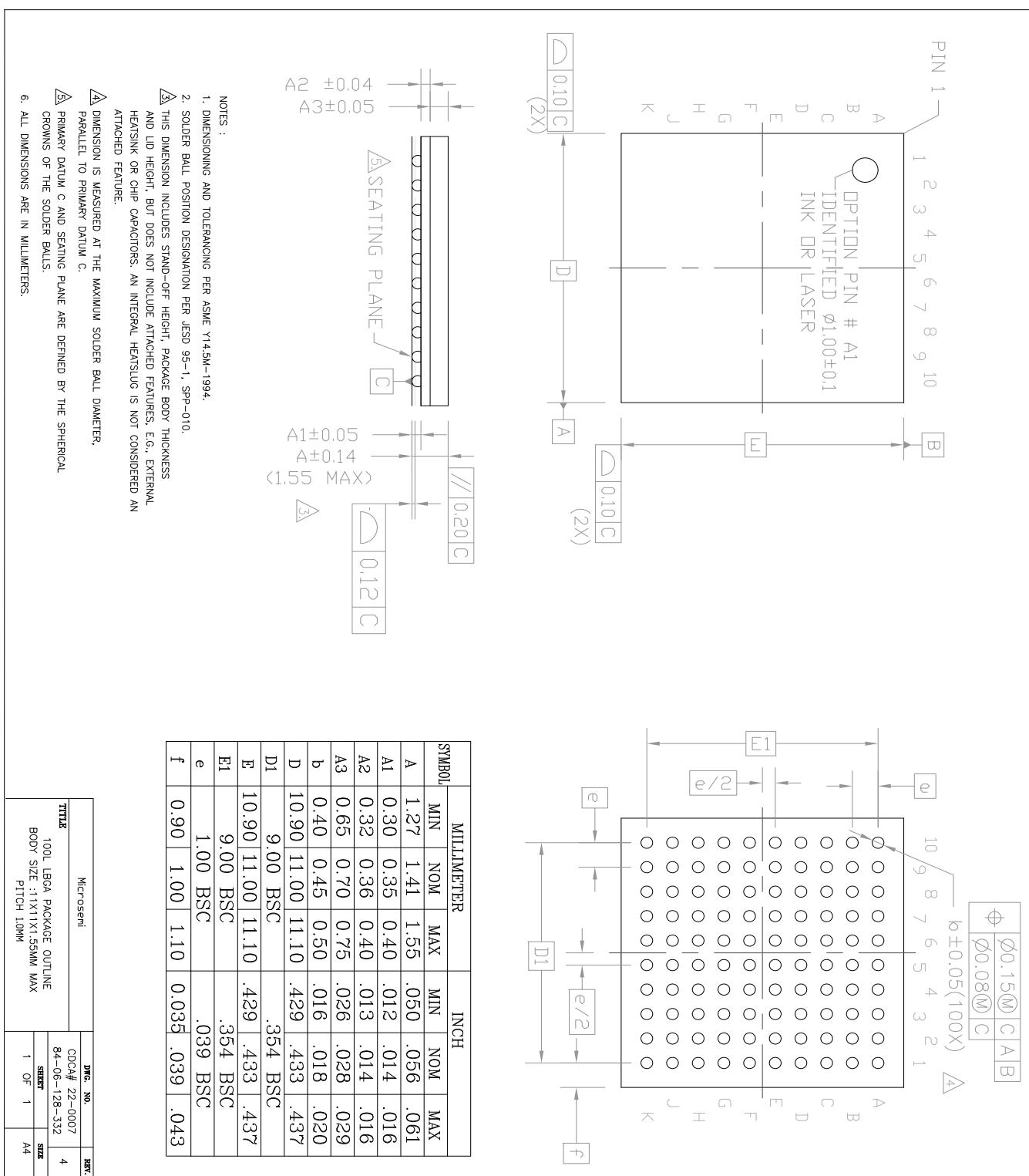


Figure 1 - Functional Block Diagram

Mechanical Drawing





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