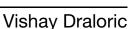
HALOGEN

FREE





Long Side Termination Thick Film Chip Resistors



RCL e3 resistors series are the perfect choice for most fields of power measurement electronics where reliability, stability, power dissipation, and robust design is of major concern.

Typical applications include power electronics in automotive and industrial appliances.

FEATURES

- Enhanced power rating
- · Long side terminations
- · Enhanced thermal cycling performance
- Pure tin solder contacts on Ni barrier layer, provides compatibility with lead (Pb)-free and lead containing soldering processes
- AEC-Q200 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Automotive
- Industrial
- Commercial

DESCRIPTION	RCL0406 e3	RCL0612 e3	RCL1020 e3	RCL1218 e3	RCL1225 e3	
Imperial size	0406	0612	1020	1218	1225	
Metric size code	RR1016M	RR1632M	RR2550M	RR3246M	RR3263M	
Resistance range	1 Ω	to 1 MΩ; Jumper (0	1 Ω to 2.2 M Ω ; Jumper (0 Ω)	1 Ω to 1 M Ω ; Jumper (0 Ω)		
Resistance tolerance			± 5 %; ± 1 %			
Temperature coefficient	perature coefficient ± 200 ppm/K; ± 100 ppm/K					
Rated dissipation, $P_{70}^{\ (1)}$	0.25 W	0.5 W	1.0 W	1.0 W	2.0 W ⁽²⁾	
Operating voltage, U _{max.} AC _{RMS} /DC	50 V	50 V 75 V 200 V		200 V	200 V	
Permissible film temperature, $\vartheta_{\text{F max.}}^{(1)}$			155 °C			
Operating temperature range			-55 °C to +155 °C			
Max. resistance change at P_{70} for resistance range, $ \Delta R/R $ after:						
1000 h			≤ 1.0 %			
8000 h	≤ 2.0 %					
Permissible voltage against ambient (insulation):						
1 min, <i>U</i> _{ins}	100 V	100 V	300 V	300 V	300 V	

Notes

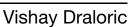
APPLICATION INFORMATION

When the resistor dissipates power, a temperature rise above the ambient temperature occurs, dependent on the thermal resistance of the assembled resistor together with the printed circuit board. The rated dissipation applies only if the permitted film temperature is not exceeded.

These resistors do not feature a limited lifetime when operated within the permissible limits. However, resistance value drift increasing over operating time may result in exceeding a limit acceptable to the specific application, thereby establishing a functional lifetime.

⁽¹⁾ Please refer to APPLICATION INFORMATION below.

⁽²⁾ Specified power rating requires dedicated mounting conditions to achieve the required thermal resistance.



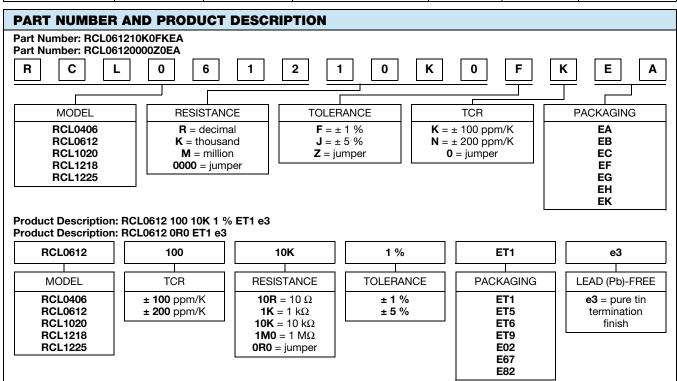


TEMPERATU	RE COEFFICIENT AND	RESISTANCE RANG	E	
TYPE / SIZE	TCR	TOLERANCE	RESISTANCE	E-SERIES
	± 200 ppm/K	± 5 %	1 Ω to 1 MΩ	E24
RCL0406 e3	± 100 ppm/K	± 1 %	1 Ω to 1 MΩ	E24; E96
	Jumper, $I_{\text{max.}} = 4 \text{ A}$	≤ 10 mΩ	0 Ω	-
	± 200 ppm/K	± 5 %	1 Ω to 1 MΩ	E24
RCL0612 e3	± 100 ppm/K	± 1 %	1 Ω to 1 MΩ	E24; E96
	Jumper, I _{max.} = 6 A	≤ 10 mΩ	0 Ω	-
	± 200 ppm/K	± 5 %	1 Ω to 1 MΩ	E24
RCL1020 e3	± 100 ppm/K	± 1 %	1 Ω to 1 MΩ	E24; E96
	Jumper, $I_{\text{max.}} = 10 \text{ A}$	≤ 10 mΩ	0 Ω	-
	± 200 ppm/K	± 5 %	1 Ω to 2.2 MΩ	E24
RCL1218 e3	± 100 ppm/K	± 1 %	1 Ω to 2.2 MΩ	E24; E96
	Jumper, $I_{\text{max.}} = 7 \text{ A}$	≤ 20 mΩ	0 Ω	-
	± 200 ppm/K	± 5 %	1 Ω to 1 MΩ	E24
RCL1225 e3	± 100 ppm/K	± 1 %	1 Ω to 1 MΩ	E24; E96
	Jumper, $I_{\text{max.}} = 12 \text{ A}$	≤ 10 mΩ	0 Ω	-

Note

- The temperature coefficient of resistance (TCR) is not specified for 0 Ω jumpers.

PACKAGING									
TYPE / SIZE	CODE	QUANTITY	PACKAGING STYLE	WIDTH	PITCH	PACKAGING DIMENSIONS			
	EA = ET1	5000				Ø 180 mm/7"			
RCL0406 e3	EB = ET5	10 000			4 mm	Ø 285 mm/11.25"			
	EC = ET6	20 000	Paper tape acc. to	0		Ø 330 mm/13"			
	EA = ET1	5000	IEC 60286-3, Type 1a	8 mm	4 mm	Ø 180 mm/7"			
RCL0612 e3	EB = ET5	10 000				Ø 285 mm/11.25"			
	EC = ET6	20 000				Ø 330 mm/13"			
RCL1020 e3	EF = E02	4000			4 mm				
RCL1218 e3	EK = ET9	4000	Pressed tape acc. to	12 mm	4 111111	Ø 180 mm/7"			
RCL1225 e3	EG = E67	2000	IEC 60286-3, Type 1b		8 mm	וווווו טסו ש ו			
NOL 1220 83	EH = E82	4000			4 mm 4 mm 4 mm				





Vishay Draloric

DESCRIPTION

Production is strictly controlled and follows an extensive set of instructions established for reproducibility. A cermet film layer and a glass-over are deposited on a high grade (Al_2O_3) ceramic substrate with its prepared inner contacts on both sides. A special laser is used to achieve the target value by smoothly fine trimming the resistive layer without damaging the ceramics. The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The terminations receive a final pure tin on nickel plating.

The result of the determined production is verified by an extensive testing procedure on 100 % of the individual chip resistors. Only accepted products are laid directly into the tape in accordance with **IEC 60286-3 Type 1a and Type 1b** (1).

ASSEMBLY

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using wave, reflow or vapor phase as shown in **IEC 61760-1** ⁽¹⁾. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions. The suitability of conformal coatings, potting compounds and their processes, if applied, shall be qualified by appropriate means to ensure the long-term stability of the whole system.

The resistors are RoHS-compliant, the pure tin plating provides compatibility with lead (Pb)-free and lead-containing soldering processes. Solderability is specified for 2 years after production or requalification. The permitted storage time is 20 years. The immunity of the plating against tin whisker growth has been proven under extensive testing.

MATERIALS

Vishay acknowledges the following systems for the regulation of hazardous substances:

- IEC 62474, Material Declaration for Products of and for the Electrotechnical Industry, with the list of declarable substances given therein (2)
- The Global Automotive Declarable Substance List (GADSL) (3)
- The REACH regulation (1907/2006/EC) and the related list of substances with very high concern (SVHC) (4) for its supply chain

The products do not contain any of the banned substances as per IEC 62474, GADSL, or the SVHC list, see www.vishay.com/how/leadfree.

Hence the products fully comply with the following directives:

- 2000/53/EC End-of-Life Vehicle Directive (ELV) and Annex II (ELV II)
- 2011/65/EU Restriction of the Use of Hazardous Substances Directive (RoHS) with amendment 2015/863/EU
- 2012/19/EU Waste Electrical and Electronic Equipment Directive (WEEE)

Vishay pursues the elimination of conflict minerals from its supply chain, see the Conflict Minerals Policy at www.vishay.com/doc?49037.

APPROVALS

The resistors are qualified according to AEC-Q200. Where applicable, the resistors are tested in accordance with **EN 140401-802** which refers to **EN 60115-1**, **EN 60115-8** and the variety of environmental test procedures of the **IEC 60068** (1) series.

RELATED PRODUCTS

The RCA-LS e3, Sulfur Resistant, Long Side Termination Thick Film Chip Resistors series is designed for harsh environment applications. For ordering RCA-LS e3 products please refer to latest edition of datasheet, www.vishav.com/doc?20060.

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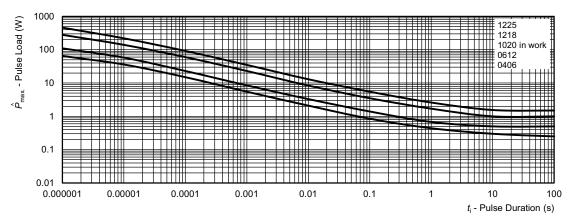
Notes

- (1) The quoted IEC standards are also released as EN standards with the same number and identical contents.
- (2) The IEC 62474 list of declarable substances is maintained in a dedicated database, which is available at http://std.iec.ch/iec62474.
- (3) The Global Automotive Declarable Substance List (GADSL) is maintained by the American Chemistry Council and available at www.gadsl.org.
- (4) The SVHC list is maintained by the European Chemical Agency (ECHA) and available at http://echa.europa.eu/candidate-list-table.



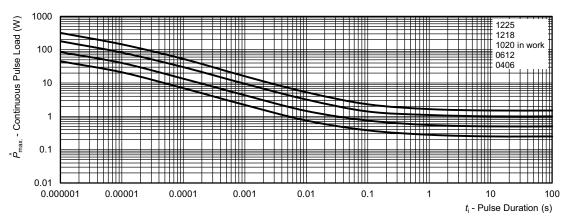
FUNCTIONAL PERFORMANCE

Single Pulse



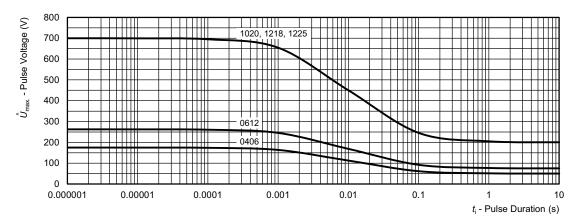
Maximum pulse load, single pulse; applicable if $\bar{P} \to 0$ and n < 1000 and $\hat{U} \le \hat{U}_{\text{max}}$; for permissible resistance change equivalent to 8000 h operation

Continuous Pulse



Maximum pulse load, continuous pulses; applicable if $\bar{P} \leq P(v_{amb})$ and $\hat{U} \leq \hat{U}_{max}$; for permissible resistance change equivalent to 8000 h operation

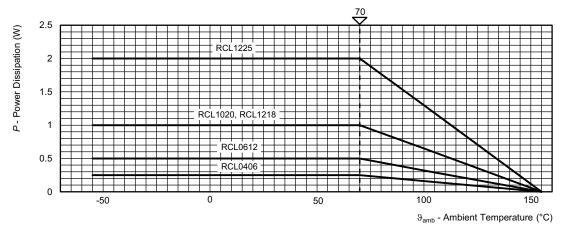
Pulse Voltage



Maximum pulse voltage, single and continuous pulses; applicable if $\hat{P} \leq \hat{P}_{\text{max}}$; for permissible resistance change equivalent to 8000 h operation

Vishay Draloric

Derating





Vishay Draloric

TESTS AND REQUIREMENTS

All executed tests are carried out in accordance with the following specifications:

EN 60115-1, generic specification

EN 60115-8 (successor of EN 140400), sectional specification

EN 140401-802, detail specification

IEC 60068-2-xx, test methods

The parameters stated in the Test Procedures and Requirements table are based on the required tests and permitted limits of EN 140401-802. The table presents only the most important tests, for the full test schedule refer to the documents listed above. However, some additional tests and a number of improvements against those minimum requirements have been included.

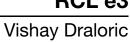
The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5201-1.

The tests are carried out under standard atmospheric conditions in accordance with IEC 60068-1, 4.3, whereupon the following values are applied:

Temperature: 15 °C to 35 °C Relative humidity: 25 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar).

TEST PR	OCEDUR	ES AND REQUIR	EMENTS			
EN 60115-1 CLAUSE			PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (Δ <i>R</i>)		
			Stability for product types:	STABILITY CLASS 1 OR BETTER	STABILITY CLASS 2 OR BETTER	
			RCL e3	1 Ω to 2.2 MΩ		
4.5	-	Resistance	-	± 1 %	± 5 %	
4.8	-	Temperature coefficient	(20 / -55 / 20) °C and (20 / 155 / 20) °C	± 100 ppm/K	± 200 ppm/K	
4.25.1	_	Endurance at 70 °C	$U = \sqrt{P_{70} \times R}$ or $U = U_{\text{max.}}$; whichever is the less severe; 1.5 h on; 0.5 h off			
1.20.1		Endurance at 70 °C	70 °C; 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
			70 °C; 8000 h	± (2 % R + 0.1 Ω)	± (4 % R + 0.1 Ω)	
4.25.3	-	Endurance at upper category temperature	155 °C, 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH;	± (1 % R + 0.05 Ω)		
4.37	67 (Cy)	Damp heat, steady state, accelerated	(85 ± 2) °C; (85 ± 5) % RH; $U = 0.1 \times \sqrt{P_{70} \times R} \le 100 \text{ V};$ 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
4.23	-	Climatic sequence:	-			
4.23.2	2 (Bb)	dry heat	125 °C; 16 h			
4.23.3	30 (Db)	damp heat, cyclic	55 °C; 24 h; ≥ 90 % RH; 1 cycle			
4.23.4	1 (Ab)	cold	-55 °C; 2 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)	
4.23.5	13 (M)	low air pressure	8.5 kPa; 2 h; (25 ± 10) °C	_ (1 /0 /1 1 0.00 22)	_ (2 /0/1 / 0/1 22)	
4.23.6	23.6 30 (Db) damp heat, cyclic		55 °C; 24 h; ≥ 90 % RH; 5 cycles			
4.23.7	-	DC load	$U = \sqrt{P_{70} \times R} \le U_{\text{max.};} \text{ 1 min}$			
-	1 (Aa)	Cold	-55 °C; 2 h	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)	
4.19	14 (Na)	Rapid change of temperature	30 min at -55 °C and 30 min at 125 °C; 1000 cycles	\pm (1 % R + 0.05 Ω) no visible damage		





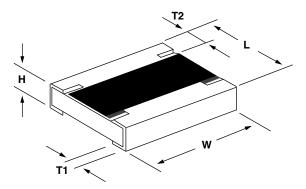
EN 60115-1 CLAUSE	IEC 60068-2 ⁽¹⁾ TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (Δ <i>R</i>)			
			Stability for product types:	STABILITY CLASS 1 OR BETTER	STABILITY CLASS 2 OR BETTER		
			RCL e3				
4.13	-	Short time overload	$U = 2.5 \times \sqrt{P_{70} \times R} \le 2 \times U_{\text{max.}};$ whichever is the less severe; 5 s	± (2 % R + 0.05 Ω)			
4.27	-	Single pulse high voltage overload	Severity no. 4: $U = 10 \times \sqrt{P_{70} \times R}$ or $U = 2 \times U_{\text{max.}}$; whichever is the less severe; 10 pulses 10 µs/700 µs		+ 0.05 Ω) e damage		
4.39	-	Periodic electric overload	$U = \sqrt{15 \times P_{70} \times R}$ or $U = 2 \times U_{\text{max.}}$; whichever is the less severe; 0.1 s on; 2.5 s off; 1000 cycles	± (1 % R + 0.05 Ω) no visible damage			
4.40	-	Electrostatic discharge (human body model)	IEC 61340-3-1 ⁽¹⁾ ; 3 pos. + 3 neg. discharges; ESD voltage acc. to the size	± (1 % R + 0.05 Ω)			
4.22	6 (Fc)	Vibration	Endurance by sweeping; 10 Hz to 2000 Hz; no resonance; amplitude \leq 1.5 mm or \leq 200 m/s ² ; 7.5 h	± (0.25 % R + 0.05 Ω) no visible damage	± (0.5 % R + 0.05 Ω) no visible damage		
4.17	58 (Td)	Solderability	Solder bath method; Sn60Pb40 non-activated flux; (235 ± 5) °C; (2 ± 0.2) s Solder bath method; Sn96.5Ag3Cu0.5 non-activated flux; (245 ± 5) °C; (3 ± 0.3) s	Good tinning (≥ 95 % covered) no visible damage			
4.18	58 (Td)	Resistance to soldering heat	Solder bath method (260 ± 5) °C; (10 ± 1) s	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)		
4.29	45 (XA)	Component solvent resistance	Isopropyl alcohol; +50 °C; method 2	No visible damage			
4.32	21 (Uu ₃)	Shear (adhesion)	17.7 N	No visible damage			
4.33	21 (Uu ₁)	Substrate bending	Depth 2 mm; 3 times	\pm (0.25 % R + 0.05 Ω) no visible damage, no open circuit in bent posit			
4.7	-	Voltage proof	$U = 1.4 \times U_{ins}$; 60 s	<u> </u>	or breakdown		
4.35	-	Flammability, needle flame test	IEC 60695-11-5 ⁽¹⁾ ; 10 s	No burning	g after 30 s		

Note

⁽¹⁾ The quoted IEC standards are also released as EN standards with the same number and identical contents.

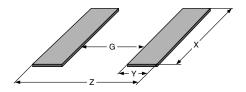






DIMENSIONS AND MASS									
TYPE / SIZE	L (mm)	W (mm)	H (mm)	T1 (mm)	T2 (mm)	MASS (mg)			
RCL0406 e3	1.0 ± 0.10	1.6 ± 0.10	0.35 ± 0.10	0.2 +0.10 / -0.15	0.2 ± 0.10	2			
RCL0612 e3	1.6 ± 0.20	3.2 ± 0.20	0.55 ± 0.10	0.35 ± 0.15	0.25 ± 0.15	11			
RCL1020 e3	2.5 ± 0.20	5.0 ± 0.20	0.55 ± 0.10	0.38 ± 0.15	0.25 ± 0.15	25.5			
RCL1218 e3	3.2 +0.10 / -0.20	4.6 ± 0.15	0.55 ± 0.05	0.45 ± 0.20	0.4 ± 0.20	29.5			
RCL1225 e3	3.2 ± 0.20	6.3 ± 0.20	0.70 ± 0.10	0.8 ± 0.20	0.4 ± 0.20	55			

SOLDER PAD DIMENSIONS



RECOMMENDED SOLDER PAD DIMENSIONS								
		WAVE SO	LDERING		REFLOW SOLDERING			
TYPE / SIZE	G (mm)	Y (mm)	X (mm)	Z (mm)	G (mm)	Y (mm)	X (mm)	Z (mm)
RCL0406 e3	0.30	0.80	1.95	1.90	0.35	0.60	1.75	1.55
RCL0612 e3	0.50	1.20	3.70	2.90	0.60	1.00	3.50	2.60
RCL1020 e3	1.30	1.25	5.50	3.80	1.30	1.10	5.25	3.50
RCL1218 e3	1.80	1.30	5.10	4.40	1.90	1.10	4.90	4.90
RCL1225 e3	1.10	1.80	6.80	4.70	1.20	1.60	6.60	4.40

Notes

- The given solder pad dimensions reflect the considerations for board design and assembly as outlined e.g in standards IEC 61188-5-x (1) or in publication IPC-7351.
- Still, the given solder pad dimensions will be found adequate for most general applications.
- (1) The quoted IEC standards are also released as EN standards with the same number and identical contents.



Legal Disclaimer Notice

Vishay

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Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000