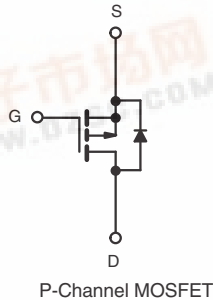
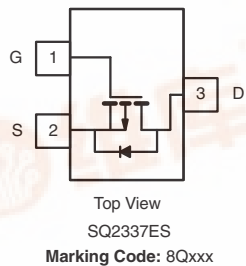


Automotive P-Channel 80 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY

V_{DS} (V)	- 80
$R_{DS(on)}$ (Ω) at $V_{GS} = -10$ V	0.270
I_D (A)	- 2.2

**TO-236
(SOT-23)**

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified^c
- Find out more about Vishay's Automotive Grade Product Requirements at:
www.vishay.com/applications


ORDERING INFORMATION

Package	SOT-23
Lead (Pb)-free and Halogen-free	SQ2337ES-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	- 80	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	$T_C = 25\text{ }^\circ\text{C}$ - 2.2	A
		$T_C = 125\text{ }^\circ\text{C}$ - 1.3	
Continuous Source Current (Diode Conduction)	I_S	- 3.7	
Pulsed Drain Current ^a	I_{DM}	- 9	
Single Pulse Avalanche Current	I_{AS}	- 11	
Single Pulse Avalanche Energy	E_{AS}	6	mJ
Maximum Power Dissipation ^a	P_D	$T_C = 25\text{ }^\circ\text{C}$ 3	W
		$T_C = 125\text{ }^\circ\text{C}$ 1	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	166	$^\circ\text{C/W}$
Junction-to-Foot (Drain)	R_{thJF}	50	

Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



SQ2337ES

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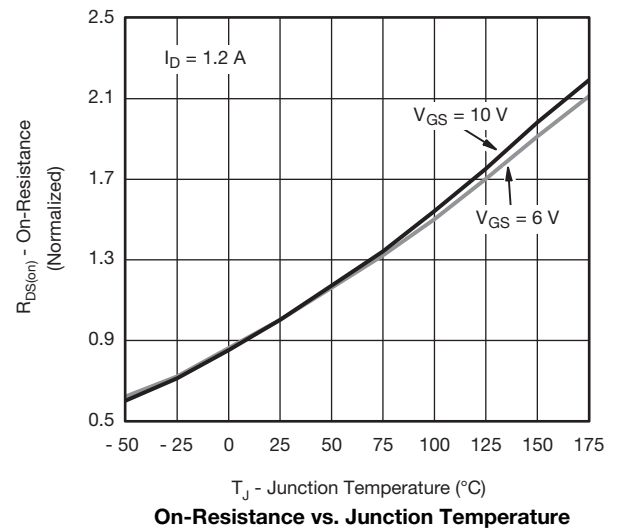
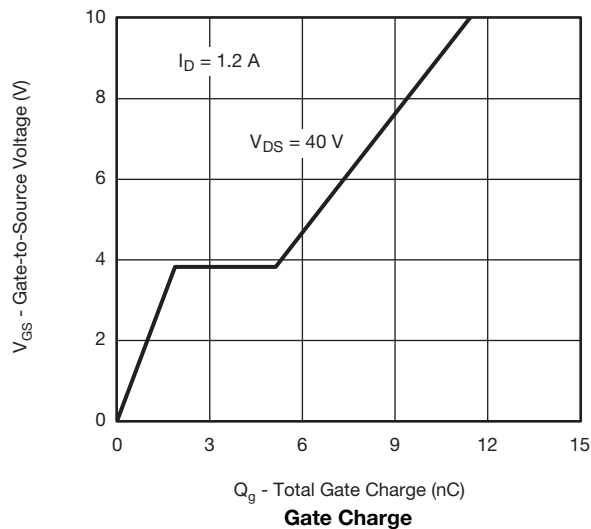
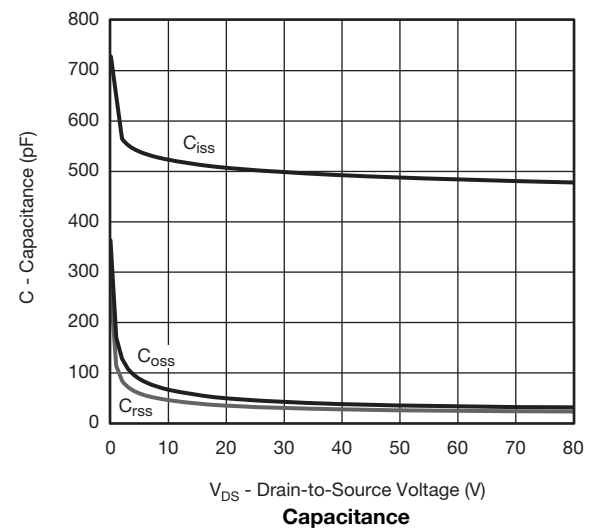
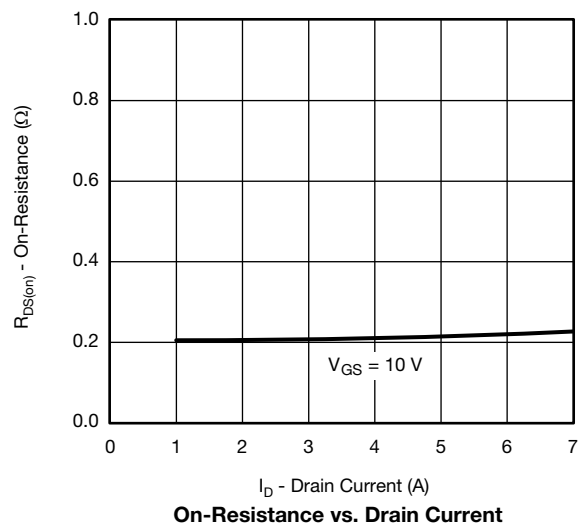
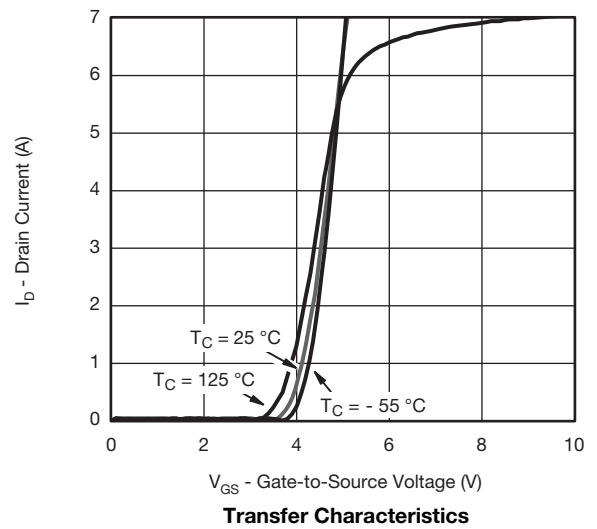
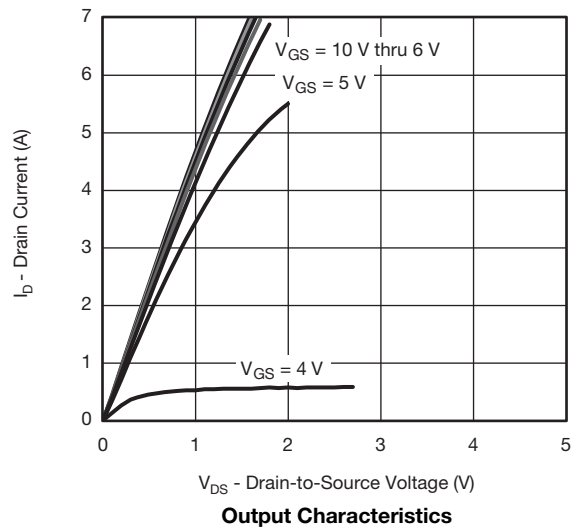


SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA		- 80	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA		- 2.5	-	- 3.5	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = - 80 V	-	-	- 1	μA
		V _{GS} = 0 V	V _{DS} = - 80 V, T _J = 125 °C	-	-	- 50	
		V _{GS} = 0 V	V _{DS} = - 80 V, T _J = 175 °C	-	-	- 150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	V _{DS} ≥ 5 V	- 8	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 1.2 A	-	0.210	0.270	Ω
Forward Transconductance ^b	g _{fs}	V _{DS} = - 15 V, I _D = - 1.2 A		-	4.5	-	S
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = - 40 V, f = 1 MHz	-	495	620	pF
Output Capacitance	C _{oss}			-	40	55	
Reverse Transfer Capacitance	C _{rss}			-	30	40	
Total Gate Charge ^c	Q _g	V _{GS} = - 10 V	V _{DS} = - 40 V, I _D = - 1.2 A	-	11.5	18	nC
Gate-Source Charge ^c	Q _{gs}			-	1.9	-	
Gate-Drain Charge ^c	Q _{gd}			-	3.3	-	
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = - 40 V, R _L = 41.6 Ω I _D ≅ - 0.96 A, V _{GEN} = - 10 V, R _g = 1 Ω		-	5	8	ns
Rise Time ^c	t _r			-	10	15	
Turn-Off Delay Time ^c	t _{d(off)}			-	18	27	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	- 9	A
Forward Voltage	V _{SD}	I _F = - 2 A, V _{GS} = 0 V		-	- 0.8	- 1.2	V

Notes

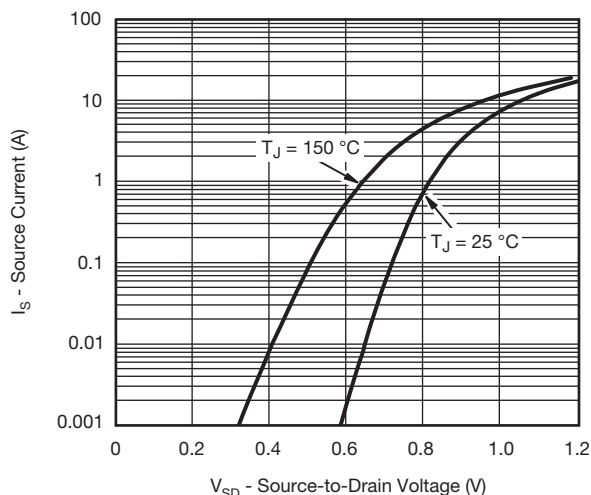
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

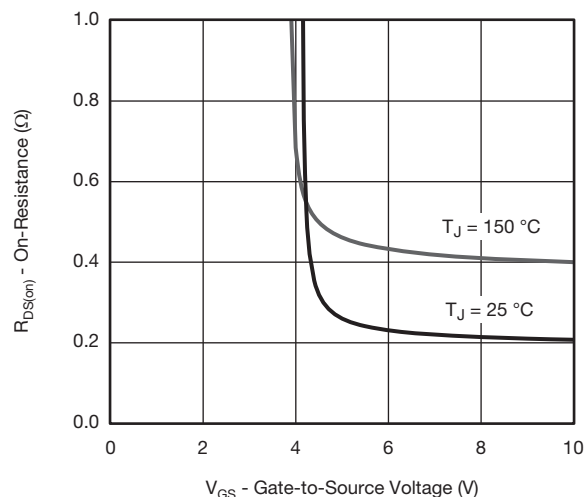
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


SQ2337ES

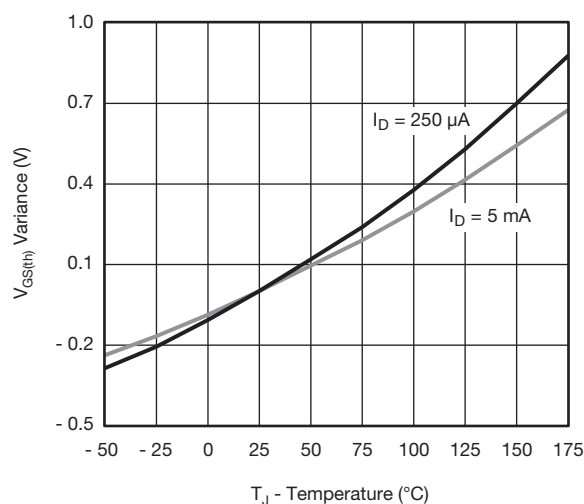
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**TYPICAL CHARACTERISTICS** ($T_A = 25^\circ\text{C}$, unless otherwise noted)

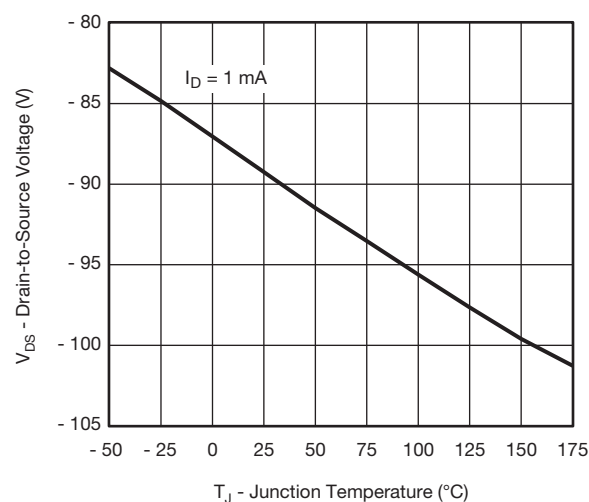
Source-Drain Diode Forward Voltage



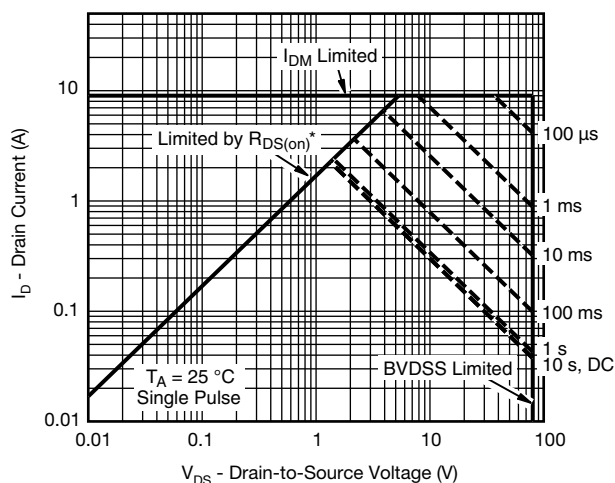
On-Resistance vs. Gate-to-Source Voltage



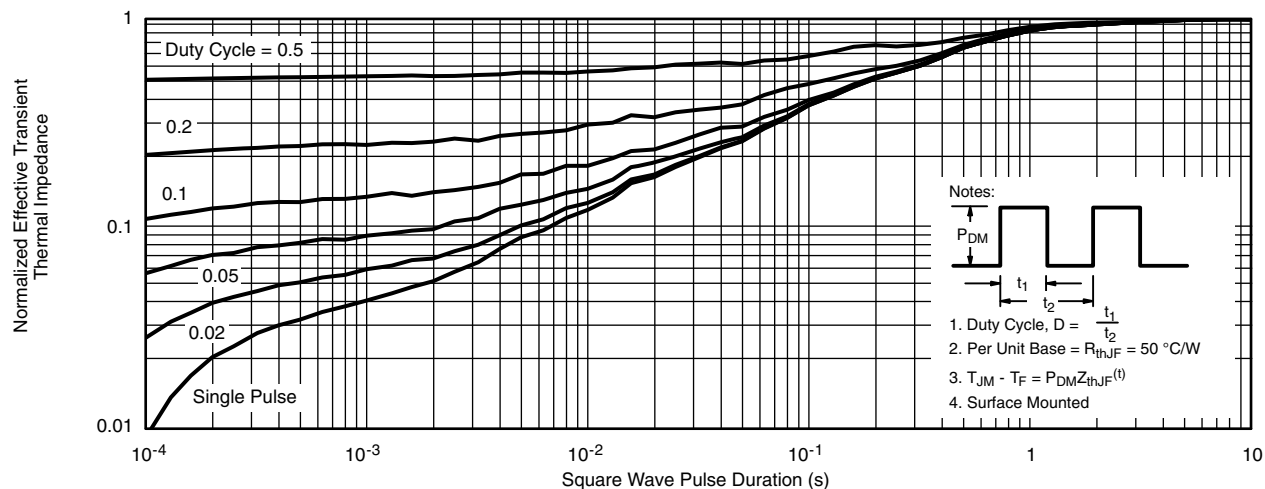
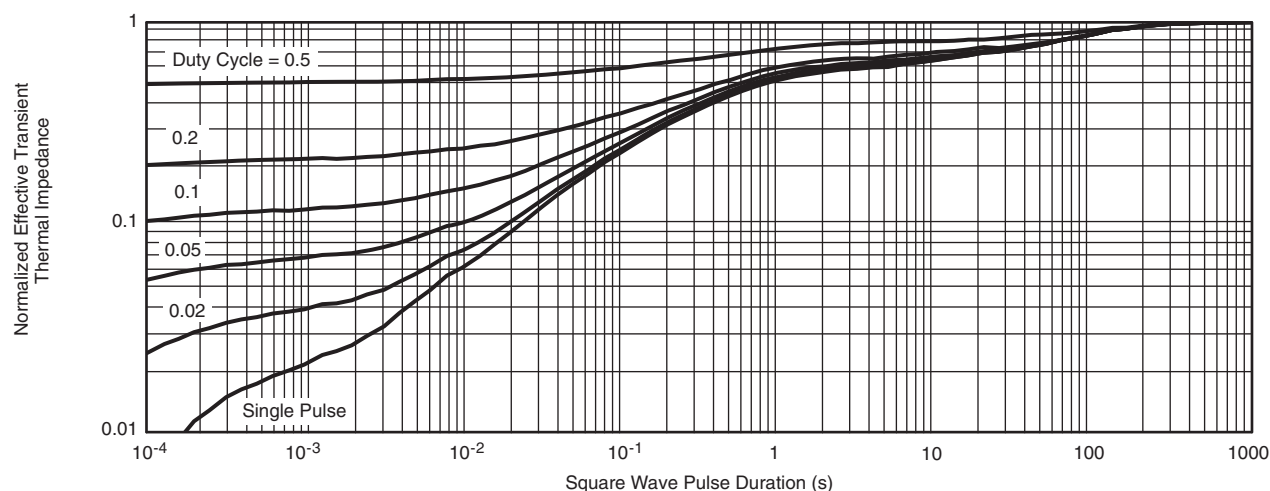
Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Foot****Normalized Thermal Transient Impedance, Junction-to-Ambient****Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25°C)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?266717.

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