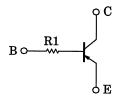
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

## **RN2714**

# Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices incorporated in a USV (5-pin ultra-super-mini-type)
- Built-in bias resistors
- Simplified circuit design
- Reduced quantity of parts and manufacturing process

#### **Equivalent Circuit**

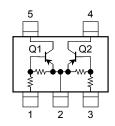


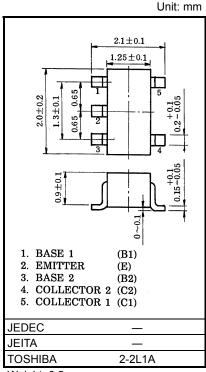
#### Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-5	٧
Collector current	Ic	-100	mA
Collector power dissipation	P <sub>C</sub> *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

<sup>\* :</sup> Total rating

### **Equivalent Circuit (top view)**





Weight: 6.2 mg

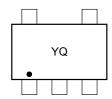
RN2714



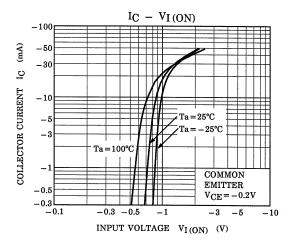
## Electrical Characteristics (Ta = 25°C)

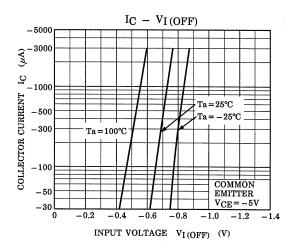
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current	I <sub>CBO</sub>	_	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cutoff current	I <sub>EBO</sub>	_	V <sub>EB</sub> = -5 V, I <sub>C</sub> = 0	_	_	-100	nA
DC current gain	h <sub>FE</sub>	_	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	120	_	400	_
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	_	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	_	$V_{CE} = -0.2 \text{ V}, I_C = -5 \text{ mA}$	-0.5	_	-2.0	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	_	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-0.3	_	-0.9	V
Input resistor	R1	_	_	0.7	1.0	1.3	kΩ
Resistor ratio	R1/R2	_	_	_	0.1		_

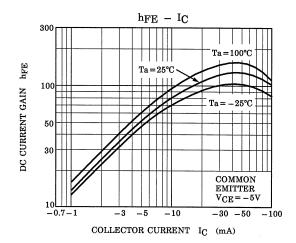
#### Marking

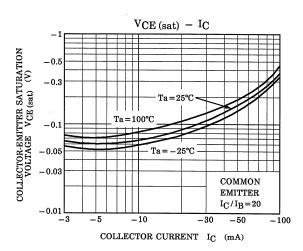


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Handbook" etc..