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Kind regards,

Team Nexperia

# **RB751 series**

# Schottky barrier single diodes

Rev. 01 — 21 May 2007

**Product data sheet** 

### 1. Product profile

### 1.1 General description

Planar Schottky barrier single diodes with an integrated guard ring for stress protection, encapsulated in small Surface-Mounted Device (SMD) plastic packages.

Table 1. Product overview

Type number	Package	Package	
	NXP	JEITA	configuration
RB751CS40	SOD882	-	leadless ultra small
RB751S40	SOD523	SC-79	ultra small
RB751V40	SOD323	SC-76	very small

#### 1.2 Features

- Low forward voltage
- Low capacitance

### 1.3 Applications

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Reverse polarity protection

#### 1.4 Quick reference data

Table 2. Quick reference data

Parameter	Conditions	Min	Тур	Max	Unit
forward current		-	-	120	mA
repetitive peak reverse voltage		-	-	40	V
forward voltage	$I_F = 1 \text{ mA}$	<u>[1]</u> _	-	370	mV
	forward current repetitive peak reverse voltage	forward current repetitive peak reverse voltage	forward current - repetitive peak reverse - voltage	forward current repetitive peak reverse voltage	forward current - 120 repetitive peak reverse - 40 voltage

<sup>[1]</sup> Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



# 2. Pinning information

Table 3. Pinning

Pin	Description	Simplified outline	Symbol
SOD882			
1	cathode	[1]	- 4
2	anode	Transparent top view	1 <del>[4]</del> 2 sym001
SOD323;	SOD523		
1	cathode	[1]	
2	anode	001aab540	1 <del>] 2</del> sym001

<sup>[1]</sup> The marking bar indicates the cathode.

## 3. Ordering information

Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
RB751CS40	-	leadless ultra small plastic package; 2 terminals; body $1.0 \times 0.6 \times 0.5$ mm	SOD882			
RB751S40	SC-79	plastic surface-mounted package; 2 leads	SOD523			
RB751V40	SC-76	plastic surface-mounted package; 2 leads	SOD323			

# 4. Marking

Table 5. Marking codes

Type number	Marking code
RB751CS40	F6
RB751S40	G4
RB751V40	W8

### 5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{RRM}$	repetitive peak reverse voltage		-	40	V
$V_R$	reverse voltage		-	40	V
I <sub>F</sub>	forward current		-	120	mA
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> < 10 ms	-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	<u>[1]</u>		
	RB751CS40		[2] _	250	mW
	RB751S40		[2] -	280	mW
	RB751V40		-	280	mW
Tj	junction temperature		-	150	°C
$T_{amb}$	ambient temperature		-65	+150	°C
$T_{stg}$	storage temperature		-65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u>			
	RB751CS40		[2] _	-	500	K/W
	RB751S40		[2] _	-	450	K/W
	RB751V40		-	-	450	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 7. Characteristics

Table 8. Characteristics

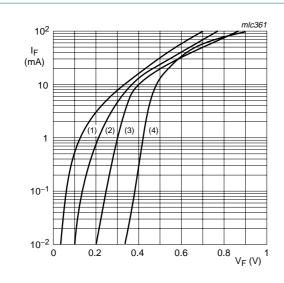
T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{F}$	forward voltage	$I_F = 1 \text{ mA}$	<u>[1]</u> _	-	370	mV
$I_R$	reverse current	$V_R = 30 V$	-	-	0.5	μΑ
$C_d$	diode capacitance	$V_R = 1 V$ ; $f = 1 MHz$	-	2	-	pF

<sup>[1]</sup> Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .

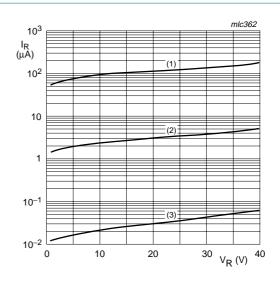
<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

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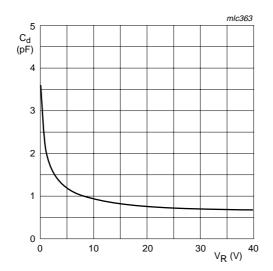
- (1)  $T_{amb} = 125 \, ^{\circ}C$
- (2)  $T_{amb} = 85 \, ^{\circ}C$
- (3)  $T_{amb} = 25 \, ^{\circ}C$
- (4)  $T_{amb} = -40 \, ^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1)  $T_{amb} = 125 \, ^{\circ}C$
- (2)  $T_{amb} = 85 \, ^{\circ}C$
- (3)  $T_{amb} = 25 \, ^{\circ}C$

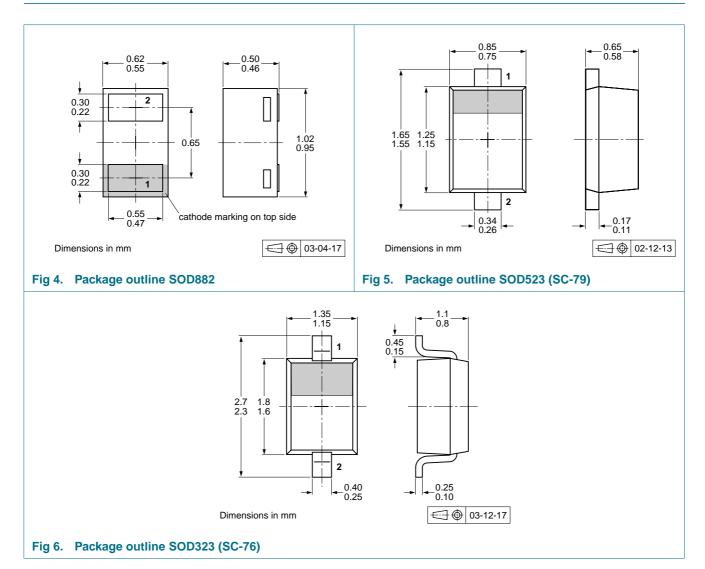
Fig 2. Reverse current as a function of reverse voltage; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \,^{\circ}\text{C}$ 

Fig 3. Diode capacitance as a function of reverse voltage; typical values

## 8. Package outline



# 9. Packing information

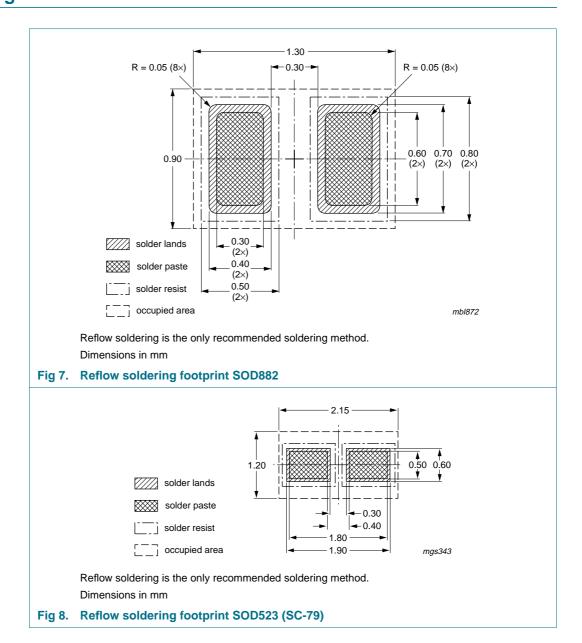
Table 9. Packing methods

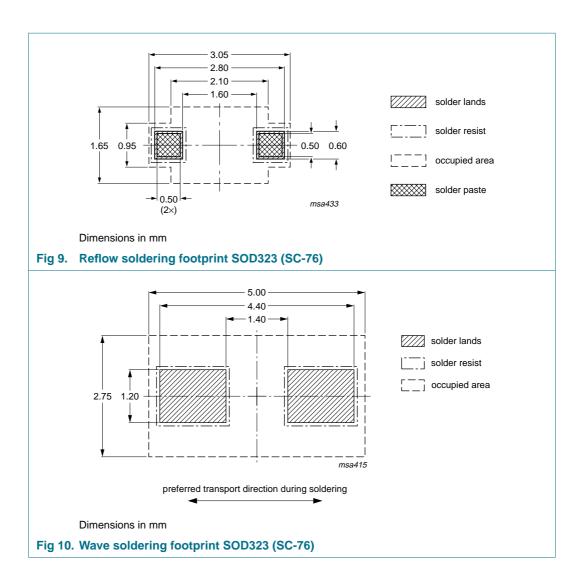
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number Package		Description	Packin	Packing quantity		
			3000	8000	10000	
RB751CS40	SOD882	2 mm pitch, 8 mm tape and reel	-	-	-315	
RB751S40	RB751S40 SOD523	2 mm pitch, 8 mm tape and reel	-	-315	-	
		4 mm pitch, 8 mm tape and reel	-115	-	-135	
RB751V40	SOD323	4 mm pitch, 8 mm tape and reel	-115	-	-135	

<sup>[1]</sup> For further information and the availability of packing methods, see Section 13.

## 10. Soldering





# 11. Revision history

### Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
RB751_SER_1	20070521	Product data sheet	-	-

### 12. Legal information

#### 12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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