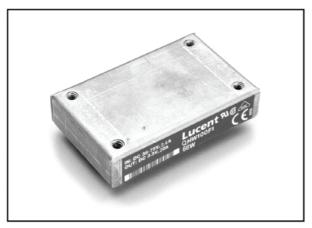


# QHW050F1, QHW075F1, and QHW100F1 Power Modules: dc-dc Converters; 36 to 75 Vdc Input, 3.3 Vdc Output; 33 W to 66 W



The QHW Series Power Modules use advanced, surfacemount technology and deliver high-quality, efficient, and compact dc-dc conversion.

## **Applications**

- Distributed power architectures
- Communications equipment
- Computer equipment

## **Options**

- Heat sinks available for extended operation
- Auto-restart after overcurrent shutdown
- Case ground pin

#### **Features**

- Small size: 36.8 mm x 57.9 mm x 12.7 mm (1.45 in. x 2.28 in. x 0.50 in.)
- High power density
- Extra high efficiency: 85% typical
- Low output noise
- Constant frequency
- Industry-standard pinout
- Metal baseplate
- 2:1 input voltage range
- Overvoltage and overcurrent protection
- Remote on/off
- Remote sense
- Adjustable output voltage
- Overtemperature protection
- ISO\* 9001 Certified manufacturing facilities
- UL<sup>†</sup>1950 Recognized, CSA<sup>‡</sup> C22.2 No. 950-95 Certified, and VDE<sup>§</sup> 0805 (EN60950, IEC950) Licensed
- CE mark meets 73/23/EEC and 93/68/EEC directives\*\*
- \* ISO is a registered trademark of the International Organization for Standardization.
- † UL is a registered trademark of Underwriters Laboratories, Inc.
- ‡ CSA is a registered trademark of Canadian Standards Assn.
- § VDE is a trademark of Verband Deutscher Elektrotechniker e.V.
  \*\* This product is intended for integration into end-use equipment.
  All the required procedures for CE marking of end-use equipment should be followed. (The CE mark is placed on selected

#### Description

The QHW050F1, QHW075F1, and QHW100F1 Power Modules are dc-dc converters that operate over an input voltage range of 36 Vdc to 75 Vdc and provide a precisely regulated dc output. The outputs are fully isolated from the inputs, allowing versatile polarity configurations and grounding connections. The modules have maximum power ratings from 33 W to 66 W at a typical full-load efficiency of 85%.

products.)

The sealed modules offer a metal baseplate for excellent thermal performance. Threaded-through holes are provided to allow easy mounting or addition of a heat sink for high-temperature applications. The standard feature set includes remote sensing, output trim, and remote on/off for convenient flexibility in distributed power applications.

## **Absolute Maximum Ratings**

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage:				
Continuous	Vı	_	75	Vdc
Transient (100 ms)	VI, trans	_	100	V
Operating Case Temperature (See Thermal Considerations section; see Figure 26.)	Тс	-40	100	°C
Storage Temperature	Tstg	<b>-</b> 55	125	°C
I/O Isolation Voltage (for 1 minute)	_	_	1500	Vdc

## **Electrical Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

**Table 1. Input Specifications** 

Parameter	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	Vı	36	48	75	Vdc
Maximum Input Current (VI = 0 V to 75 V; Io = Io, max; see Figures 1—3):					
QHW050F1	II, max		_	2.5	Α
QHW075F1	II, max			3.5	Α
QHW100F1	II, max	_	_	4.5	Α
Inrush Transient	i <sup>2</sup> t	_	_	1.0	A <sup>2</sup> s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 µH source impedance; see Figure 17.)	lı	_	10	_	mAp-p
Input Ripple Rejection (120 Hz)	_		60	_	dB

#### **Fusing Considerations**

#### CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow fuse with a maximum rating of 20 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

## **Electrical Specifications** (continued)

**Table 2. Output Specifications** 

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Voltage Set Point	All	Vo, set	3.24	3.3	3.36	Vdc
(VI = 48 V; IO = IO, max; TC = 25 °C)						
Output Voltage	All	Vo	3.2	_	3.4	Vdc
(Over all operating input voltage, resistive						
load, and temperature conditions until end of						
life. See Figure 19.)						
Output Regulation:				0.04		000
Line (V <sub>I</sub> = 36 V to 75 V)	All	_	_	0.01	0.1	%Vo
Load (Io = Io, min to Io, max)	All		_	0.05	0.2	%Vo
Temperature (Tc = -40 °C to +100 °C)	All	_	_	15	50	mV
Output Ripple and Noise Voltage						
(See Figure 18.): RMS	All				40	mVrms
Peak-to-peak (5 Hz to 20 MHz)	All				150	mVp-p
	All		0		*	
External Load Capacitance		_		_		μF
Output Current	QHW050F1	lo	0.5	_	10	A
(At Io < Io, min, the modules may exceed output	QHW075F1	lo	0.5	_	15	A
ripple specifications.)	QHW100F1	lo	0.5		20	A
Output Current-limit Inception	QHW050F1	IO, cli	_	15	20 <sup>†</sup>	A
(Vo = 90% of Vo, nom)	QHW075F1	IO, cli	_	20	26 <sup>†</sup> 32 <sup>†</sup>	A
	QHW100F1	IO, cli	_	25	321	A
Efficiency (V <sub>I</sub> = 48 V; Io = Io, max; Tc = 70 °C)	QHW050F1	η	_	85	_	%
	QHW075F1	η	_	85	_	% %
0 % 11 5	QHW100F1	η	_	84	_	
Switching Frequency	All	_	_	380	_	kHz
Dynamic Response						
$(\Delta Io/\Delta t = 1 \text{ A}/10 \text{ µs}, V_1 = 48 \text{ V}, T_c = 25 \text{ °C};$						
tested with a 220 μF aluminum and a 1.0 μF						
ceramic capacitor across the load):						
Load Change from Io = 50% to 75% of Io, max: Peak Deviation	All			5		%Vo, set
Settling Time (Vo < 10% of peak deviation)	All			400		μs
Load Change from Io = 50% to 25% of Io, max:	į – Alii	_	_	700		μo
Peak Deviation	All	_	_	5	_	%Vo, set
Settling Time (Vo < 10% of peak deviation)	All	_	_	400	_	μs
* Consult your sales representative or the factory	L	l	l	l	l	

#### **Table 3. Isolation Specifications**

Parameter	Min	Тур	Max	Unit
Isolation Capacitance	_	2500	_	pF
Isolation Resistance	10	_	_	MΩ

<sup>\*</sup> Consult your sales representative or the factory.
† These are manufacturing test limits. In some situations, results may differ.

## **General Specifications**

Parameter	Min	Тур	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C):				
QHW050F1		3,000,000		hours
QHW075F1		2,500,000		hours
QHW100F1		2,000,000		hours
Weight	_	_	75 (2.7)	g (oz.)

## **Feature Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Symbol	Min	Тур	Max	Unit
Remote On/Off Signal Interface					
(V <sub>I</sub> = 0 V to 75 V; open collector or equivalent compatible;					
signal referenced to V <sub>I</sub> (–) terminal; see Figure 20 and					
Feature Descriptions.):					
Logic Low—Module On					
Logic High—Module Off					
Logic Low:					
At Ion/off = 1.0 mA	Von/off	0	_	1.2	V
At $Von/off = 0.0 V$	Ion/off	_	_	1.0	mA
Logic High:					
At $I_{on/off} = 0.0 \mu A$	Von/off	_	_	15	V
Leakage Current	Ion/off	_	_	50	μA
Turn-on Time (See Figure 16.)	_	_	20	35	ms
(Io = 80% of Io, max; Vo within $\pm 1\%$ of steady state)					
Output Voltage Adjustment (See Feature Descriptions.):					
Output Voltage Remote-sense Range	_	_	<b>—</b>	0.5	V
Output Voltage Set-point Adjustment Range (trim)	_	95		110	%VO, nom
Output Overvoltage Protection	VO, sd	3.8*	_	4.5*	V
Overtemperature Protection	Tc	_	105	_	°C

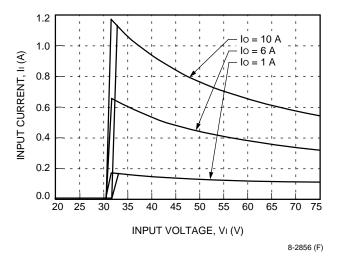
<sup>\*</sup> These are manufacturing test limits. In some situations, results may differ.

## Solder, Cleaning, and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical testing. The result of inadequate circuit-board cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning, and drying procedures, refer to Tyco Electronics *Board-Mounted Power Modules Soldering and Cleaning* Application Note (AP97-021EPS).

#### **Characteristic Curves**

The following figures provide typical characteristics for the power modules. The figures are identical for both on/off configurations.



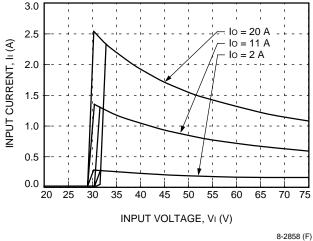
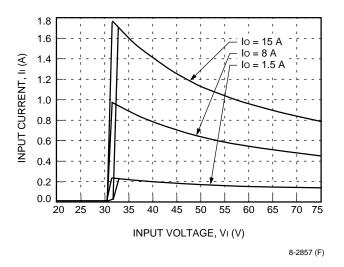


Figure 1. Typical QHW050F1 Input Characteristics at Room Temperature

Figure 3. Typical QHW100F1 Input Characteristics at Room Temperature



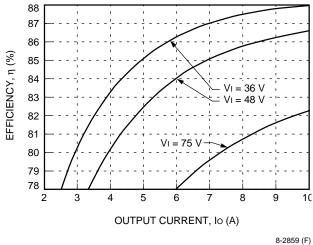


Figure 2. Typical QHW075F1 Input Characteristics at Room Temperature

Figure 4. Typical QHW050F1 Converter Efficiency vs. Output Current at Room Temperature

## Characteristic Curves (continued)

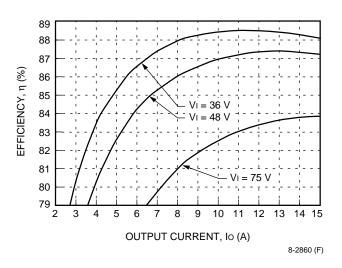


Figure 5. Typical QHW075F1 Converter Efficiency vs. Output Current at Room Temperature

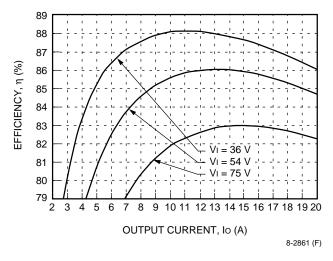
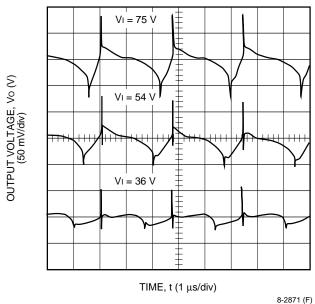
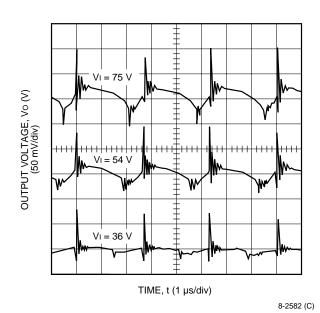


Figure 6. Typical QHW100F1 Converter Efficiency vs. Output Current at Room Temperature



Note: See Figure 18 for test conditions.

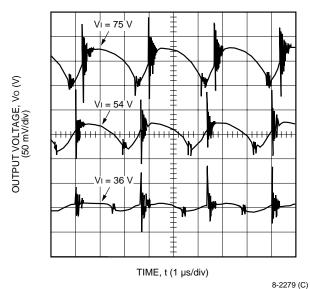
Figure 7. Typical QHW050F1 Output Ripple Voltage at Room Temperature; Io = Io, max



Note: See Figure 18 for test conditions.

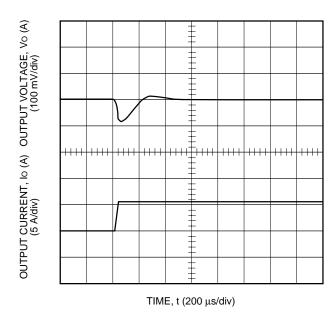
Figure 8. Typical QHW075F1 Output Ripple Voltage at Room Temperature; Io = Io, max

### Characteristic Curves (continued)



Note: See Figure 18 for test conditions.

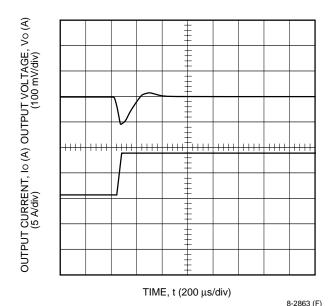
Figure 9. Typical QHW100F1 Output Ripple Voltage at Room Temperature; Io = Io, max



8-2862 (F) Note: Tested with a 220 µF aluminum and a 1.0 µF ceramic capacitor

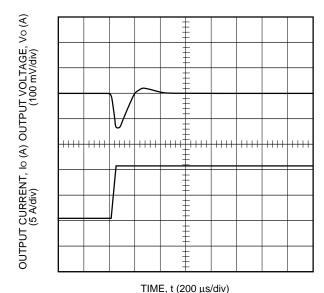
across the load.

Figure 10. Typical QHW050F1 Transient Response to Step Increase in Load from 50% to 75% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)



Note: Tested with a 220 µF aluminum and a 1.0 µF ceramic capacitor

Figure 11. Typical QHW075F1 Transient Response to Step Increase in Load from 50% to 75% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)

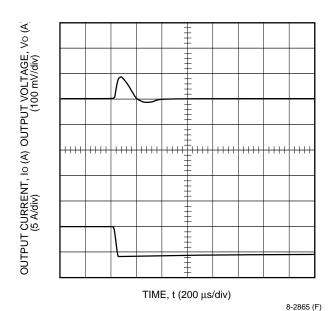


Note: Tested with a 220  $\mu F$  aluminum and a 1.0  $\mu F$  ceramic capacitor across the load.

8-2864 (F)

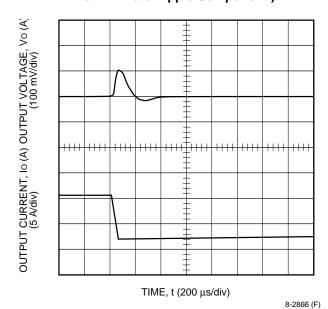
Figure 12. Typical QHW100F1 Transient Response to Step Increase in Load from 50% to 75% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)

#### Characteristic Curves (continued)



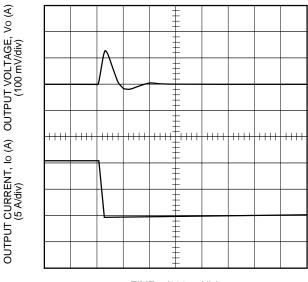
Note: Tested with a 220  $\mu F$  aluminum and a 1.0  $\mu F$  ceramic capacitor across the load.

Figure 13. Typical QHW050F1 Transient Response to Step Decrease in Load from 50% to 25% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)



Note: Tested with a 220  $\mu F$  aluminum and a 1.0  $\mu F$  ceramic capacitor across the load.

Figure 14. Typical QHW075F1 Transient Response to Step Decrease in Load from 50% to 25% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)

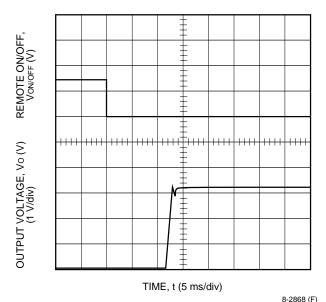


TIME, t (200 µs/div)

8-2867 (F)

Note: Tested with a 220  $\mu F$  aluminum and a 1.0  $\mu F$  ceramic capacitor across the load.

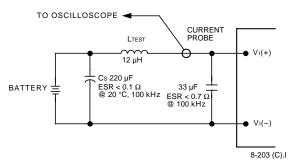
Figure 15. Typical QHW100F1 Transient Response to Step Decrease in Load from 50% to 25% of Full Load at Room Temperature and 54 Vdc Input. (Waveform Averaged to Eliminate Ripple Component.)



Note: Tested with a 220  $\mu F$  aluminum and a 1.0  $\mu F$  ceramic capacitor across the load.

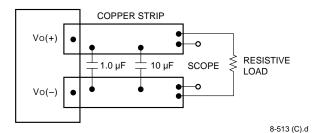
Figure 16. Typical Start-Up from Remote On/Off; lo = lo. max

## **Test Configurations**



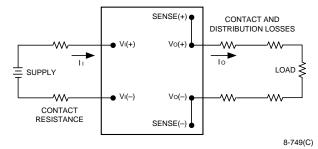
Note: Measure input reflected-ripple current with a simulated source inductance (LTEST) of 12 µH. Capacitor Cs offsets possible battery impedance. Measure current as shown above.

Figure 17. Input Reflected-Ripple Test Setup



Note: Use a 1.0  $\mu$ F ceramic capacitor and a 10  $\mu$ F aluminum or tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load between 51 mm and 76 mm (2 in. and 3 in.) from the module.

Figure 18. Peak-to-Peak Output Noise Measurement Test Setup



Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \left(\frac{[Vo(+) - Vo(-)]Io}{[VI(+) - VI(-)]II}\right) \times 100$$
 %

Figure 19. Output Voltage and Efficiency Measurement Test Setup

## **Design Considerations**

#### **Input Source Impedance**

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. For the test configuration in Figure 17, a 33  $\mu\text{F}$  electrolytic capacitor (ESR < 0.7  $\Omega$  at 100 kHz) mounted close to the power module helps ensure stability of the unit. For other highly inductive source impedances, consult the factory for further application quidelines.

## **Safety Considerations**

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL*1950, *CSA* C22.2 No. 950-95, and *VDE* 0805 (EN60950, IEC950).

If the input source is non-SELV (ELV or a hazardous voltage greater than 60 Vdc and less than or equal to 75 Vdc), for the module's output to be considered meeting the requirements of safety extra-low voltage (SELV), all of the following must be true:

- The input source are to be provided with reinforced insulation from any hazardous voltages, including the ac mains.
- One VI pin and one Vo pin are to be grounded or both the input and output pins are to be kept floating.
- The input pins of the module are not operator accessible.
- Another SELV reliability test is conducted on the whole system, as required by the safety agencies, on the combination of supply source and the subject module to verify that under a single fault, hazardous voltages do not appear at the module's output.

**Note:** Do not ground either of the input pins of the module without grounding one of the output pins. This may allows a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a maximum 20 A normal-blow fuse in the ungrounded lead.

## **Feature Descriptions**

#### **Overcurrent Protection**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting for up to one second. If overcurrent exists for more than one second, the unit will shut down.

At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase).

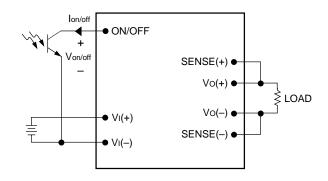
The module is available in two overcurrent configurations. In one configuration, when the unit shuts down it will latch off. The overcurrent latch is reset by either cycling the input power or by toggling the ON/OFF pin for one second. In the other configuration, the unit will try to restart after shutdown. If the output overload condition still exists when the unit restarts, it will shut down again. This operation will continue indefinitely until the overcurrent condition is corrected.

#### Remote On/Off

Negative logic remote on/off turns the module off during a logic high and on during a logic low. To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the V<sub>I</sub>(–) terminal (Von/off). The switch can be an open collector or equivalent (see Figure 20). A logic low is Von/off = 0 V to 1.2 V. The maximum lon/off during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum Von/off generated by the power module is 15 V. The maximum allowable leakage current of the switch at Von/off = 15 V is 50  $\mu$ A.

If not using the remote on/off feature, short the ON/OFF pin to  $V_1(-)$ .



8-720 (C).c

Figure 20. Remote On/Off Implementation

#### Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections. The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table, i.e.:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 0.5 \text{ V}$$

The voltage between the Vo(+) and Vo(-) terminals must not exceed the minimum output overvoltage protection value shown in the Feature Specifications table. This limit includes any increase in voltage due to remote-sense compensation and output voltage setpoint adjustment (trim). See Figure 21.

If not using the remote-sense feature to regulate the output at the point of load, then connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

#### Feature Descriptions (continued)

#### Remote Sense (continued)

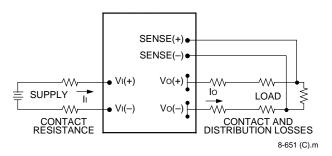


Figure 21. Effective Circuit Configuration for Single-Module Remote-Sense Operation

#### **Output Voltage Set-Point Adjustment (Trim)**

Output voltage trim allows the user to increase or decrease the output voltage set point of a module. This is accomplished by connecting an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-) pins. The trim resistor should be positioned close to the module.

If not using the trim feature, leave the TRIM pin open.

With an external resistor between the TRIM and SENSE(–) pins ( $R_{adj-down}$ ), the output voltage set point (Vo, adj) decreases (see Figure 22). The following equation determines the required external-resistor value to obtain a percentage output voltage change of  $\Delta\%$ .

$$R_{\text{adj-down}} \, = \left(\frac{510}{\Delta\%} - 10.2\right) \ k\Omega$$

The test results for this configuration are displayed in Figure 23. This figure applies to all output voltages.

With an external resistor connected between the TRIM and SENSE(+) pins (Radj-up), the output voltage set point (Vo, adj) increases (see Figure 24).

The following equation determines the required external-resistor value to obtain a percentage output voltage change of  $\Delta\%$ .

$$\mbox{Radj-up} \; = \left( \frac{5.1 \mbox{V}_{O}(100 + \Delta\%)}{1.225 \Delta\%} - \frac{510}{\Delta\%} - 10.2 \right) \; \; k\Omega \label{eq:Radj-up}$$

The test results for this configuration are displayed in Figure 25.

The voltage between the Vo(+) and Vo(-) terminals must not exceed the minimum output overvoltage protection value shown in the Feature Specifications table. This limit includes any increase in voltage due to

remote-sense compensation and output voltage setpoint adjustment (trim). See Figure 21.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

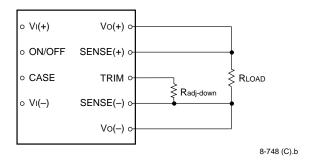


Figure 22. Circuit Configuration to Decrease Output Voltage

## Feature Descriptions (continued)

## Output Voltage Set-Point Adjustment (Trim) (continued)

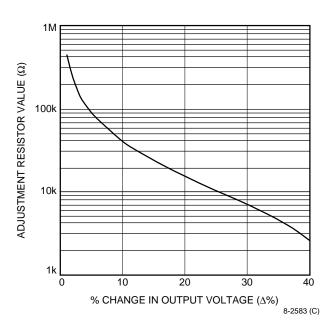


Figure 23. Resistor Selection for Decreased Output Voltage

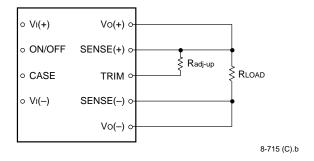


Figure 24. Circuit Configuration to Increase Output Voltage

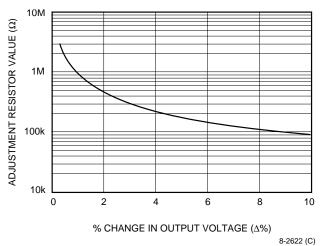


Figure 25. Resistor Selection for Increased Output Voltage

#### **Output Overvoltage Protection**

The output overvoltage protection consists of circuitry that monitors the voltage on the output terminals. If the voltage on the output terminals exceeds the overvoltage protection threshold, then the module will shut down and latch off. The overvoltage latch is reset by either cycling the input power for 1.0 second or by toggling the on/off signal for 1.0 second.

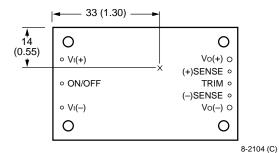
#### **Overtemperature Protection**

These modules feature an overtemperature protection circuit to safeguard against thermal damage. The circuit shuts down and latches off the module when the maximum case temperature is exceeded. The module can be restarted by cycling the dc input power for at least 1.0 second or by toggling the primary or secondary referenced remote on/off signal for at least 1.0 second.

#### **Thermal Considerations**

#### Introduction

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the unit are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment. Proper cooling can be verified by measuring the case temperature. Peak temperature (Tc) occurs at the position indicated in Figure 26.



Note: Top view, pin locations are for reference only.

Measurements shown in millimeters and (inches).

Figure 26. Case Temperature Measurement Location

The temperature at this location should not exceed 100 °C. The output power of the module should not exceed the rated power for the module as listed in the Ordering Information table.

Although the maximum case temperature of the power modules is 100 °C, you can limit this temperature to a lower value for extremely high reliability.

#### **Heat Transfer Without Heat Sinks**

Increasing airflow over the module enhances the heat transfer via convection. Figure 27 shows the maximum power that can be dissipated by the module without exceeding the maximum case temperature versus local ambient temperature (TA) for natural convection through 3 m/s (600 ft./min.).

Note that the natural convection condition was measured at 0.05 m/s to 0.1 m/s (10 ft./min. to 20 ft./min.); however, systems in which these power modules may be used typically generate natural convection airflow rates of 0.3 m/s (60 ft./min.) due to other heat dissipating components in the system. The use of Figure 27 is shown in the following example.

#### **Example**

What is the minimum airflow necessary for a QHW100F1 operating at  $V_I = 54$  V, an output current of 15 A, transverse orientation, and a maximum ambient temperature of 40 °C?

#### Solution

Given:  $V_1 = 54 \text{ V}$   $I_0 = 15 \text{ A}$  $T_A = 40 \text{ °C}$ 

Determine PD (Use Figure 31):

 $P_{D} = 8.0 \text{ W}$ 

Determine airflow (v) (Use Figure 27):

v = 0.5 m/s (100 ft./min.)

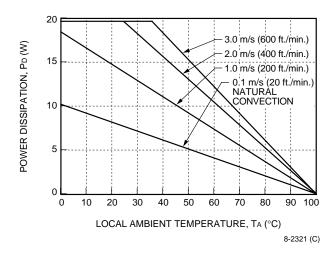


Figure 27. Forced Convection Power Derating with No Heat Sink; Transverse Orientation

#### **Heat Transfer Without Heat Sinks** (continued)

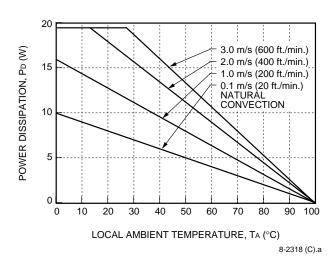


Figure 28. Forced Convection Power Derating with No Heat Sink; Longitudinal Orientation

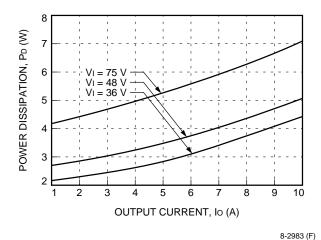


Figure 29. QHW050F1 Power Dissipation vs. Output Current at 25 °C

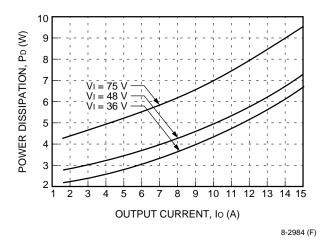


Figure 30. QHW075F1 Power Dissipation vs. Output Current at 25 °C

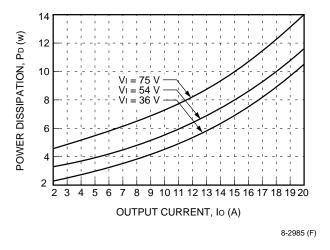


Figure 31. QHW100F1 Power Dissipation vs. Output Current at 25 °C

#### **Heat Transfer with Heat Sinks**

The power modules have through-threaded, M3 x 0.5 mounting holes, which enable heat sinks or cold plates to attach to the module. The mounting torque must not exceed 0.56 N-m (5 in.-lb.). For a screw attachment from the pin side, the recommended hole size on the customer's PWB around the mounting holes is 0.130  $\pm$  0.005 inches. If a larger hole is used, the mounting torque from the pin side must not exceed 0.25 N-m (2.2 in.-lbs.).

#### Heat Transfer with Heat Sinks (continued)

Thermal derating with heat sinks is expressed by using the overall thermal resistance of the module. Total module thermal resistance ( $\theta$ ca) is defined as the maximum case temperature rise ( $\Delta$ Tc,  $_{max}$ ) divided by the module power dissipation (PD):

$$\theta \text{ca} \, = \, \left[ \frac{\Delta T \text{c, max}}{P_D} \right] \, = \, \left[ \frac{(T \text{c} - T \text{A})}{P_D} \right]$$

The location to measure case temperature (Tc) is shown in Figure 26. Case-to-ambient thermal resistance vs. airflow is shown, for various heat sink configurations, heights, and orientations, as shown in Figures 32 and 33. Longitudinal orientation is defined as when the long axis of the module is parallel to the airflow direction, whereas in the transverse orientation, the long axis is perpendicular to the airflow. These curves were obtained by experimental testing of heat sinks, which are offered in the product catalog.

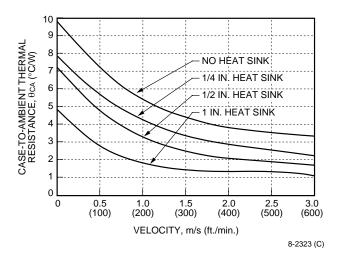


Figure 32. Case-to-Ambient Thermal Resistance Curves; Transverse Orientation

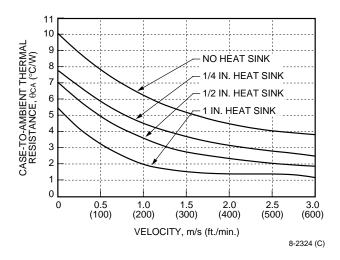


Figure 33. Case-to-Ambient Thermal Resistance Curves; Longitudinal Orientation

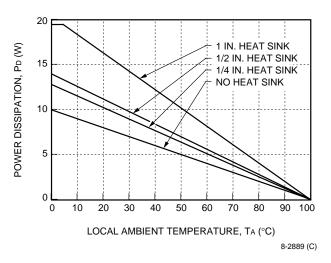


Figure 34. Heat Sink Power Derating Curves; Natural Convection; Transverse Orientation

#### Heat Transfer with Heat Sinks (continued)

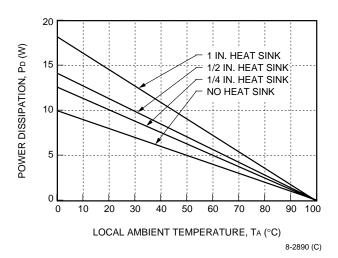


Figure 35. Heat Sink Power Derating Curves; Natural Convection; Longitudinal Orientation

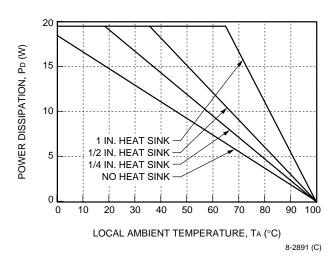


Figure 36. Heat Sink Power Derating Curves; 1.0 m/s (200 lfm); Transverse Orientation

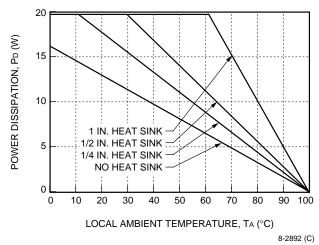


Figure 37. Heat Sink Power Derating Curves; 1.0 m/s (200 lfm); Longitudinal Orientation

These measured resistances are from heat transfer from the sides and bottom of the module as well as the top side with the attached heat sink; therefore, the case-to-ambient thermal resistances shown are generally lower than the resistance of the heat sink by itself. The module used to collect the data in Figures 32 and 33 had a thermal-conductive dry pad between the case and the heat sink to minimize contact resistance. The use of Figures 32 and 33 is shown in the following example.

#### Example

If an 85 °C case temperature is desired, what is the minimum airflow necessary? Assume the QHW100F1 module is operating at  $V_{\rm I}=54$  V and an output current of 20 A, maximum ambient air temperature of 40 °C, and the heat sink is 1/2 inch. The module is oriented in the transverse direction.

#### Heat Transfer with Heat Sinks (continued)

#### **Solution**

Given: 
$$V_1 = 54 \text{ V}$$
  
 $I_0 = 20 \text{ A}$   
 $T_A = 40 \text{ °C}$   
 $T_C = 85 \text{ °C}$   
Heat sink = 1/2 inch

Determine PD by using Figure 31:

$$P_D = 11.0 W$$

Then solve the following equation:

$$\theta ca = \left[\frac{(Tc - TA)}{PD}\right]$$

$$\theta ca = \left[\frac{(85 - 40)}{11.0}\right]$$

$$\theta$$
ca = 4.1 °C/W

Use Figure 32 to determine air velocity for the 1/2 inch heat sink.

The minimum airflow necessary for this module is 0.7 m/s (140 ft./min.).

#### **Custom Heat Sinks**

A more detailed model can be used to determine the required thermal resistance of a heat sink to provide necessary cooling. The total module resistance can be separated into a resistance from case-to-sink ( $\theta$ cs) and sink-to-ambient ( $\theta$ sa) as shown in Figure 38.

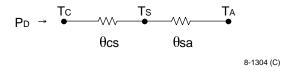


Figure 38. Resistance from Case-to-Sink and Sink-to-Ambient

For a managed interface using thermal grease or foils, a value of  $\theta$ cs = 0.1 °C/W to 0.3 °C/W is typical. The solution for heat sink resistance is:

$$\theta sa = \left[\frac{(Tc - TA)}{PD}\right] - \theta cs$$

This equation assumes that all dissipated power must be shed by the heat sink. Depending on the userdefined application environment, a more accurate model, including heat transfer from the sides and bottom of the module, can be used. This equation provides a conservative estimate for such instances.

#### **EMC Considerations**

For assistance with designing for EMC compliance, please refer to the FLTR100V10 data sheet (DS99-294EPS).

## **Layout Considerations**

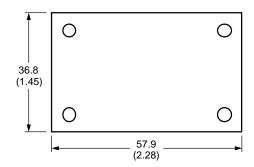
Copper paths must not be routed beneath the power module mounting inserts. For additional layout guidelines, refer to the FLTR100V10 data sheet (DS99-294EPS).

## **Outline Diagram**

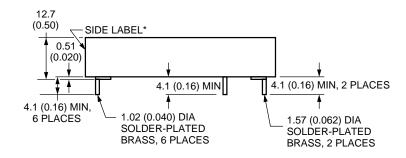
Dimensions are in millimeters and (inches).

Tolerances:  $x.x \text{ mm} \pm 0.5 \text{ mm} (x.xx \text{ in.} \pm 0.02 \text{ in.})$  $x.xx \text{ mm} \pm 0.25 \text{ mm} (x.xxx \text{ in.} \pm 0.010 \text{ in.})$ 

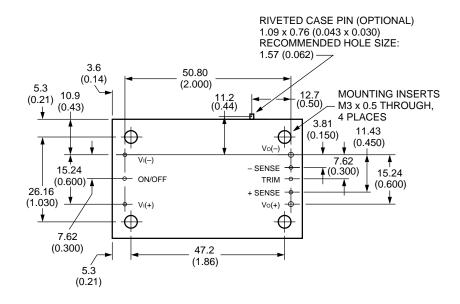
#### **Top View**



#### Side View



#### **Bottom View**



8-1769 (F).b

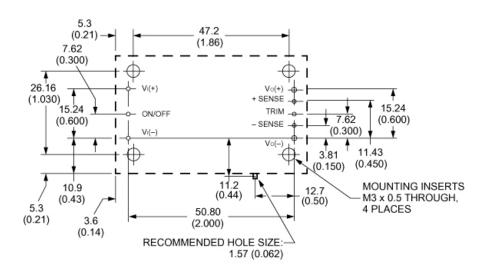
voltage and current ratings, and bar code.

 $<sup>^{\</sup>star}$  Side label includes Tyco logo, product designation, safety agency making put/output

#### Recommended Hole Pattern

Component-side footprint.

Dimensions are in millimeters and (inches).



8-1769 (F).b

## **Ordering Information**

Table 4. Device Codes

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Comcode
48 V	3.3 V	33 W	Negative	QHW050F1	108561234
48 V	3.3 V	49.5 W	Negative	QHW075F1	108219700
48 V	3.3 V	66W	Negative	QHW100F1	108152141

Optional features can be ordered using the suffixes shown in Table 5. The suffixes follow the last letter of the device code and are placed in descending order. For example, the device codes for a QHW100F1 module with the following options are shown below:

Auto-restart after overcurrent shutdown

QHW100F41

Table 5. Device Options

Option	Device Code Suffix
Short pins: 2.79 mm ± 0.25 mm (0.110 in. +0.020 in./–0.010 in.)	8
Case ground pin	7
Short pins: 3.68 mm ± 0.25 mm (0.145 in. ± 0.010 in.)	6
Auto-restart after overtemperature, over- voltage, or overcurrent shutdown	4



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