

24-Bit Bus Exchange Switch

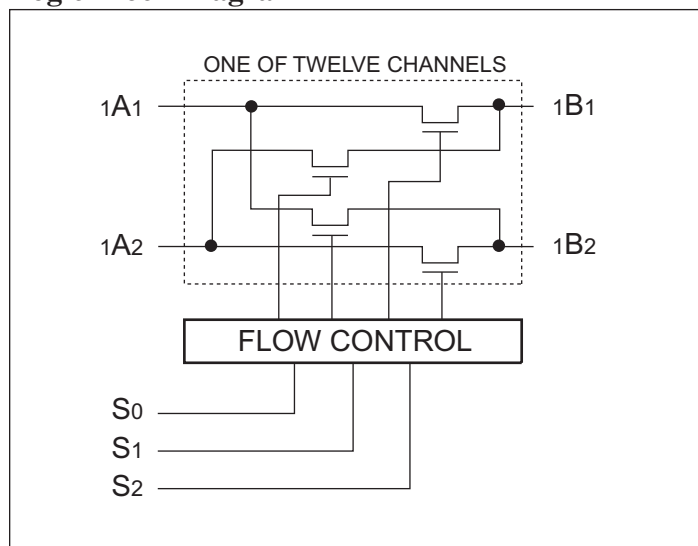
Features

- Near-Zero propagation delay
- 5-ohm switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.1µA typical)
 - Ideally suited for notebook applications
- Industrial operating temperature: –40°C to +85°C
- Packaging (Pb-free & Green Available):
 - 56-pin 240-mil wide thin plastic TSSOP (A)

Description

Pericom Semiconductor's PI5C16212 is a 24-bit bus exchange switch designed with Low On-Resistance allowing inputs to be connected directly to outputs. This device operates as a 24-bit bus switch or a 12-bit exchanger that provides data exchanging between the four signal ports via the data select pins (S0-S2).

Logic Block Diagram



Truth Table⁽¹⁾

Function	S2	S1	S0	A1	A2
Disconnect	L	L	L	Z	Z
A1 to B1	L	L	H	B1	Z
A1 to B2	L	H	L	B2	Z
A2 to B1	L	H	H	Z	B1
A2 to B2	H	L	L	Z	B2
Disconnect	H	L	H	Z	Z
A1 to B1, A2 to B2	H	H	L	B1	B2
A1 to B2, A2 to B1	H	H	H	B2	B1

Note:

1. H = High Voltage Level, L = Low Voltage Level, Z = High Impedance

Pin Configuration

S0	1	56	S1
1A1	2	55	S2
1A2	3	54	1B1
2A1	4	53	1B2
2A2	5	52	2B1
3A1	6	51	2B2
3A2	7	50	3B1
GND	8	49	GND
4A1	9	48	3B2
4A2	10	47	4B1
5A1	11	46	4B2
5A2	12	45	5B1
6A1	13	44	5B2
6A2	14	43	6B1
7A1	15	42	6B2
7A2	16	41	7B1
VCC	17	40	7B2
8A1	18	39	8B1
GND	19	38	GND
8A2	20	37	8B2
9A1	21	36	9B1
9A2	22	35	9B2
10A1	23	34	10B1
10A2	24	33	10B2
11A1	25	32	11B1
11A2	26	31	11B2
12A1	27	30	12B1
12A2	28	29	12B2

Pin Description

Pin Name	I/O	Description
S0 - S2	I	Select Inputs
xAx	I/O	Bus A
xBx	I/O	Bus B

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential.....	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current.....	120mA
Power Dissipation	1W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Condition

Parameter	Description	Min.	Max.	Units
V _{CC}	Supply Voltage	4	5.5	V
V _{IH}	High-Level Input Voltage	2		
V _{IL}	Low-Level Input Voltage		0.8	
T _A	Operating Free-Air Temperature	–40	85	°C

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±10%)

Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I _I	Input Current	V _{CC} = Max, V _{IN} = V _{CC} or GND			±1	μA
		V _{CC} = 0, V _{IN} = V _{CC}			10	
I _{OZ}	High Impedance Output Current	0 – A, B – V _{CC}			±1	
V _{IK}	Clamp Diode Voltage	V _{CC} = Min, I _{IN} = –18mA		–0.7	–1.2	V
I _{OS}	Short Circuit Current ⁽³⁾	A (B) = 0V, B (A) = V _{CC}	100			mA
V _H	Input Hysteresis at Control Pins			150		mV
R _{ON}	Switch On Resistance ⁽⁴⁾	V _{CC} = Min, V _{IN} = 0.0V, I _{ON} = 64mA			7	Ω
		V _{CC} = Min, V _{IN} = 2.4V, I _{ON} = 15mA			12	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{MHz}$)

Parameters ⁽¹⁾	Description	Test Conditions	Min.	Max.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3	6	pF
C_{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0\text{V}$	6	14	
C_{ON}	A/B Capacitance, Switch On	$V_{IN} = 0\text{V}$	12	30	

Note:

1. The parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameter	Description	Test Conditions		Min.	Typ ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$		0.1	3.0	μA
ΔI_{CC} ⁽³⁾	Supply Current per Input @ TLL HIGH	$V_{CC} = 5.5\text{V}$	$V_{IN} = 3.4\text{V}^{(4)}$			2.5	mA
I_{CCD}	Supply Current per Input per MHz ⁽⁵⁾ $S_N = \text{GND}$, Control Input Toggling 50% Duty Cycle	$V_{CC} = \text{Max.}$ A & B Pins Open				0.25	mA/ MHz

Notes:

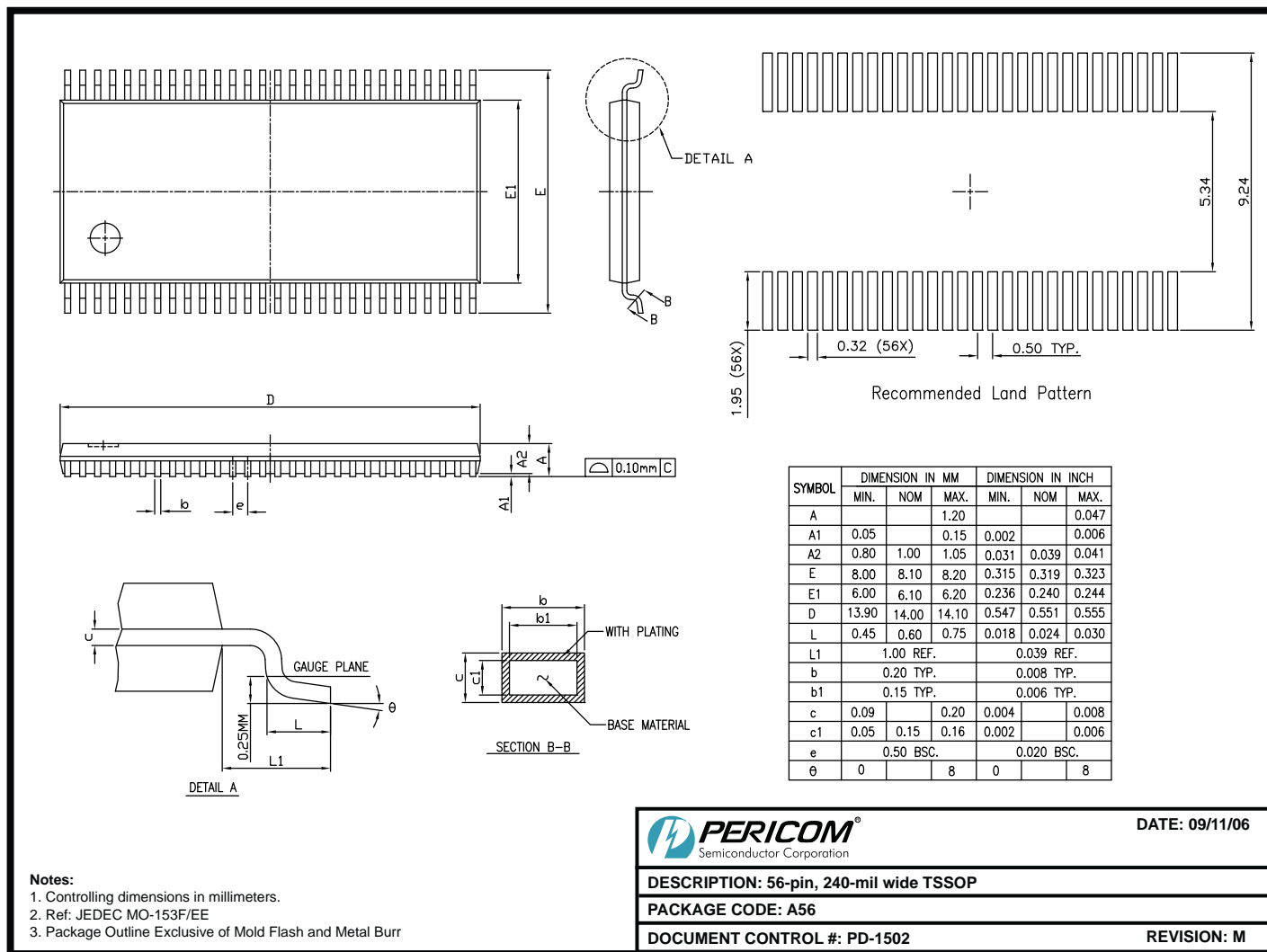
- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for applicable device.
- Typical values are at $V_{CC} = 5.0\text{V}$, $+25^\circ\text{C}$ ambient.
- This is the increase in supply current for each input that is at the specified TTL voltage level rather than V_{CC} or GND.
- Per TTL driven input ($V_{IN} = 3.4\text{V}$, control inputs only); A and B pins do not contribute to I_{CC} .
- This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics Over Operating Range

Parameters	Description	Conditions	Com.		Units
			Min.	Max.	
t_{PLH} t_{pHL}	Propagation Delay ^(1,2) , xAx to xBx, xBx to xAx	$C_L = 50\text{pF}$ $R_L = 500\text{-ohm}$		0.25	ns
t_{PLH} t_{pHL}	Propagation Delay, S to Ax or Bx		1.5	7.5	
t_{PZH} t_{PZL}	Bus Enable Time, S to xAx or xBx		1.5	7.0	
t_{PHZ} t_{PLZ}	Bus Disable Time, S to xAx or xBx		1.5	6.5	

Notes:

- This parameter is guaranteed but not tested on Propagation Delays.
- The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Packaging Mechanical: 56-Pin 240-Mil Wide Thin Plastic TSSOP (A56)


06-0736

Note:

- For latest package info, please check: <http://www.pericom.com/products/packaging/mechanicals.php>

Ordering Information

Ordering Code	Package Code	Package Description
PI5C16212AE	A	Pb-free & Green, 56-pin TSSOP

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
2. E = Pb-free and Green
3. Adding an X suffix = Tape/Reel

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