

1:5/1:7 Clock Buffer for Networking Applications

Features

- High Frequency >156 MHz
- High-speed, low-noise, non-inverting buffer
 - PI49FCT3802 is 1:5 buffer
 - PI49FCT3803 is 1:7 buffer
- Low-skew (<250ps) between any two output clocks
- Low duty cycle distortion <250ps
- Low propagation delay <2.5ns
- 5V Tolerant input
- Multiple V_{DD}, GND pins for noise reduction
- 3.3V supply voltage
- Packaging (Pb-free & Green available):
 - 16-pin TSSOP (L)
 - 16-pin QSOP (Q)

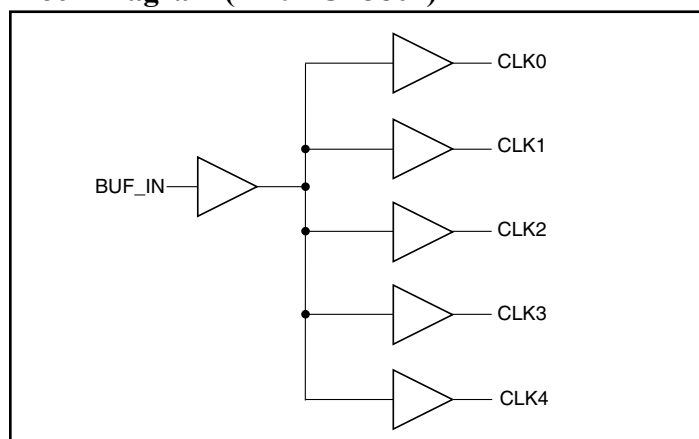
Description

The PI49FCT380x is a 3.3V compatible, high-speed, low-noise non-inverting clock buffer. The key goal in designing the PI49FCT380x is to target networking applications that require low-skew, low-jitter, and high-frequency clock distribution. Providing output-to-output skew as low as 250ps, the PI49FCT380x is an ideal clock distribution device for synchronous systems. Designing synchronous networking systems requires a tight level of skew from a large number of outputs.

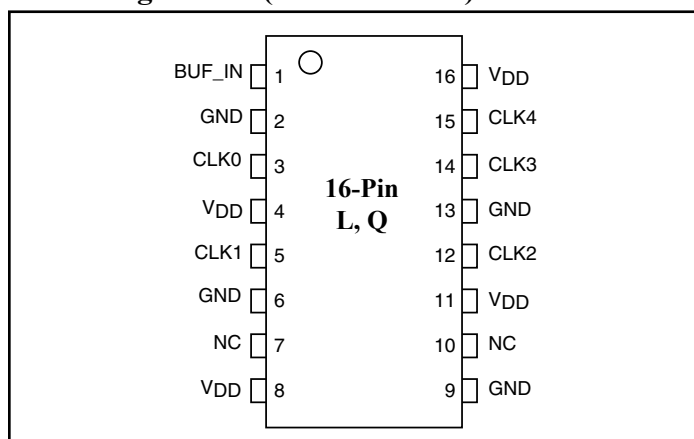
Pin Description

Pin Name		Description
3802	3803	
BUF_IN	BUF_IN	Input
CLK[0:4]	CLK[0:4]	Outputs
GND	GND	GND
V _{DD}	V _{DD}	Power

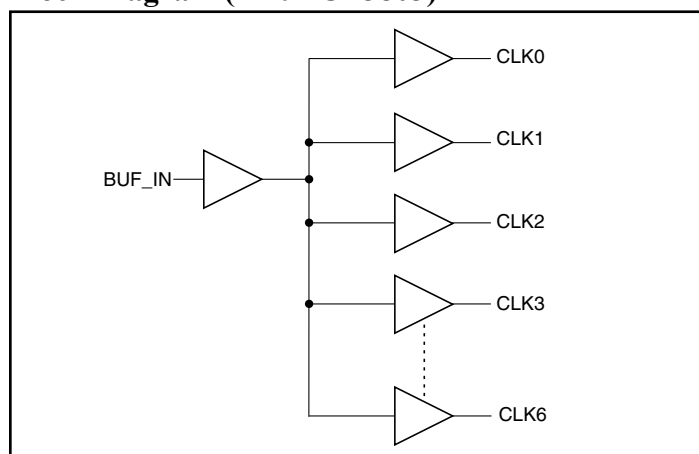
Block Diagram (PI49FCT3802)



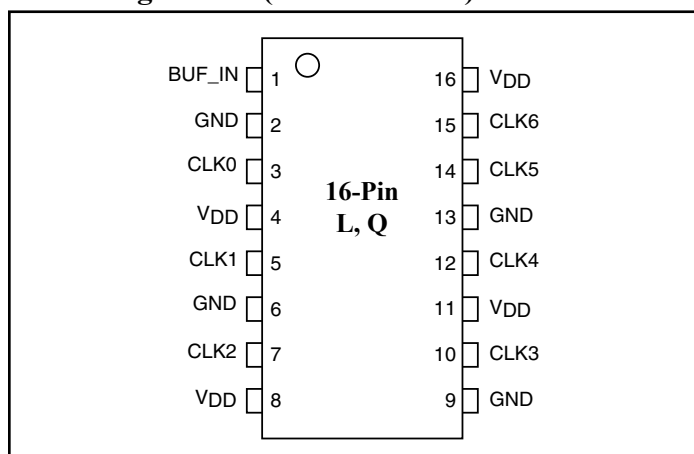
Pin Configuration (PI49FCT3802)



Block Diagram (PI49FCT3803)



Pin Configuration (PI49FCT3803)



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	-65°C to +150°C
V _{DD} Voltage	-0.5V to 5.5V
Output Voltage.....	-0.5V to 5.5V
Input Voltage	-0.5V to 5.5V
DC Output Current.....	-60mA to +60mA
Power Dissipation	500mW

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Operating Range

V _{DD} Voltage	3.3V ± 0.3V
Commercial Temperature	0°C to +70°C
Industrial Temperature	-40°C to +85°C
Input Frequency	DC to 156 MHz
Capacitive Loading	10pF to 50pF

DC Electrical Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level (Input Pins)		2.0		5.5	V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level (Input Pins)		-0.5		0.8	
I _{IH}	Input HIGH Current	V _{DD} = Max.	V _{IN} = V _{DD}			1	μA
I _{IL}	Input LOW Current	V _{DD} = Max.	V _{IN} = GND			-1	
V _{IK}	Clamp Diode Voltage	V _{DD} = Min., I _{IN} = -18mA			-0.7	-1.2	V
V _{OH}	Output HIGH Voltage	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OH} = -0.1mA	V _{DD} -0.2			
			I _{OH} = -12mA	2.4 ⁽³⁾	3.0		
V _{OL}	Output LOW Voltage	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}	I _{OH} = 0.1mA			0.2	
			I _{OH} = 12mA		0.3	0.5	
I _{OH}	Output HIGH Current	V _{DD} = 3.0V, V _{IN} = V _{IH} OR V _{IL} , V _{OUT} = 1.5V ⁽⁴⁾		-45	-75	-180	mA
I _{OL}	Output LOW Current	V _{DD} = 3.0V, V _{IN} = V _{IH} OR V _{IL} , V _{OUT} = 1.5V ⁽⁴⁾		50	92	200	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{DD} = 3.3V, +25°C ambient and maximum loading.
- V_{OH} = V_{DD} - 0.6V at rated current.
- This parameter is determined by device characterization but is not production tested.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

Power Supply Characteristics

Parameters	Description	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Units
I_{DDQ}	Quiescent Power Supply Current	$V_{DD} = \text{Max.}$	$V_{IN} = \text{GND or } V_{DD}$		0.1	30	μA
ΔI_{DD}	Supply Current per Inputs @ TTL High	$V_{DD} = \text{Max.}$	$V_{IN} = V_{DD} - 0.6\text{V}(3)$		47	300	
I_{DD}	Dynamic Supply Current	$V_{DD} = 3.6\text{V},$ No load	50 MHz		43		mA
			67 MHz		56		
			80 MHz		66		
			100 MHz		81		
			125 MHz		97		
			156 Mhz		121		

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at $V_{DD} = 3.3\text{V}$, $+25^\circ\text{C}$ ambient.
- Per TTL driven input ($V_{IN} = V_{DD} - 0.6\text{V}$); all other inputs at V_{DD} or GND.

Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Max.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.0	4	pF
C_{OUT}	Output Capacitance	$V_{OUT} = 0\text{V}$	—	6	

Note:

- This parameter is determined by device characterization but is not production tested.

Switching Characteristics ($V_{DD} = 3.3\text{V} \pm 0.3\text{V}$, $T_A = 85^\circ\text{C}$)

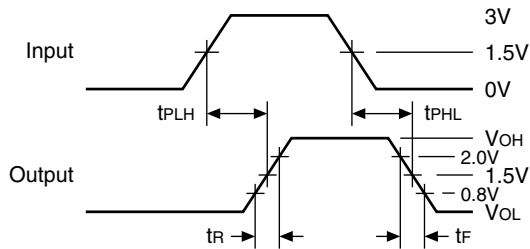
Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
t_R/t_F	CLKn Rise/Fall Time 0.8V ~ 2.0V	$C_L = 15\text{pF},$ 125 Mhz		0.7	1.0	ns
t_{PLH} t_{PHL}	Propagation Delay BUF_IN to CLKn		1.0	2.2	2.5	
$t_{sk(o)}^{(3)}$	Skew between two outputs of the same package (same transition)			110	250	ps
$t_{sk(p)}^{(3)}$	Skew between opposite transitions ($t_{PHL} - t_{PLH}$) of the same output			200	250	
$t_{sk(t)}^{(3)}$	Skew between two outputs of different packages ⁽⁴⁾				0.55	ns

Notes:

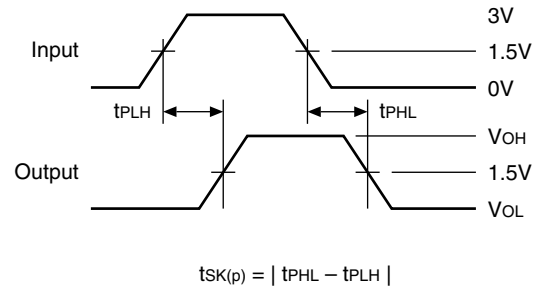
- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew measured at worse cast temperature (max. temp).
- Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.

Switching Waveforms

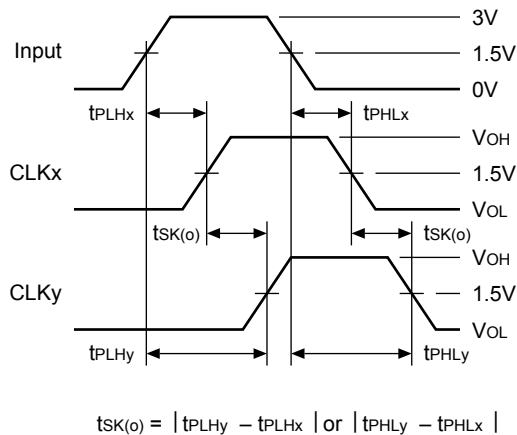
Propagation Delay



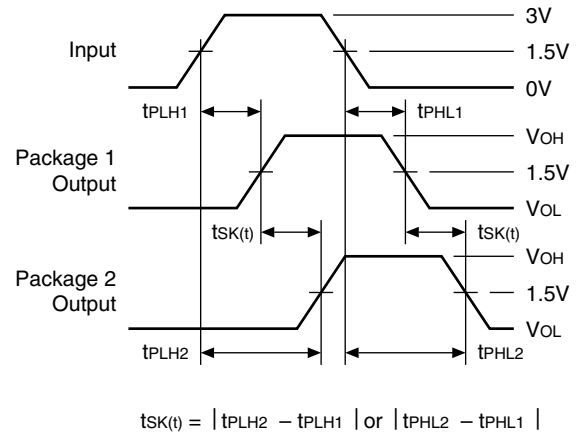
Pulse Skew – $t_{SK(P)}$



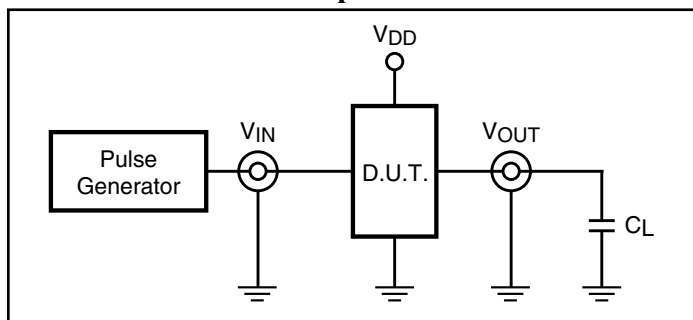
Output Skew – $t_{SK(O)}$



Package Skew – $t_{SK(T)}$



Test Circuits for All Outputs



Definitions:

C_L = Load capacitance: includes jig and probe capacitance.
 R_T = Termination resistance, should be equal to Z_{out} of the Pulse Generator.

The technical drawing consists of three main views:

- Top View:** Shows a rectangular component with 16 pins on each long edge. Dimensions include overall width (.189 / .197), pin pitch (.025 BSC), and total length (4.80 / 5.00). A circular feature is labeled 'O'.
- Side View:** Shows the profile of the component with a seating plane indicated. Dimensions include height (.150 / .157) and various radii and thicknesses at the base and sides.
- Detail A:** Provides a magnified view of the corner features, showing specific radii and thicknesses for the mounting flange and the internal structure.

All dimensions are provided in both inches and millimeters. The text "X.XX X.XX DENOTES DIMENSIONS IN MILLIMETERS" is included at the bottom right.

Ordering Information

Ordering Code	Package Code	Package Description
PI49FCT3802L	L	16-pin TSSOP
PI49FCT3802LE	L	Pb-free & Green, 16-pin TSSOP
PI49FCT3802Q	Q	16-pin QSOP
PI49FCT3802QE	Q	Pb-free & Green, 16-pin QSOP
PI49FCT3803L	L	16-pin TSSOP
PI49FCT3803LE	L	Pb-free & Green, 16-pin TSSOP
PI49FCT3803Q	Q	16-pin QSOP
PI49FCT3803QE	Q	Pb-free & Green, 16-pin QSOP

Notes:

1. Thermal characteristics can be found on the web at www.pericom.com/packaging/

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