

## High-Speed USB 2.0 (480 Mbps) 1-Port Switch with Full Power Down Feature

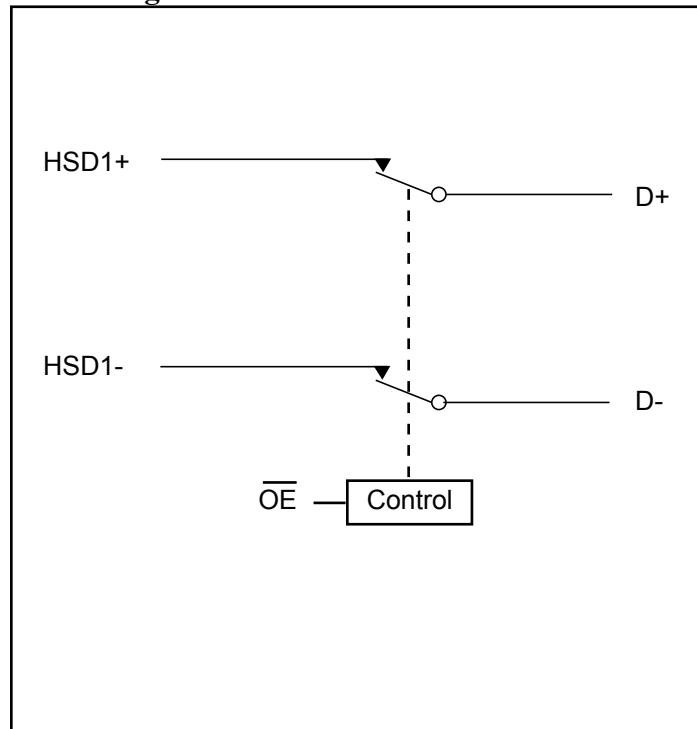
### Features

- V<sub>DD</sub> Operation at 3 V and 4.3 V
- 1.8-V Compatible Control-Pin Inputs
- I<sub>OFF</sub> Supports Full Power-Down Mode
- Operation
  - r<sub>on</sub> = 6-Ohm Typical
  - Δr<sub>on</sub> < 0.35-Ohm Typical
  - C<sub>io(ON)</sub> = 5 pF Typical
  - Low Power Consumption
  - ESD Performance
    - 7kV Human-Body Model, per JESD22 spec (A114-B, Class II)
    - 1000-V Charged-Device Model (C101)
    - ±4kV contact, per IEC61000-4-2
  - Wide –3-dB Bandwidth = 2110 MHz Typical
  - Packaged in 8-Pin TQFN (1.5 mm ' 1.5 mm)

### Application

- Routes Signals for USB 1.0, 1.1, and 2.0

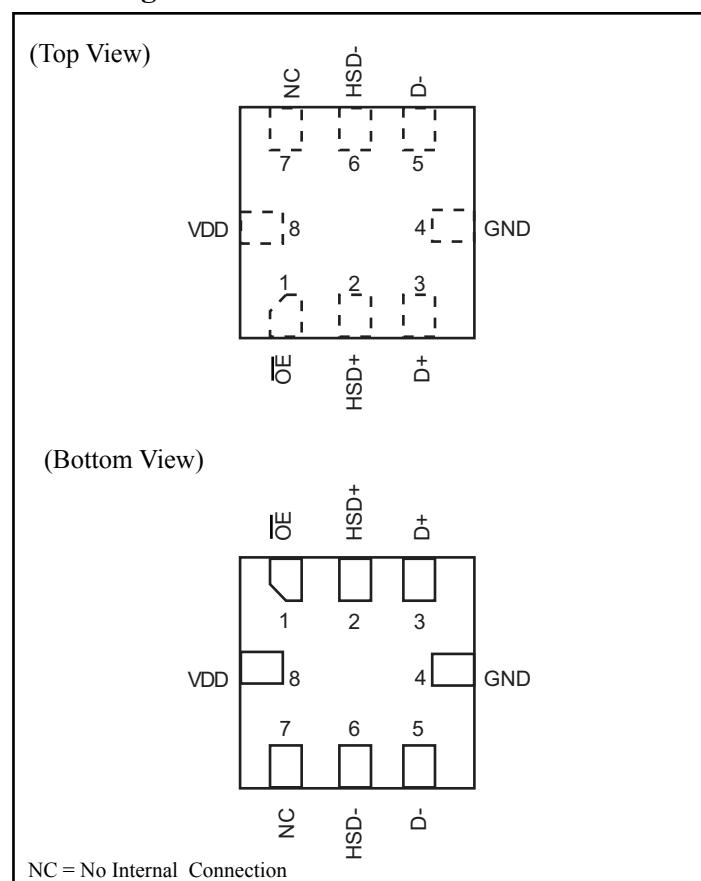
### Block Diagram



### Description

The PI3USB32 is a high-bandwidth switch specially designed for the switching of high-speed USB 2.0 signals in handset and consumer applications, such as cell phones, digital cameras, and notebooks with hubs or controllers with limited USB I/Os. The wide bandwidth (750 MHz) of this switch allows signals to pass with minimum edge and phase distortion. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew and high channel-to-channel noise isolation, and is compatible with various standards, such as high-speed USB 2.0 (480 Mbps).

### Pin Configuration



### Pin Description

Name	Description
$\overline{OE}$	Bus-switch enable
D+, D-, HSD+, HSD-	Data ports
NC	No connect

### Truth Table

$\overline{OE}$	Description
H	Disconnect
L	D+, D- = HSD+, HSD-

### Absolute Maximum Ratings<sup>(1)</sup>

(Over operating free-air temperature range unless otherwise noted.)

Supply Voltage Range (V <sub>DD</sub> ).....	-0.5V to +7V
Control Input Voltage Range (V <sub>IN</sub> ) <sup>(2)(3)</sup> .....	-0.5V to +7V
VI/O Switch I/O Voltage Range <sup>(2)(3)(4)</sup>	
HSD+, HSD- .....	-0.5V to V <sub>DD</sub> +0.3
D+, D- when V <sub>DD</sub> > 0 .....	-0.5V to V <sub>DD</sub> +0.3
D+, D- when V <sub>DD</sub> > 0 .....	5.25V
Control Input Clamp Current.....	-50 mA
I/O Port Clamp Current.....	-50 mA
ON-state switch current <sup>(5)</sup> .....	$\pm 64$ mA
Continuous current through V <sub>DD</sub> or GND .....	$\pm 100$ mA
I/O Port Clamp Current.....	-50 mA

#### Note:

(1) Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

(2) All voltages are with respect to ground, unless otherwise specified.

(3) The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

(4) V<sub>I</sub> and V<sub>O</sub> are used to denote specific conditions for V<sub>I/O</sub>.

(5) I<sub>I</sub> and I<sub>O</sub> are used to denote specific conditions for I<sub>I/O</sub>.

Recommended Operating Conditions<sup>(1)</sup>

Parameter	Description		Min.	Max.	Units
$V_{DD}$	Supply voltage		3.0	4.3	V
$V_{IH}$	High-level control input voltage	$V_{DD} = 3V$ to $3.6V$	1.3		V
		$V_{DD} = 4.3V$	1.7		
$V_{IL}$	Input LOW Voltage	$V_{DD} = 3V$ to $3.6V$		0.5	V
		$V_{DD} = 4.3V$		0.6	
$V_{I/O}$	Data input/output voltage		0	$V_{DD}$	V
$T_A$	Operating free-air temperature		-40	85	°C

## Notes:

1. All unused control inputs of the device must be held at  $V_{DD}$  or GND to ensure proper device operation.

## Electrical Characteristics over operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS <sup>1</sup>		MIN	TYP <sup>(2)</sup>	MAX	UNIT
$V_{IK}$	$V_{DD} = 3V$ , $I_I = -18mA$				-1.2	V
$I_{IN}$	Control inputs	$V_{DD} = 4.3V$ , $V_{IN} = 0$ to $4.3V$ , $V_{DD} = 0V$			$\pm 1$	$\mu A$
$I_{OZ}^{(3)}$		$V_{DD} = 4.3V$ , $V_O = 0$ to $3.6V$ , $V_I = 0$ , Switch OFF			$\pm 1$	$\mu A$
$I_{OFF}$	D+ and D-	$V_{DD} = 0V$ , $V_O = 0$ to $4.3V$ , $V_I = 0$ , $V_{IN} = V_{DD}$ or GND			$\pm 2$	$\mu A$
$I_{CC}$		$V_{DD} = 4.3V$ , $I_{I/O} = 0$			70	$\mu A$
$\Delta I_{CC}^{(4)}$	Control inputs	$V_{DD} = 4.3V$ , $V_{IN} = 2.6V$			10	$\mu A$
$C_{IN}$		$V_{DD} = 0V$ , $V_{IN} = V_{DD}$ or GND		1		pF
$C_{IO(OFF)}$	$V_{DD} = 3.3V$ , Switch OFF			2		pF
$C_{IO(ON)}$	$V_{DD} = 3.3V$ , Switch ON			5		pF
$R_{ON}^{(5)}$	$V_{DD} = 3V$ , $V_I = 0.4V$ , $I_O = -8mA$			6	10	$\Omega$
$\Delta R_{ON}$	$V_{DD} = 3V$ , $V_I = 0.4V$ , $I_O = -8mA$			0.35		$\Omega$
$\Delta R_{ON(flat)}$	$V_{DD} = 3V$ , $V_I = 0V$ or $1V$ , $I_O = -8mA$			2		$\Omega$

## Notes:

1.  $V_{IN}$  and  $I_{IN}$  refer to control input (OE).  $V_I$ ,  $V_O$ ,  $I_I$ , and  $I_O$  refer to data pins.
2. All typical values are at  $V_{DD} = 3.3V$  (unless otherwise noted),  $T_A = 25^\circ C$ .
3. For I/O ports, the parameter  $I_{OZ}$  includes the input leakage current.
4. This is the increase in supply current for each input that is at the specified TTL voltage level, rather than  $V_{DD}$  or GND.
5. Measured by the voltage drop between the D and HSD terminals at the indicated current through the switch. ON-state resistance is determined by the lower of the voltages of the two terminals.

**Dynamic Electrical Characteristics** (over operating range,  $T_A = -40^\circ\text{C}$  TO  $85^\circ\text{C}$ ,  $V_{DD} = 3.3\text{ V} \pm 10\%$ ,  $\text{GND} = 0\text{ V}$ )

Parameter	Description	Test Conditions <sup>(1)</sup>	Typ. <sup>(1)</sup>	Units
$O_{IRR}$	OFF isolation	$R_L = 50\text{-Ohms}$ , $f = 240\text{ MHz}$	-30	dB
BW	Bandwidth (-3 dB)	$R_L = 50\text{-Ohms}$	2110	MHz
Xtalk	Crosstalk	$R_L = 50\text{-Ohms}$ , $f = 240\text{ MHz}$	-55	dB

**Notes:**

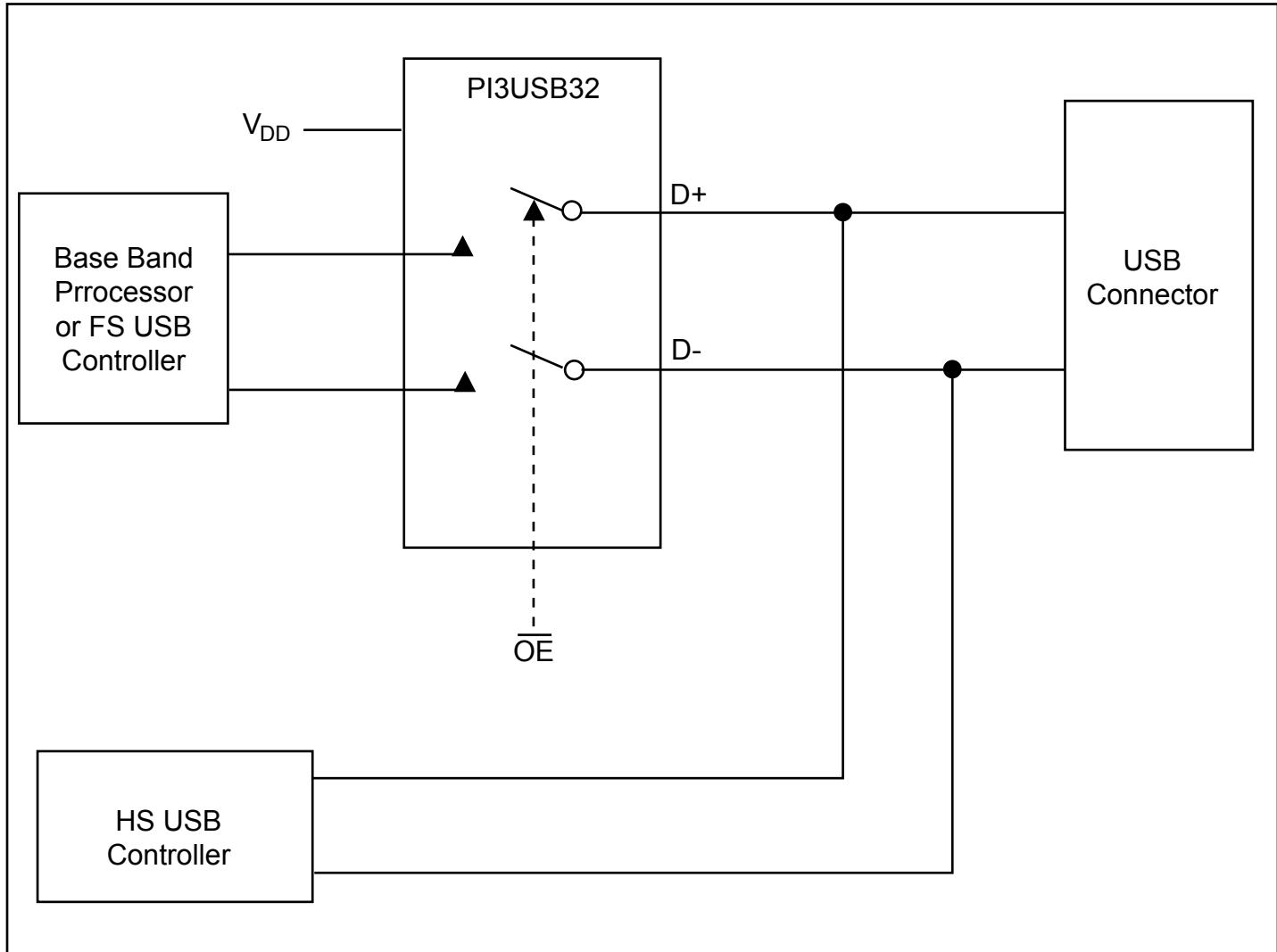
 1. For Max or Min conditions, use the appropriate value specified under Electrical Characteristics for the applicable device type.

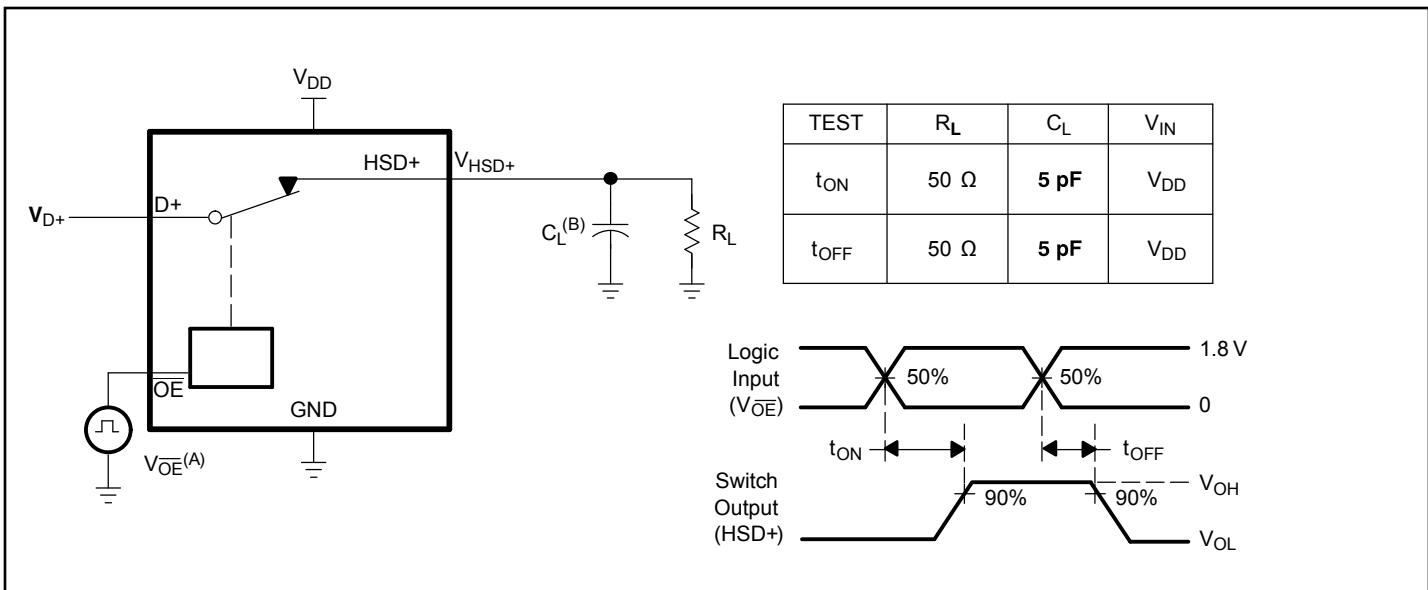
**Switching Characteristics** (over operating range,  $T_A = -40^\circ\text{C}$  TO  $85^\circ\text{C}$ ,  $V_{DD} = 3.3\text{ V} \pm 10\%$ ,  $\text{GND} = 0\text{ V}$ )

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ.	Max.	Units
$t_{PD}$	Propagation Delay <sup>(2,3)</sup>	$R_L = 50\text{ }\Omega$ , $C_L = 5\text{ pF}$			0.35	ns
$t_{ON}$	Line Enable Time, $\overline{\text{OE}}$ to D+/D-	$R_L = 50\text{ }\Omega$ , $C_L = 5\text{ pF}$			55	ns
$t_{OFF}$	Line Disable Time - $\overline{\text{OE}}$ to D+/D-	$R_L = 50\text{ }\Omega$ , $C_L = 5\text{ pF}$			25	ns
$t_{SK(P)}$	Skew between opposite transitions of the same output ( $t_{PHL} - t_{PLH}$ ) <sup>2</sup>	$R_L = 50\text{ }\Omega$ , $C_L = 5\text{ pF}$		50		ps
$t_J$	Total Jitter <sup>2</sup>	$R_L = 50\text{ }\Omega$ , $C_L = 5\text{ pF}$ , $t_R = t_F = 500\text{ ps}$ at 480 Mbps (PBRS = $2^{15} - 1$ )		200		ps

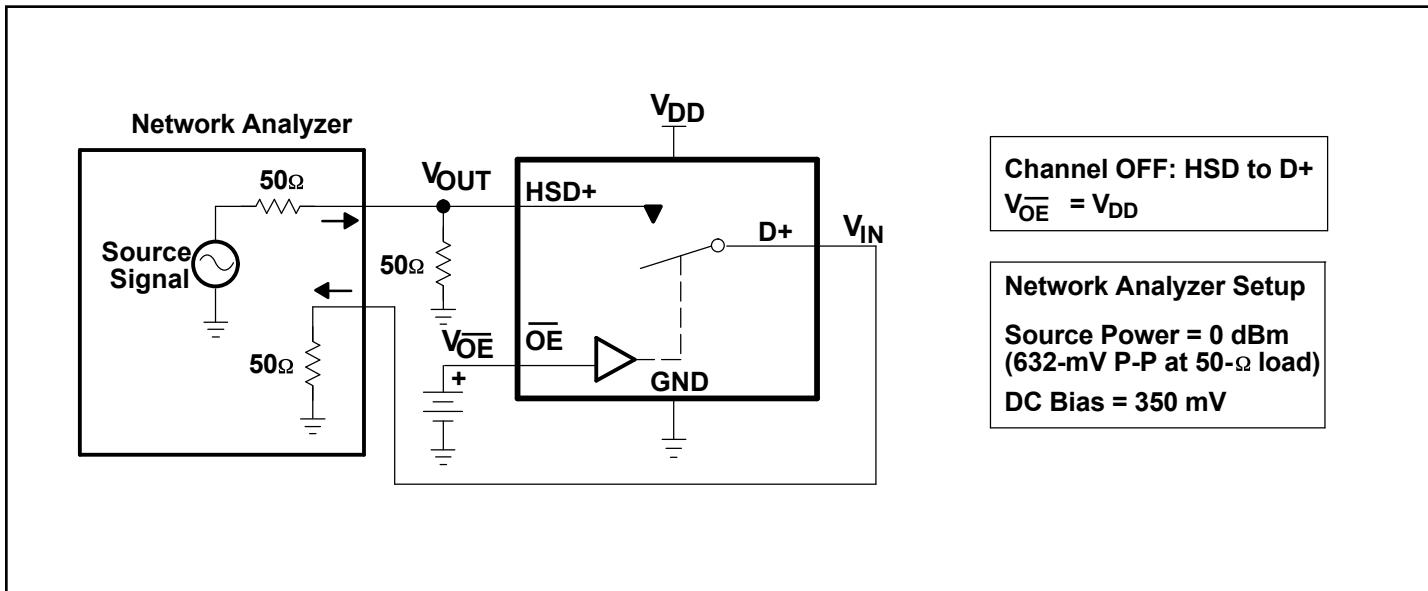
**Notes:**

 1. For Max or Min conditions, use the appropriate value specified under Electrical Characteristics for the applicable device type.
   
 2. Guaranteed by design.
   
 3. The bus switch contributes no propagational delay other than the RC delay of the on resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 10-pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

**Application Information**

**Application Diagram**



### Turn-On (t<sub>ON</sub>) and Turn-Off Time (t<sub>OFF</sub>)



**Figure 8. OFF Isolation (OIRR)**

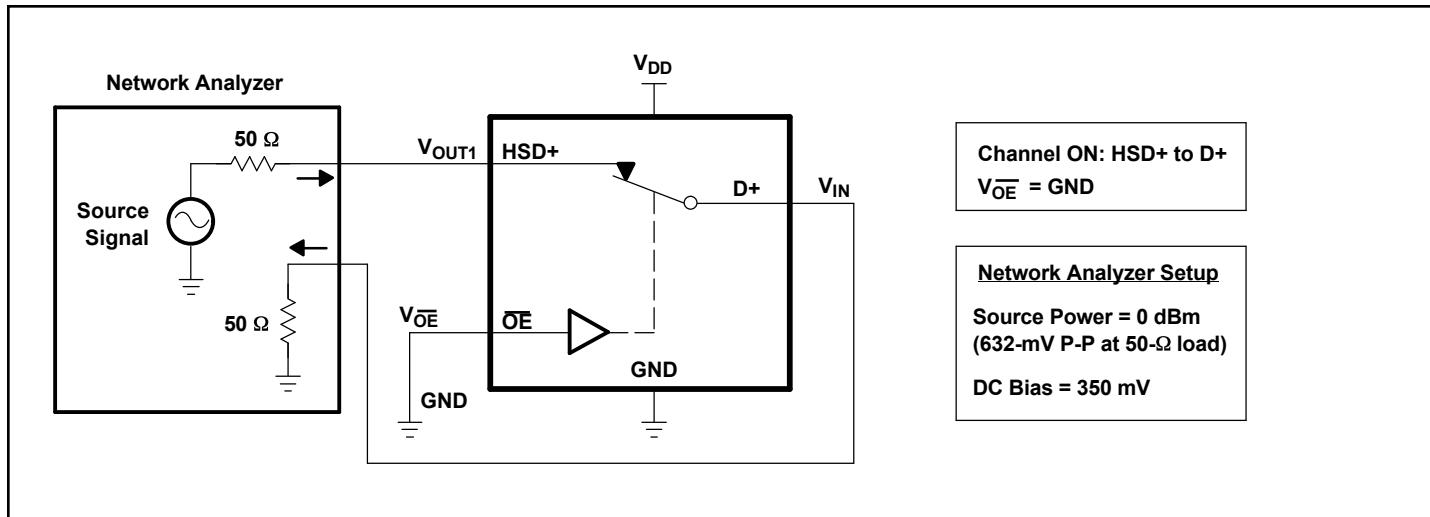


Figure 10. Bandwidth (BW)

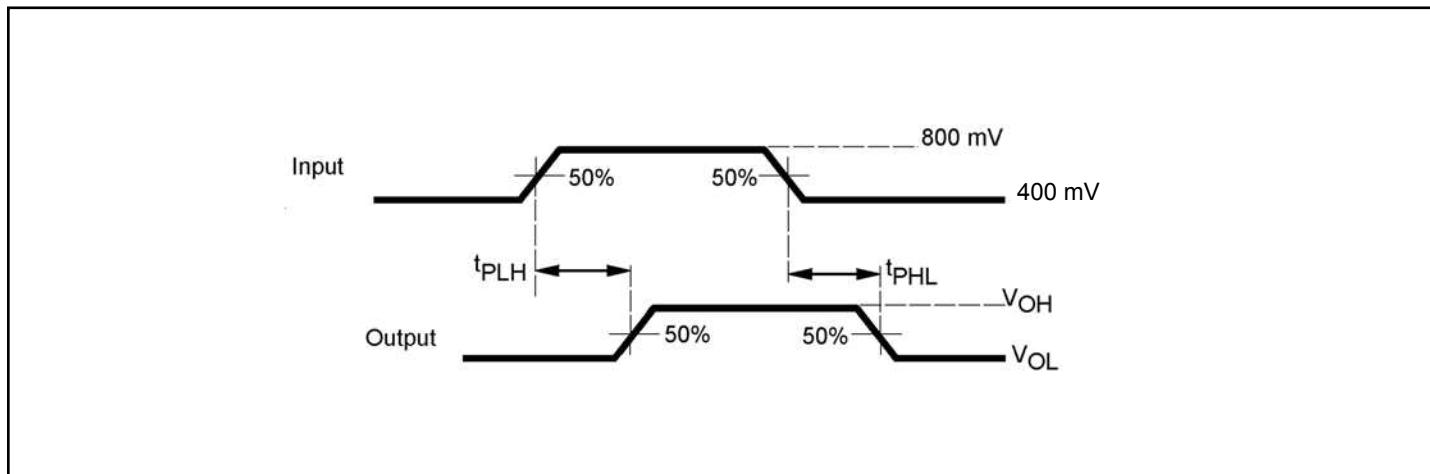
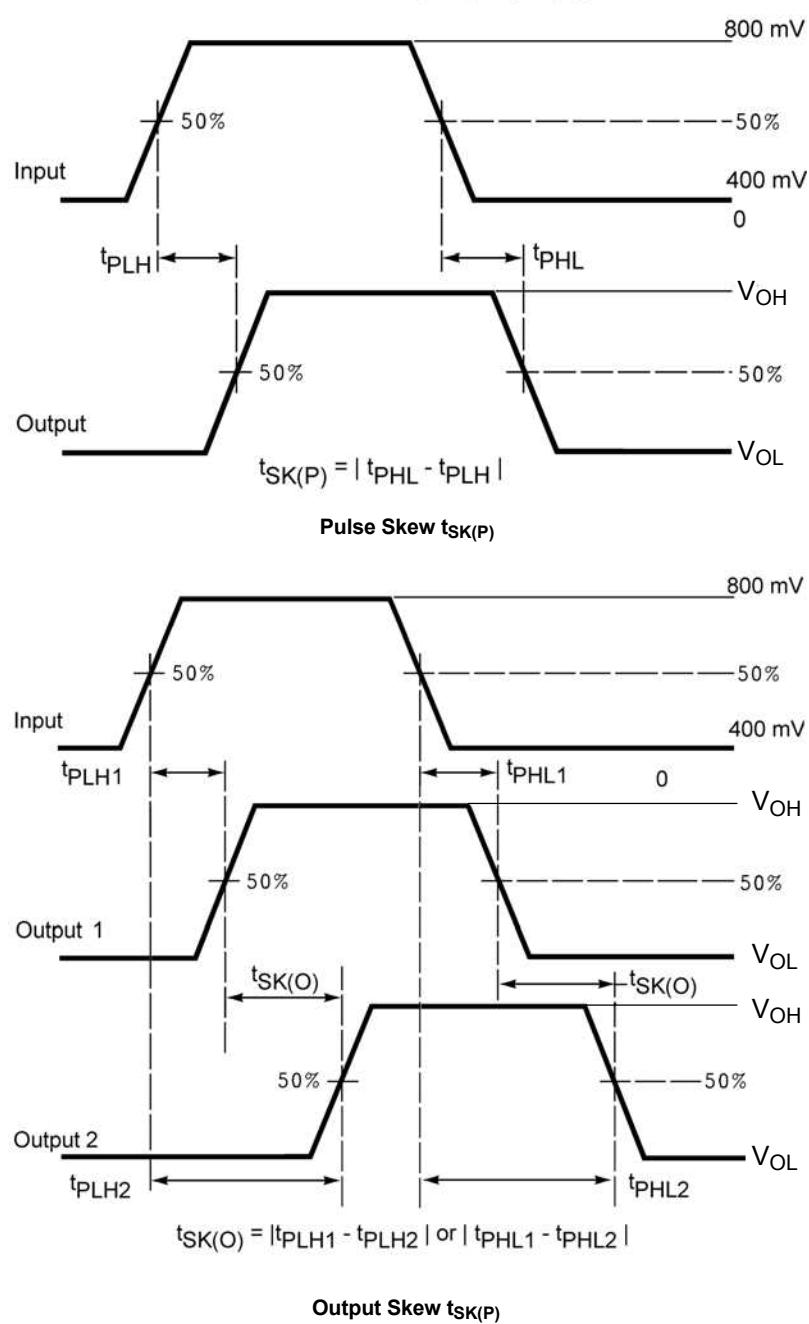
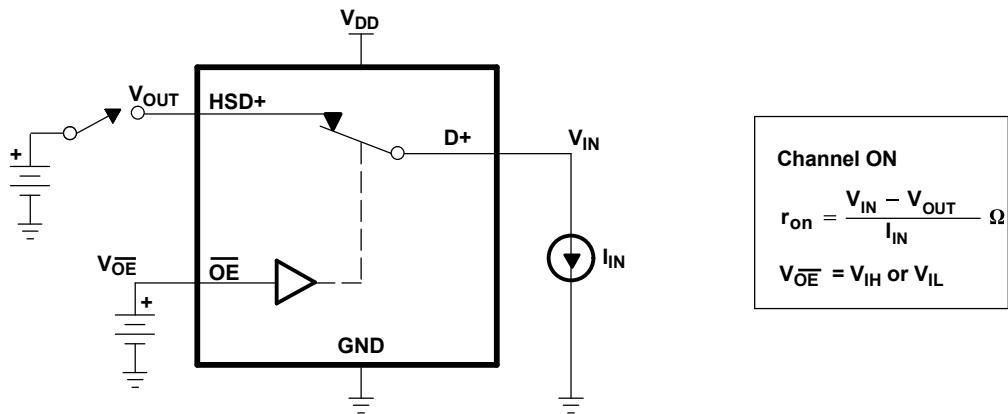
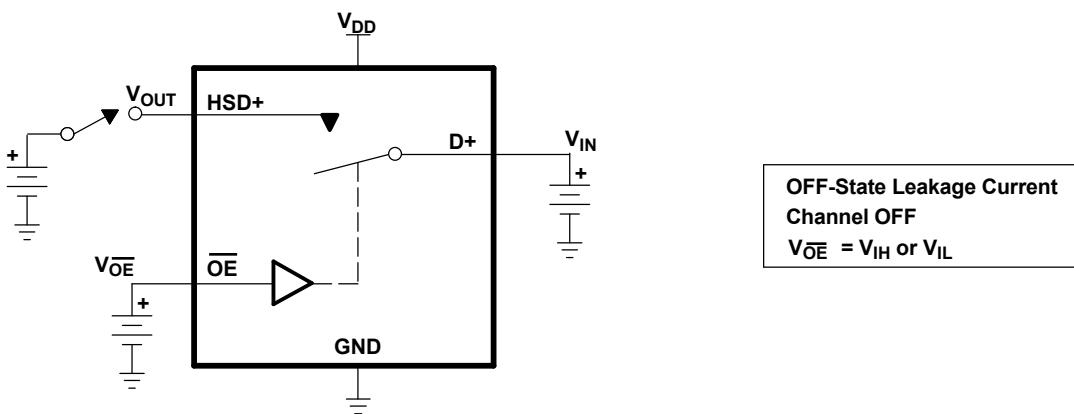
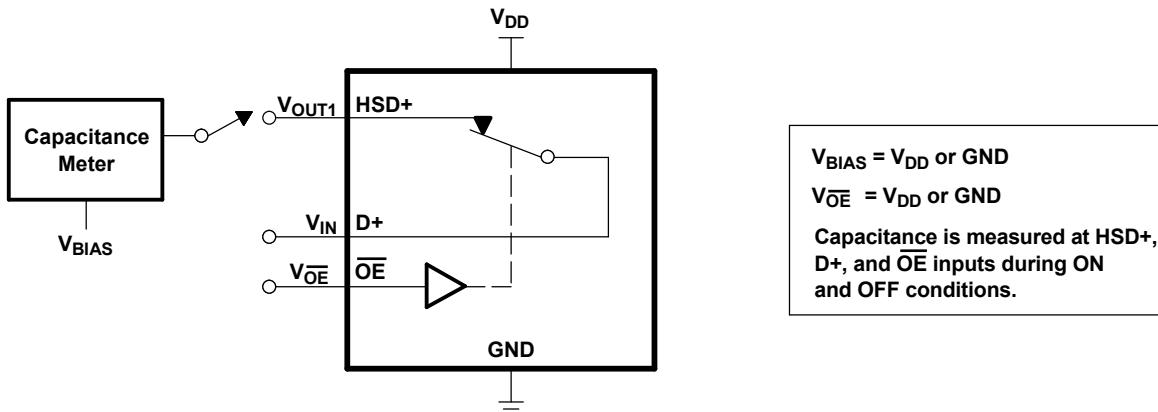
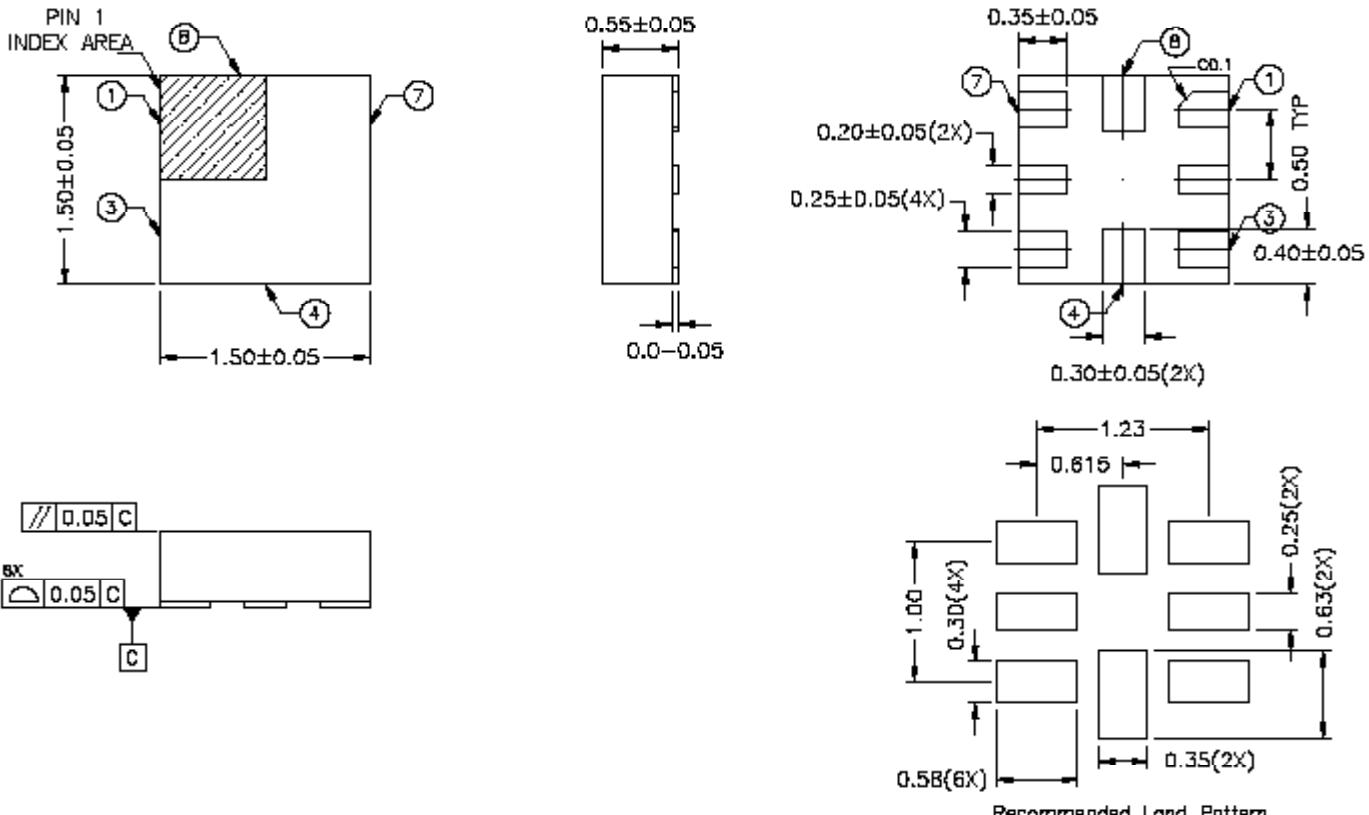


Figure 11. Propagation Delay


**Skew Test**


**ON-State Resistance ( $r_{on}$ )**

**OFF-State Leakage Current**




**NOTE :**

1. ALL DIMENSION ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS COPLANARITY SHALL NOT EXCEED 0.05 mm.
3. REFER JEDEC MO-288


**PERICOM®**  
Semiconductor Corporation

DATE: 09/26/08

DESCRIPTION: B-Lead, Thin Leadframe Land Grid Array (TLLGA)

PACKAGE CODE: XA8

DOCUMENT CONTROL #: PD-2081

REVISION: -

D-2081

**Ordering Information**

Ordering Code	Package Code	Package Description	Top Mark
PI3USB32XA8	XA	8-pin, TQFN	EK

**Notes:**

- Thermal characteristics can be found on the company web site at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)
- E = Pb-free and Green
- Adding X suffix = Tape/Reel

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