

**3.3V Quad, 2:1 Mux/DeMux
Fast Ethernet LAN Switch w/ Single Enable**
Features

- R_{ON} is 4Ω typical
- Low crosstalk: -27dB @ 250 MHz
- Near-Zero propagation delay: 250ps
- Switching speed: 9ns
- Channel On capacitance: 9pF (typical)
- V_{CC} Operating Range: +3.0V to +3.6V
- $>2\text{kV}$ ESD protection (human body model)
- >350 MHz bandwidth (or data frequency)
- Switching on Data I/O Ports (0 to 5V)
- I_{OFF} supports Partial-Power-Down mode operation
- Data and Control Inputs have Undershoot Clamp Diodes
- Packaging (Pb-free & Green available)
 - 16-pin 150-mil wide plastic QSOP (Q)
 - 16-pin 173-mil wide plastic TSSOP (L)

Applications

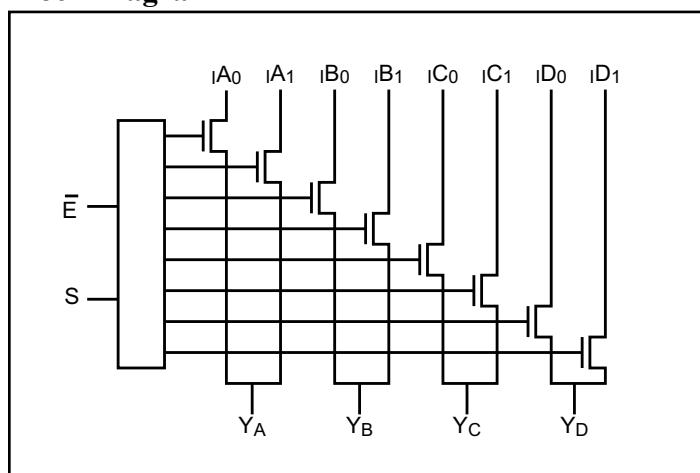
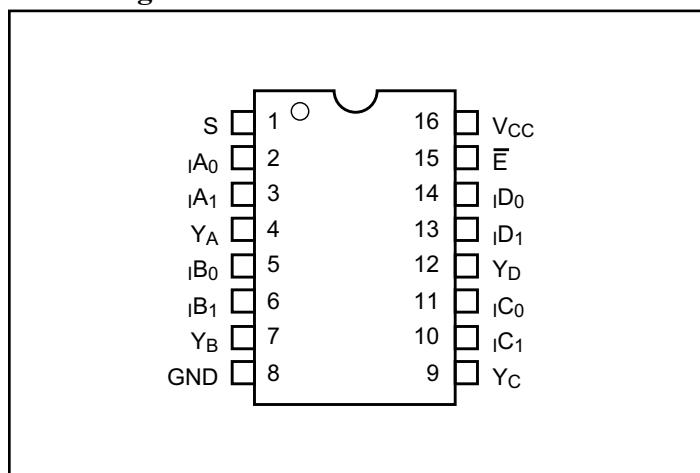
- Dual Physical Layer Device sharing to one interface connector
- Routes signals for 10/100 Mbit Ethernet

Description

The PI3L110 is a 8- to 4-bit multiplexer/demultiplexer LAN Switch with Hi-Z outputs. Industry leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and I/O capacitance. The device multiplexes differential outputs from a Fast Ethernet transceiver (PHY) device to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew, high channel-to-channel noise isolation and is compatible with various standards, such as 10/100 Base-T (Ethernet).

Generally, this part can be used to replace mechanical relays in low-voltage LAN applications that interface a physical layer over CAT 5 or CAT 6 unshielded twisted pair cable through an isolation transformer.

The PI3L110 is fully specified for partial-power-down applications using I_{OFF} . The I_{OFF} feature ensures that damaging current will not backflow throughout the device when it is powered down. The device has isolation during Power Off.

Block Diagram

Pin Configuration


Truth Table⁽¹⁾

Ē	S	YA	YB	YC	YD	Function
H	X	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Disable
L	L	I_{A0}	I_{B0}	I_{C0}	I_{D0}	$S = 0$
L	H	I_{A1}	I_{B1}	I_{C1}	I_{D1}	$S = 1$

Note:

1. H = High Voltage Level
- L = Low Voltage Level
- X = Don't Care

Pin Description

Pin Name	Description
I_{AN-1D_N}	Data Inputs
S	Select Inputs
\bar{E}	Enable
YA-YD	Data Outputs
GND	Ground
V _{CC}	Power

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Supply Voltage to Ground Potential	-0.5V to +4.0V
DC Input Voltage	-0.5V to +5.5V
DC Output Current.....	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for 10/100 Base-T Ethernet Switching over Operating Range

($T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameter	Description	Test Conditions⁽¹⁾	Min.	Typ.⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed HIGH level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed LOW level	-0.5		0.8	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Max.}$, $I_{IN} = -18\text{mA}$		-0.7	-1.2	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}$, $V_{IN} = V_{CC}$			± 5	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}$, $V_{IN} = \text{GND}$			± 5	μA
I_{OFF}	Current while I/O disabled	$V_{CC} = 0$, $V_O = 0$ to 5V			1	μA
R_{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = \text{Min.}$, $1.25\text{V} \leq V_{IN} \leq V_{CC}$, $I_{IN} = -10\text{mA}$ to -30mA		4.0	8.0	Ω
$R_{FLAT(ON)}$	On-Resistance Flatness ⁽⁴⁾	$V_{CC} = \text{Min.}$, $V_{IN} @ 1.25\text{V}$ and V_{CC} , $I_{IN} = -10\text{mA}$ to -30mA		1		
ΔR_{ON}	On-Resistance match from center ports to any other port ⁽⁴⁾	$V_{CC} = \text{Min.}$, $1.25\text{V} \leq V_{IN} \leq V_{CC}$, $I_{IN} = -10\text{mA}$ to -30mA		0.9	2.0	

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^\circ\text{C}$ ambient and maximum loading.
3. Measured by the voltage drop between I_{IN} and Y_N pins at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I_{IN} & Y_N) pins.
4. This parameter is determined by device characterization but is not production tested.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ.	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0V	2	3	pF
C _{OFF(In)}	Port I _N Capacitance, Switch OFF		4	6	
C _{ON}	Y _N /I _N Capacitance, Switch ON		9	10	

Notes:

1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max., V _{IN} = GND or V _{CC}			800	µA

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.

Dynamic Electrical Characteristics Over the Operating Range

(T_A = -40°C to +85°C, V_{CC} = 3.3V ±10%, GND = 0V)

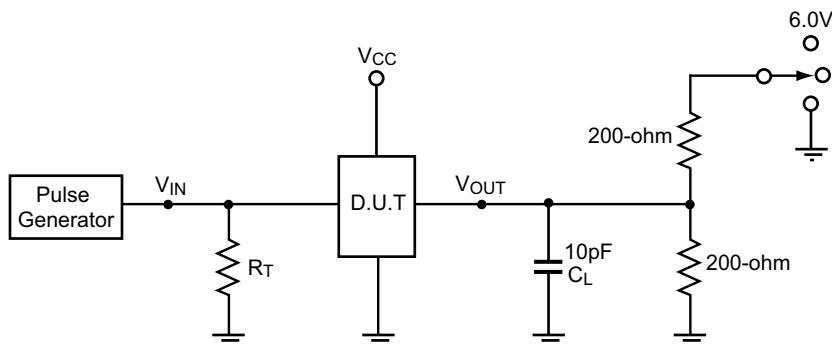
Parameter	Description	Test Conditions	Min.	Typ.	Max.	Units
X _{TALK}	Crosstalk	R _L = 50Ω, f = 250 MHz		-40		dB
O _{IRR}	OFF Isolation			-30		
BW	Bandwidth -3dB	R _L = 50Ω		350		MHz

Characteristics

Paramenter	Description	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Units
t _{PD}	Propagation Delay ^(2,3)	R _L = 50Ω, f = 250 MHz		0.25		ns
t _{PZH} , t _{PZL}	Line Enable Time - S or \bar{E} to Y _N , I _N		0.5		15	
t _{PHZ} , t _{P LZ}	Line Disable Time - S to \bar{E} to Y _N , I _N		0.5		9	
t _{SK(p)}	Skew between opposite transitions of the same output (t _{PHL} - t _{P LH}) ⁽²⁾			0.1	0.2	

Notes:

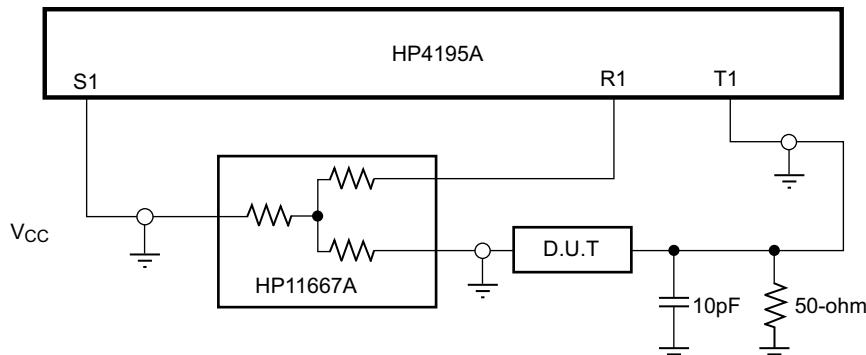
1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Guaranteed by design.
3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

Test Circuit for Electrical Characteristics

Notes:

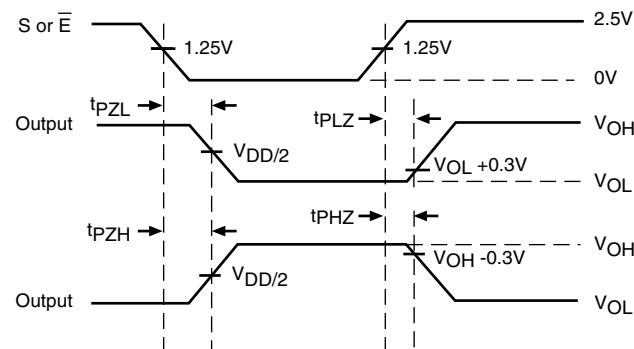
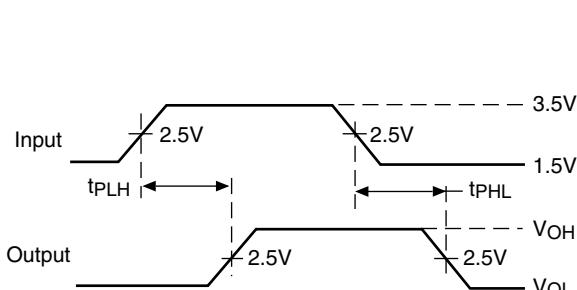
- C_L = Load capacitance: includes jig and probe capacitance.
- R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
- Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_0 = 50\Omega$, $t_R \leq 2.5\text{ns}$, $t_F \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

Switch Positions

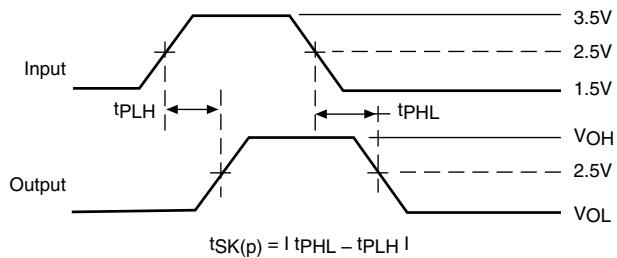
Test	Switch
t_{PLZ} , t_{PZL}	6.0V
t_{PHZ} , t_{PZH}	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics


Switching Waveforms



Voltage Waveforms Enable and Disable Times



Pulse Skew - $t_{SK(p)}$

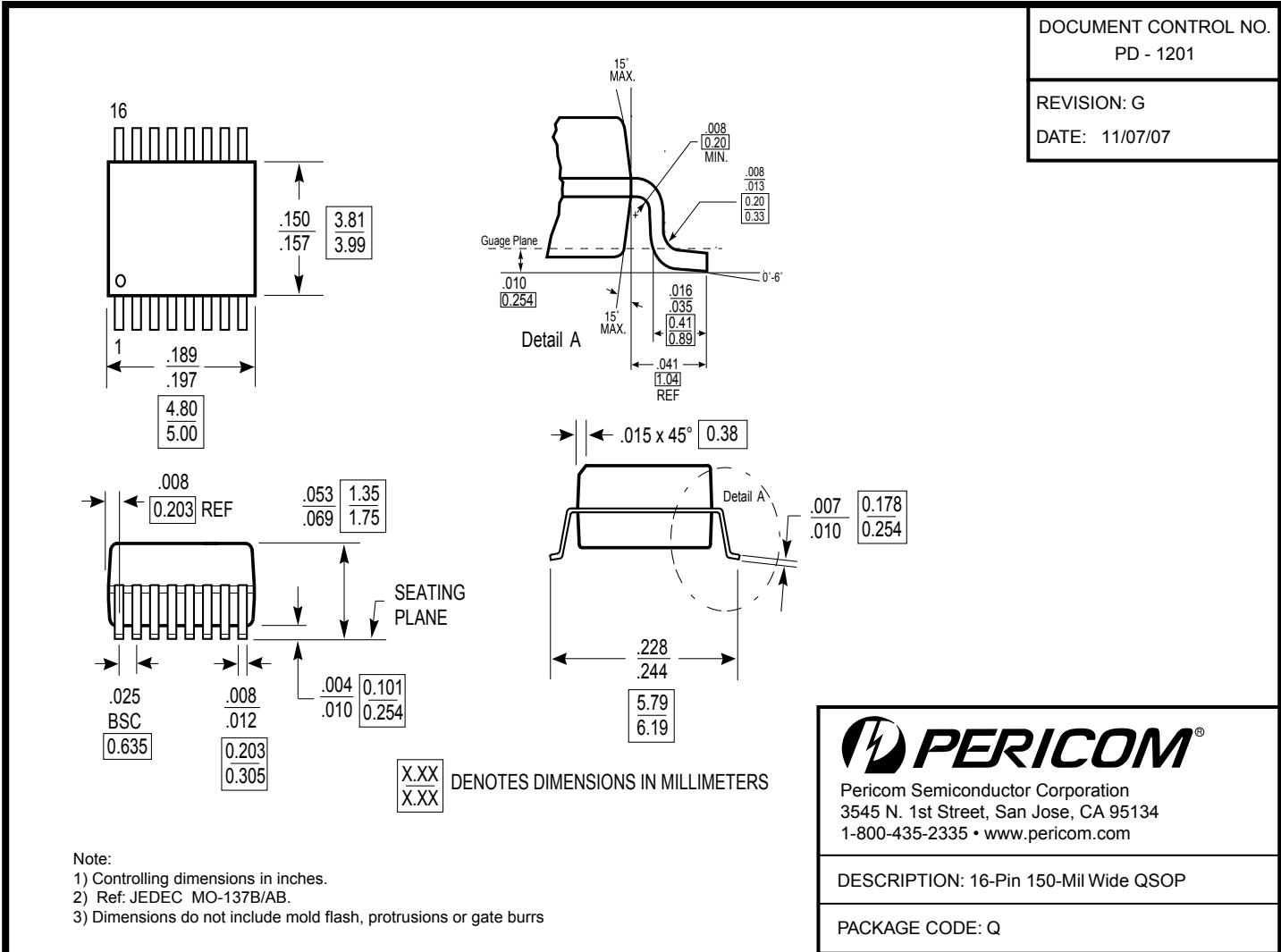
Applications Information

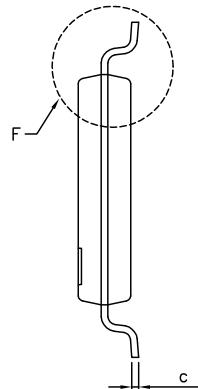
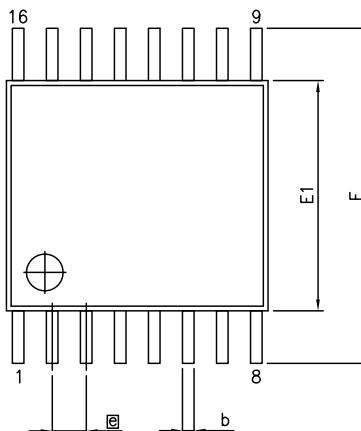
Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V. Driving the control inputs Rail-to-Rail® minimizes power consumption.

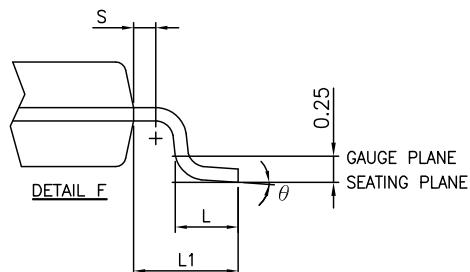
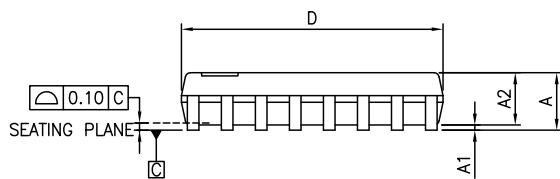
Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Packaging Mechanical: 16-Pin QSOP (Q)



Packaging Mechanical: 16-Pin TSSOP (L)

VARIATIONS (ALL DIMENSIONS SHOWN IN MM)

SYMBOLS	MIN.	NOM.	MAX.
A	—	—	1.20
A1	0.05	—	0.15
A2	0.80	—	1.05
b	0.19	—	0.30
c	0.09	—	0.20
D	4.90	5.00	5.10
E1	4.30	4.40	4.50
E	6.40 BSC		
e	0.65 BSC		
L1	1.00 REF		
L	0.45	0.60	0.75
S	0.20	—	—
θ	0°	—	8°



Notes:
 1. Refer JEDEC MO-153F/AB
 2. Controlling dimensions in millimeters
 3. Package outline exclusive of mold flash and metal burr


DATE: 05/03/12
DESCRIPTION: 16-Pin, 173mil Wide TSSOP
PACKAGE CODE: L
DOCUMENT CONTROL #: PD-1310
REVISION: F
Ordering Information

Ordering Code	Package Code	Package Description
PI3L110QE	Q	Pb-free & Green, 16-pin 150-mil wide plastic QSOP
PI3L110LE	L	Pb-free & Green, 16-pin 173-mil wide plastic TSSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel