TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74ACT153P,TC74ACT153F,TC74ACT153FN

Dual 4-Channel Multiplexer

The TC74ACT153 is an advanced high speed CMOS DUAL 4-CHANNEL MULTIPLEXERs fabricated with silicon gate and double-layer metal wiring C²MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipations.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

Each of these data (1C0-1C3, 2C0-2C3) is selected by the two address inputs A and B.

Separate strobe inputs ($1\overline{G}$, $\ 2\overline{G}$) are provided for each of the two four-line sections.

The strobe input can be used to inhibit the data output; the output is fixed in low level unconditionally.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

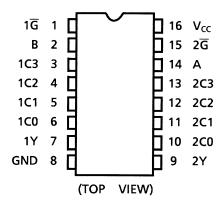
Features

- High speed: $t_{pd} = 5.4 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 8 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$
- Compatible with TTL outputs: VIL = 0.8 V (max)

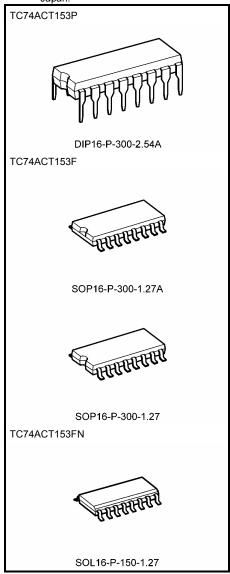
$$V_{IH} = 2.0 \text{ V (min)}$$

- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74F153

Pin Assignment



Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

 DIP16-P-300-2.54A
 : 1.00 g (typ.)

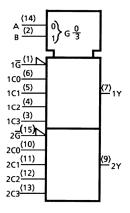
 SOP16-P-300-1.27A
 : 0.18 g (typ.)

 SOP16-P-300-1.27
 : 0.18 g (typ.)

 SOL16-P-150-1.27
 : 0.13 g (typ.)

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IEC Logic Symbol

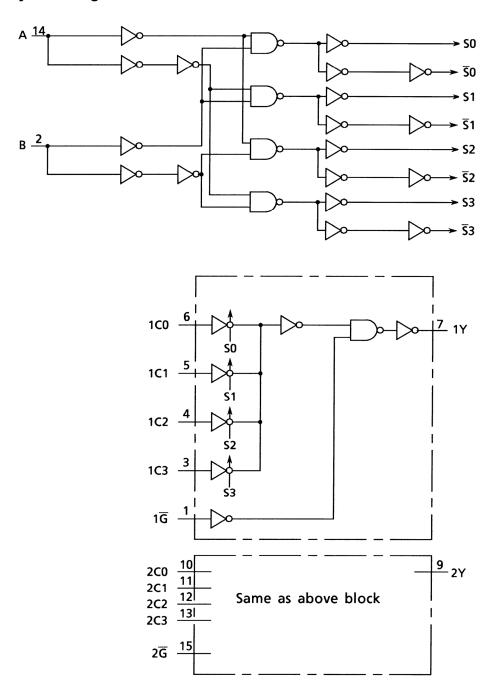


Truth Table

Select Inputs			Data l	Inputs	Strobe	Output		
В	Α	C0	C1	C2	C3	G	Υ	
Х	Х	Х	Х	Х	Х	Н	L	
L	L	L	Х	Х	Х	L	L	
L	L	Н	Х	Х	Х	L	Н	
L	Н	Х	L	Х	Х	L	L	
L	Н	Х	Н	Х	Х	L	Н	
Н	L	Х	Х	L	Х	L	L	
Н	L	Х	Х	Н	Х	L	Н	
Н	Н	Х	Х	Х	L	L	L	
Н	Н	Х	Х	Х	Н	L	Н	

X: Don't care

System Diagram





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	P _D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of Ta = -40 to $65^{\circ}C$. From Ta = 65 to $85^{\circ}C$ a derating factor of -10 mW/°C should be applied up to 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	4.5 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	Vout	0 to V _{CC}	٧
Operating temperature	T _{opr}	−40 to 85	°C
Input rise and fall time	dt/dV	0 to 10	ns/V

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
High-level input voltage	V _{IH}	_			4.5 to 5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V _{IL}	_			4.5 to 5.5	_	_	0.8	_	0.8	V
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -50 \mu A$		4.5	4.4	4.5	_	4.4	_	
High-level output voltage			$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_	V
3.			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	_	_	_	3.85	_	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 50 \ \mu A$		4.5	_	0.0	0.1	_	0.1	
Low-level output voltage			$I_{OL} = 24 \text{ mA}$		4.5	_	_	0.36	_	0.44	V
l			$I_{OL} = 75 \text{ mA}$	(Note)	5.5		_	—		1.65	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND			5.5		_	±0.1	_	±1.0	μА
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND			5.5	_	_	8.0	_	80.0	μΑ
	lc	Per input: V _{IN} = 3.4 V		5.5	_		1.35		1.5	mA	
		Other inp	ut: V _{CC} or GND		5.5			1.33	_	1.5	IIIA

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: $t_r = t_f = 3$ ns)

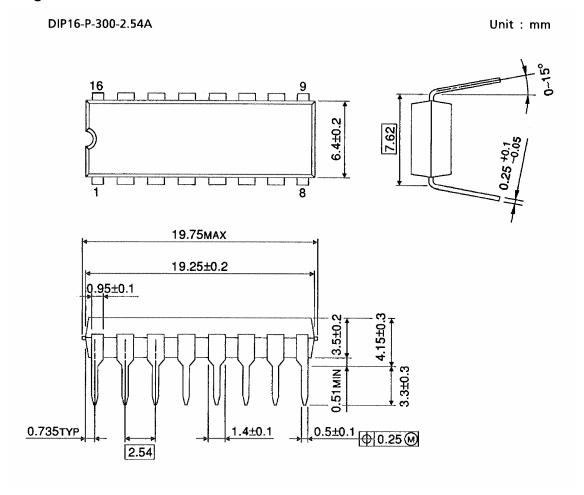
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time (Cn-Y)	t _{pLH}	_	5.0 ± 0.5	_	6.1	9.7	1.0	11.0	ns
Propagation delay time (A, B-Y)	t _{pLH}	_	5.0 ± 0.5	_	7.8	11.8	1.0	13.5	ns
Propagation delay time (\overline{G} -Y)	t _{pLH}	_	5.0 ± 0.5	_	5.6	9.7	1.0	11.0	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)			_	47	_	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

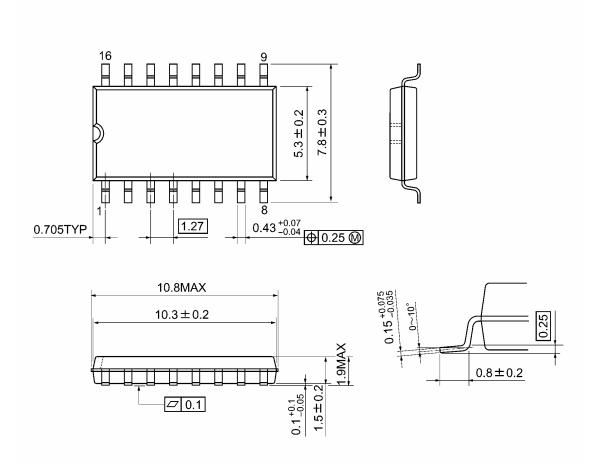
Package Dimensions



Weight: 1.00 g (typ.)

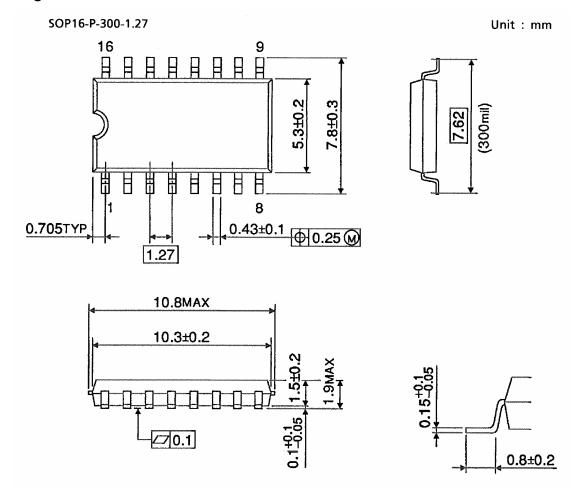
Package Dimensions

SOP16-P-300-1.27A Unit: mm



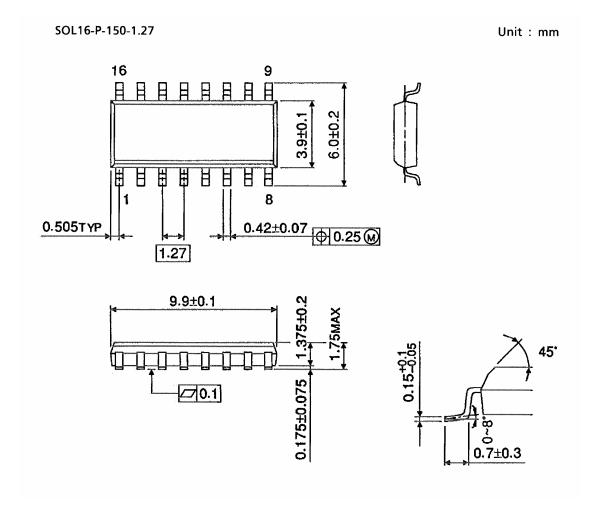
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Note: Lead (Pb)-Free Packages

DIP16-P-300-2.54A SOP16-P-300-1.27A SOL16-P-150-1.27

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