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## Voltage Detectors in 4-Bump (2 x 2) Chip-Scale Package

### **General Description**

The MAX6406-MAX6411 is a family of ultra-low power circuits used for monitoring battery, power-supply, and regulated system voltages. Each detector contains a precision bandgap reference comparator and is trimmed to specified trip threshold voltages. These devices provide excellent circuit reliability and low cost by eliminating external components and adjustments when monitoring system voltages from 2.5V to 5.0V. A manual reset input is also included.

The MAX6406-MAX6411 assert a signal whenever the VCC supply voltage falls below a preset threshold. These devices are differentiated by their output logic configurations and preset threshold voltages. The MAX6406/MAX6409 (push-pull) and the MAX6408/ MAX6411 (open-drain) have an active-low output (OUT is logic low when V<sub>CC</sub> is below V<sub>TH</sub>). The MAX6407/ MAX6410 have an active-high push-pull output (OUT is logic high when V<sub>CC</sub> is below V<sub>TH</sub>). All parts are guaranteed to be in the correct output logic state for VCC down to 1V. The detector is designed to ignore fast transients on VCC. The MAX6406/MAX6407/ MAX6408 have voltage thresholds between 2.20V and 3.08V in approximately 100mV increments. The MAX6409/MAX6410/MAX6411 have voltage thresholds between 3.30V and 4.63V in approximately 100mV increments.

Ultra-low supply current of 500nA (MAX6406/MAX6407/ MAX6408) makes these parts ideal for use in portable equipment. These devices are available in 4-bump chip-scale packages (UCSPTM).

## **Applications**

Portable/Battery-Powered Equipment

Cell Phones

**PDAs** 

MP3 Players

Pagers

### **Selector Guide**

PART	NOMINAL V <sub>TH</sub> (V)	OUT/OUT Output Type
MAX6406	2.20 to 3.08	Push-Pull, Active-Low
MAX6407	2.20 to 3.08	Push-Pull, Active-High
MAX6408	2.20 to 3.08	Open-Drain, Active-Low
MAX6409	3.30 to 4.63	Push-Pull, Active-Low
MAX6410	3.30 to 4.63	Push-Pull, Active-High
MAX6411	3.30 to 4.63	Open-Drain, Active-Low

#### **Features**

- ♦ Tiny 4-Bump (2 × 2) Chip-Scale Package, (Package Pending Full Qualification—Expected Completion Date 6/30/01. See UCSP Reliability Section for More Details.)
- ♦ 70% Smaller Than SC70 Packages
- ♦ Ultra-Low 500nA Supply Current (MAX6406/MAX6407/MAX6408)
- ♦ Factory-Trimmed Reset Thresholds from 2.20V to 4.63V in Approximately 100mV Increments
- ♦ ±2.5% Threshold Accuracy (-40°C to +85°C)
- ♦ Manual Reset Input
- ♦ Guaranteed OUT Valid to V<sub>CC</sub> = 1.0V
- ♦ Three Reset Output Logic Options: Active-Low Push-Pull, Active-High Push-Pull, and Active-Low Open-Drain
- ♦ Immune to Short V<sub>CC</sub> Transients
- ♦ No External Components

### **Ordering Information**

PART	TEMP. RANGE	PIN-PACKAGE
MAX6406BST	-40°C to +85°C	UCSP-4
MAX6407BST	-40°C to +85°C	UCSP-4
MAX6408BST	-40°C to +85°C	UCSP-4
MAX6409BST	-40°C to +85°C	UCSP-4
MAX6410BST	-40°C to +85°C	UCSP-4
MAX6411BST	-40°C to +85°C	UCSP-4

The MAX6406-MAX6411 are available in factory-set V<sub>CC</sub> detector thresholds from 2.20V to 4.63V, in approximately 0.1V increments. Choose the desired threshold suffix from Table 1 and insert it in the blank space following "S". There are 21 standard versions with a required order increment of 2500 pieces. Sample stock is generally held on the standard versions only (Table 1). Required order increment is 10,000 pieces for nonstandard versions (Table 2). Contact factory for availability. All devices available in tape-and-reel only.

UCSP reliability is integrally linked to the user's assembly methods, circuit board material, and environment. Refer to the UCSP Reliability Notice in the UCSP Reliability section of this data sheet for more information.

#### Pin Configuration appears at end of data sheet.

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### **ABSOLUTE MAXIMUM RATINGS**

All voltages measured to GND unless otherwise noted.	Continuous Po
VCC0.3V to +6	
OUT/OUT0.3V to (V <sub>CC</sub> + 0.3'	<ul><li>V) Operating Ter</li></ul>
OUT (open-drain)0.3V to +6	V Junction Tem
MR0.3V to (V <sub>CC</sub> + 0.3'	<ul><li>V) Storage Temp</li></ul>
Input/Output Current into Any Pin20m	A Bump Reflow

Continuous Power Dissipation (T<sub>A</sub> = +70°C)
4-Pin/Bump UCSP (derate 3.8mW/°C above +70°C)...303mW

Operating Temperature Range ....-40°C to +85°C

Junction Temperature ....+150°C

Storage Temperature Range ...-65°C to +160°C

Bump Reflow Temperature ...+235°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

(V<sub>CC</sub> = 1.0V to 5.5V, T<sub>A</sub> = -40°C to +85°C, unless otherwise noted. Typical values are at V<sub>CC</sub> = 3V and T<sub>A</sub> = +25°C.) (Note1)

PARAMETER	SYMBOL	CON	DITIONS	MIN	TYP	MAX	UNITS	
Owner had Veller and Description	M	$T_A = 0$ °C to +70°C		1.0		5.5	V	
Supply Voltage Range	Vcc	$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$		1.2		5.5	V	
Supply Current	Icc	$\begin{aligned} & \text{MAX6406/MAX6407/MAX6408} \\ & \text{V}_{\text{CC}} = 3.0 \text{V for V}_{\text{TH}} \leq 2.93 \text{V}, \\ & \text{V}_{\text{CC}} = 3.2 \text{V for V}_{\text{TH}} > 2.93 \text{V, no load} \end{aligned}$			0.5	1.0	μΑ	
	,	V <sub>CC</sub> = 5.5V, no load			1.0	1.75		
Detector Threshold	\/	Table 1	T <sub>A</sub> = +25°C	V <sub>TH</sub> - 1.5%	V <sub>TH</sub> \	√ <sub>TH</sub> + 1.5%	V	
Detector Threshold	V <sub>TH</sub>	Table I	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	V <sub>TH</sub> - 2.5%	V <sub>TH</sub> \	√ <sub>TH</sub> + 2.5%	V	
Valtaga Threadaglal I krataragia		MAX6406/MAX6407	/MAX6408		6.3		\ /	
Voltage Threshold Hysteresis	,	MAX6409/MAX6410	/MAX6411		9.5		mV	
Detector Threshold Tempco	ΔV <sub>TH</sub> /°C				40		ppm/°C	
	VIL	V= 4 0V				8.0		
MR Input	V <sub>IH</sub>	$V_{TH} > 4.0V$		2.0			V	
win iliput	VIL	V <sub>TH</sub> ≤ 4.0V				0.2 x V <sub>C</sub> C	CC	
	V <sub>IH</sub>			0.7 x V <sub>C</sub> C	)			
MR Minimum Input Pulse Width	t <sub>MD</sub>			1			μs	
MR Glitch Rejection					100		ns	
MR to OUT/OUT Delay					200		ns	
MR Pullup Resistance					50	75	kΩ	
Propagation Delay		$V_{CC} = (V_{TH} + 100m)$	V) to (V <sub>TH</sub> - 100mV)		20		μs	
1 Topagation Delay		$V_{CC} = (V_{TH} - 100mV)$	/) to (V <sub>TH</sub> + 100mV)		42		μδ	
Startup Time		$V_{CC} = 0$ to $V_{TH}$ (min	)		88		μs	
OUT Output Voltage Low (MAX6406/MAX6408/MAX6409/	Vol	I <sub>SINK</sub> = 1.6mA, V <sub>CC</sub> ≥ 2.1V, <del>OUT</del> asserted				0.3	V	
MAX6411)	VOL	$I_{SINK} = 100\mu A, V_{CC}$	≥ 1.2V, OUT asserted			0.4	V	
		ISOURCE = 500µA, V	CC = 3.2V, MAX6406,	0.8 x V <sub>C</sub> (	)			
OUT Output Voltage High (MAX6406/MAX6409)	Voн	ISOURCE = 800µA, V <sub>CC</sub> = 4.5V, V <sub>TH</sub> ≤ 4.38V, OUT not asserted		0.8 x V <sub>C</sub> (	)		V	
		ISOURCE = 800µA, V V <sub>TH</sub> ≥ 4.5V, OUT not		0.8 x V <sub>C</sub> (	)			

### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{CC} = 1.0 \text{V to } 5.5 \text{V}, T_A = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}, \text{ unless otherwise noted.}$  Typical values are at  $V_{CC} = 3 \text{V}$  and  $T_A = +25 ^{\circ}\text{C}$ .) (Note1)

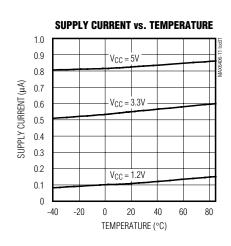
PARAMETER	SYMBOL	CONDITIONS		TYP	MAX	UNITS
OUT Output Voltage (MAX6407/MAX6410)	Vон	ISOURCE = 500µA, V <sub>CC</sub> ≥ 2.1V, OUT asserted		C		
		ISOURCE = 50μA, V <sub>CC</sub> ≥ 1.2V, OUT asserted	0.8 x V <sub>C</sub>	C		
	VoL	I <sub>SINK</sub> = 1.2mA, V <sub>CC</sub> ≥ 3.2V, OUT not asserted, MAX6407 only			0.3	V
		$I_{SINK} = 3.2$ mA, $V_{CC} \ge 4.5$ V, OUT not asserted, $V_{TH} \le 4.38$ V			0.4	
		$I_{SINK} = 3.2$ mA, $V_{CC} = V_{TH}$ (max), $V_{TH} \ge 4.5$ V, OUT not asserted			0.4	
Open-Drain OUT Output Leakage Current (Note 2)		OUT not asserted			0.1	μΑ

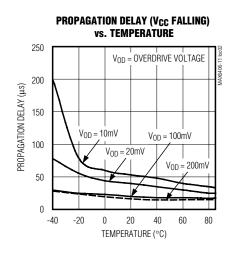
Note 1: Production testing done at +25°C only. Overtemperature limits are guaranteed by design and not production tested.

Note 2: Guaranteed by design.

## Typical Operating Characteristics

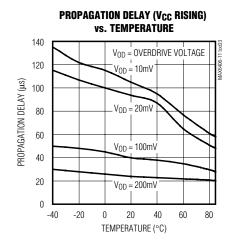
 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$ 

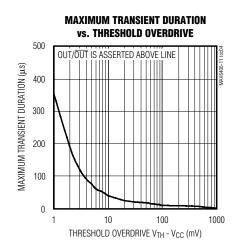




### Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$ 





## **Pin Description**

P	IN			
MAX6406/MAX6408 MAX6409/MAX6411	MAX6407/MAX6410	NAME	FUNCTION	
A1	A1	GND	Ground	
B1	_	OUT	Active-Low Output. OUT remains low while V <sub>CC</sub> is below the threshold.  OUT is open-drain on the MAX6408/MAX6411 and push-pull on the MAX6406/MAX6409.	
_	B1	OUT	Active-High Output. OUT remains high while V <sub>CC</sub> is below the threshold.	
B2	B2	MR	Active-Low Manual Reset. Internal $50k\Omega$ pullup to $V_{CC}$ . Pull low to assert the output. OUT remains asserted as long as $\overline{\text{MR}}$ is low. Leave unconnected or connect to $V_{CC}$ if unused.	
A2	A2	Vcc	Supply Voltage and Input for the Voltage Detector.	

**Table 1. Factory-Trimmed Thresholds** 

		Threshold Voltage, V <sub>TH</sub> (V)						
PART	SUFFIX		T <sub>A</sub> = +25°C	T <sub>A</sub> = -40°C to +85°C				
		MIN	TYP	MAX	MIN	MAX		
	22*	2.167	2.200	2.233	2.145	2.250		
	23*	2.285	2.320	2.355	2.262	2.375		
	24	2.364	2.400	2.436	2.340	2.460		
	25	2.462	2.500	2.537	2.437	2.562		
MAX6406BS	26*	2.591	2.630	2.669	2.564	2.692		
MAX6407BS MAX6408BS	27	2.660	2.700	2.741	2.633	2.768		
W/ WO40000	28	2.758	2.800	2.842	2.730	2.870		
	29*	2.886	2.930	2.974	2.857	3.000		
	30	2.955	3.000	3.045	2.925	3.075		
	31*	3.034	3.080	3.126	3.003	3.150		
	33	3.250	3.300	3.350	3.217	3.383		
	34	3.349	3.400	3.451	3.315	3.485		
	35	3.447	3.500	3.552	3.412	3.587		
	36	3.546	3.600	3.654	3.510	3.690		
	37	3.644	3.700	3.755	3.607	3.792		
	38	3.743	3.800	3.857	3.705	3.895		
MAX6409BS	39	3.841	3.900	3.958	3.802	3.997		
MAX6410BS MAX6411BS	40	3.940	4.000	4.060	3.900	4.100		
WII OKO TI I DO	41	4.038	4.100	4.161	3.997	4.202		
	42	4.137	4.200	4.263	4.095	4.305		
	43	4.235	4.300	4.364	4.192	4.407		
	44*	4.314	4.380	4.446	4.270	4.489		
	45	4.432	4.500	4.567	4.387	4.612		
	46*	4.560	4.630	4.699	4.514	4.746		

Factory-trimmed voltage thresholds are available in approximately 100mV increments with a 1.5% room temperature variance.

Note: Parts marked with an asterisk (\*) are standard versions.

### **Table 2. Device Marking Codes**

PART	TOP MARK	PART	TOP MARK	PART	TOP MARK
MAX6406BS31-T	AEF	MAX6407BS31-T	AEP	MAX6408BS31-T	AEZ
MAX6406BS30-T	AEE	MAX6407BS30-T	AEO	MAX6408BS30-T	AEY
MAX6406BS29-T	AED	MAX6407BS29-T	AEN	MAX6408BS29-T	AEX
MAX6406BS28-T	AEC	MAX6407BS28-T	AEM	MAX6408BS28-T	AEW
MAX6406BS27-T	AEB	MAX6407BS27-T	AEL	MAX6408BS27-T	AEV
MAX6406BS26-T	AEA	MAX6407BS26-T	AEK	MAX6408BS26-T	AEU
MAX6406BS25-T	ADZ	MAX6407BS25-T	AEJ	MAX6408BS25-T	AET
MAX6406BS24-T	ADY	MAX6407BS24-T	AEI	MAX6408BS24-T	AES
MAX6406BS23-T	ADX	MAX6407BS23-T	AEH	MAX6408BS23-T	AER
MAX6406BS22-T	ADW	MAX6407BS22-T	AEG	MAX6408BS22-T	AEQ

PART	TOP MARK	PART	TOP MARK	PART	TOP MARK
MAX6409BS46-T	AFN	MAX6410BS46-T	AAX	MAX6411BS46-T	ABL
MAX6409BS45-T	AFM	MAX6410BS45-T	AAW	MAX6411BS45-T	ABK
MAX6409BS44-T	AFL	MAX6410BS44-T	AAV	MAX6411BS44-T	ABJ
MAX6409BS43-T	AFK	MAX6410BS43-T	AAU	MAX6411BS43-T	ABI
MAX6409BS42-T	AFJ	MAX6410BS42-T	AAT	MAX6411BS42-T	ABH
MAX6409BS41-T	AFI	MAX6410BS41-T	AAS	MAX6411BS41-T	ABG
MAX6409BS40-T	AFH	MAX6410BS40-T	AAR	MAX6411BS40-T	ABF
MAX6409BS39-T	AFG	MAX6410BS39-T	AAQ	MAX6411BS39-T	ABE
MAX6409BS38-T	AFF	MAX6410BS38-T	AAP	MAX6411BS38-T	ABD
MAX6409BS37-T	AFE	MAX6410BS37-T	AAO	MAX6411BS37-T	ABC
MAX6409BS36-T	AFD	MAX6410BS36-T	AAN	MAX6411BS36-T	ABB
MAX6409BS35-T	AFC	MAX6410BS35-T	AAM	MAX6411BS35-T	ABA
MAX6409BS34-T	AFB	MAX6410BS34-T	AAL	MAX6411BS34-T	AAZ
MAX6409BS33-T	AFA	MAX6410BS33-T	AAK	MAX6411BS33-T	AAY

### **Detailed Description**

### Manual Reset Input

Many  $\mu P$ -based products require manual reset capability, allowing the operator, a test technician, or external logic circuit to initiate a reset. A logic low on  $\overline{\text{MR}}$  asserts OUT/OUT. OUT/OUT remains asserted while  $\overline{\text{MR}}$  is low. This input has an internal  $50\text{k}\Omega$  pullup resistor, so it can be left open if it is not used.  $\overline{\text{MR}}$  can be driven with TTL or CMOS logic levels, or with open-drain/collector outputs. Connect a normally open momentary switch from  $\overline{\text{MR}}$  to GND to create a manual reset function. If  $\overline{\text{MR}}$  is driven from long cables or if the device is used in a noisy environment, connect a 0.1 $\mu F$  capacitor from  $\overline{\text{MR}}$  to ground to provide additional noise immunity.

### **Applications Information**

# Interfacing to Different Logic Voltage Components

The MAX6408/MAX6411 have an active-low, opendrain output. This output structure will sink current when OUT is asserted. Connect a pullup resistor from OUT to any supply voltage up to 5.5V (Figure 1). Select a resistor value large enough to allow a valid logic low (see *Electrical Characteristics*), and small enough to register a logic high while supplying all input currents and leakage paths connected to the OUT line.

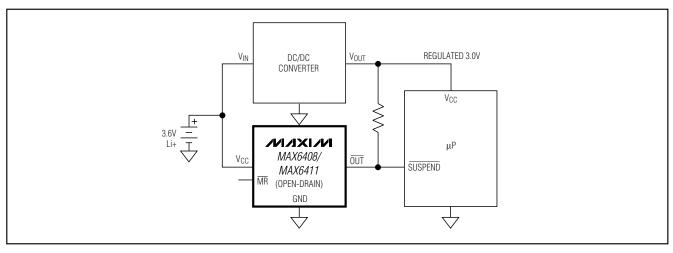


Figure 1. Interfacing to Different Logic Voltage Components

### **Negative-Going Vcc Transients**

These devices are relatively immune to short-duration, negative-going VCC transients (glitches).

The *Typical Operating Characteristics* show the Maximum Transient Duration vs. Threshold Overdrive graph, for which output pulses are not generated. The graph shows the maximum pulse width that a negative-going VCC transient may typically have before the devices issue output signals. As the amplitude of the transient increases, the maximum allowable pulse width decreases.

### **UCSP Reliability**

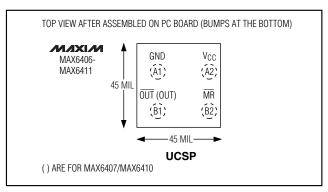
The chip-scale package (UCSP) represents a unique packaging form factor that may not perform equally to a packaged product through traditional mechanical reliability tests. CSP reliability is integrally linked to the user's assembly methods, circuit board material, and usage environment. The user should closely review these areas when considering use of a CSP package. Performance through Operating Life Test and Moisture Resistance remains uncompromised as it is primarily determined by the wafer-fabrication process.

Mechanical stress performance is a greater consideration for a CSP package. CSPs are attached through direct solder contact to the user's PC board, foregoing the inherent stress relief of a packaged product lead frame. Solder joint contact integrity must be considered. Information on Maxim's qualification plan, test data, and usage recommendations are detailed in the UCSP application note, which can be found on Maxim's website at www.maxim-ic.com.

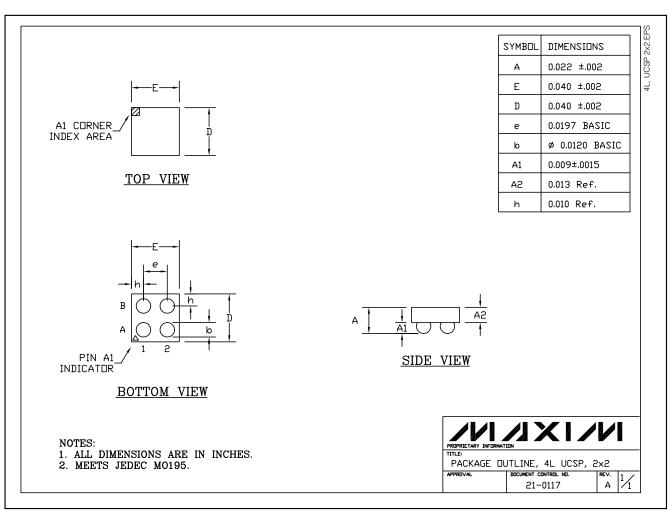
## **Chip Information**

TRANSISTOR COUNT: 512 PROCESS: BICMOS

### **Pin Configuration**



### Package Information



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.