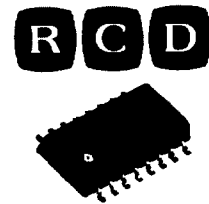


SURFACE MOUNT PASSIVE DELAY LINES

SERIES SMP1410 SMALL OUTLINE 14 PIN, 10-TAP

SERIES SMP1610 SMALL OUTLINE 16 PIN, 10-TAP

NEW
PRODUCT



FEATURES

- Economical cost, prompt delivery
- SO and Quad formats with 50-mil pin spacing
- Available on 24mm embossed plastic Tape & Reel
- 0.002" Co-planarity
- Fast rise times
- Operating temperature: 0°C to +70°C

OPTIONS

- 28 pin surface mount J-lead quad package (.450" sq)
- Custom circuits available
- Non-standard delay or impedance values
- Tighter tolerance or temp. coefficient
- Faster rise times
- 100-mil pin spacing available
- Military screening per MIL-D-83532

SPECIFICATIONS

Total Delay (nSec)	Rise Time Max. (nSec)	Delay per Tap (nSec)	Attenuation Max. (%)	Available Impedance Values ($\pm 10\%$)*
10	2.5	1 $\pm$.5	3	50 Ω , 100 Ω , 200 Ω
20	4	2 $\pm$.5	3	50 Ω , 100 Ω , 200 Ω
30	6	3 $\pm$ 1	4	50 Ω , 100 Ω , 200 Ω
40	8	4 $\pm$ 1.5	4	50 Ω , 100 Ω , 200 Ω
50	10	5 $\pm$ 1.5	8	50 Ω , 100 Ω , 200 Ω
75	15	7.5 $\pm$ 1.5	8	50 Ω , 100 Ω , 200 Ω
100	20	10 $\pm$ 2	10	50 Ω , 100 Ω , 200 Ω
125	25	12.5 $\pm$ 2	10	50 Ω , 100 Ω
150	30	15 $\pm$ 2	10	50 Ω , 100 Ω
200	40	20 $\pm$ 2	10	50 Ω , 100 Ω

* 100 Ω is the most common impedance value.

RCD SMP Series passive (analog) surface mount delay lines are a lumped constant design per applicable portions of MIL-D-23859. The series incorporates high performance inductors and multi layer capacitors in a molded package ensuring stable transmission, low temperature coefficient, and excellent environmental performance.

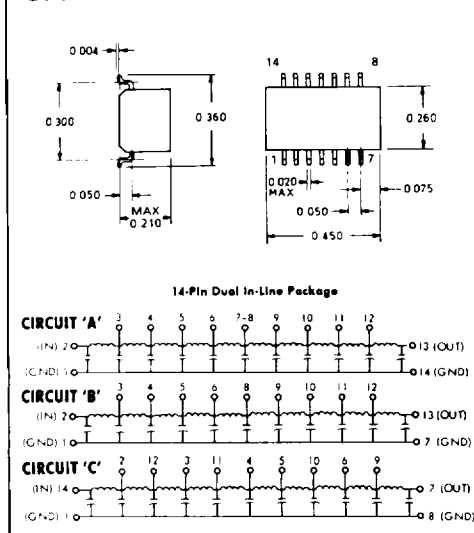
ELECTRICAL CHARACTERISTICS

Total Delay Tol.: $\pm 5\%$ or 1nS whichever is greater
 Temperature Coefficient: 100ppm/ $^{\circ}$ C Max.
 Dielectric Strength: 100VDC
 Insulation Resistance: 1000M Ω Min.
 Distortion: $\pm 10\%$ Max.

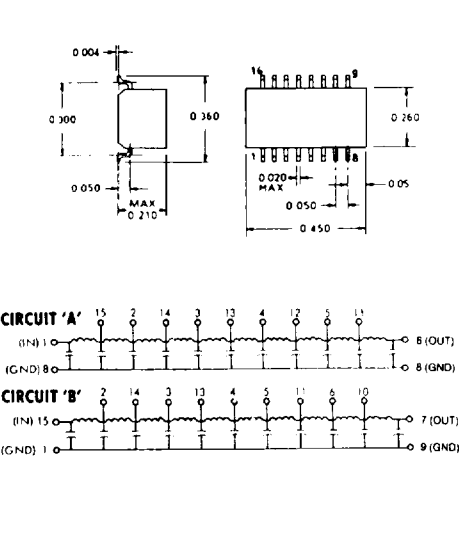
TEST CONDITIONS @25 $^{\circ}$ C

- 1) Input test pulse shall have an amplitude of 3V, rise time of 3nS max., pulse width of 3 $\times$ the total delay
- 2) Delay line to be terminated to within 1% of its characteristic impedance
- 3) Delay time measured from 50% of input pulse to 50% of output pulse
- 4) Rise time measured from 10% to 90% of output pulse

SMP 1410



SMP 1610



HOW TO ORDER

SMP1410 50nS 100 Ω A T

RCD Type (SMP1410 or SMP1610)
 Total Delay
 Impedance (50 Ω , 100 Ω , 200 Ω)
 Circuit (A, B, C)
 Packaging
 'T' is Tape & Reel (leave blank for standard packaging).