Single Schmitt-Trigger Inverter

The NLU1G14 MiniGate[™] is an advanced high-speed CMOS Schmitt-trigger inverter in ultra-small footprint.

The NLU1G14 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

The NLU1G14 can be used to enhance noise immunity or to square up slowly changing waveforms.

Features

- High Speed: $t_{PD} = 4.0 \text{ ns (Typ)} @ V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation: $I_{CC} = 1 \mu A$ (Max) at $T_A = 25^{\circ}C$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb-Free Devices

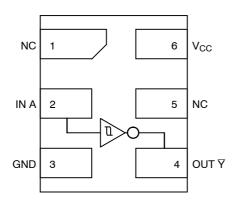


Figure 1. Pinout (Top View)

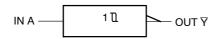


Figure 2. Logic Symbol

PIN ASSIGNMENT

1	NC
2	IN A
3	GND
4	OUT ₹
5	NC
6	V _{CC}

FUNCTION TABLE

Α	Υ
L	H
H	L



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MARKING DIAGRAMS



UDFN6 MU SUFFIX CASE 517AA





ULLGA6 1.0 x 1.0 CASE 613AD





ULLGA6 1.2 x 1.0 CASE 613AE





ULLGA6 1.45 x 1.0 CASE 613AF





UDFN6 1.0 x 1.0 CASE 517BX





UDFN6 1.45 x 1.0 CASE 517AQ



P = Device Marking M = Date Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V
V _{IN}	DC Input Voltage	-0.5 to +7.0	V
V _{OUT}	DC Output Voltage	-0.5 to +7.0	V
I _{IK}	DC Input Diode Current V _{IN} < GND	-20	mA
lok	DC Output Diode Current V _{OUT} < GND	±20	mA
I _O	DC Output Source/Sink Current	±12.5	mA
I _{CC}	DC Supply Current Per Supply Pin	±25	mA
I _{GND}	DC Ground Current per Ground Pin	±25	mA
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
TJ	Junction Temperature Under Bias	150	°C
MSL	Moisture Sensitivity	Level 1	
F _R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V _{ESD}	ESD Withstand Voltage Human Body Model (Note 2) Machine Model (Note 3) Charged Device Model (Note 4)	> 2000 > 200 N/A	V
I _{LATCHUP}	Latchup Performance Above V _{CC} and Below GND at 125°C (Note 5)	±500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

- Tested to EIA / JESD22-A114-A.
 Tested to EIA / JESD22-A115-A.
- 4. Tested to JESD22-C101-A.
- 5. Tested to EIA / JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	1.65	5.5	٧
V _{IN}	Digital Input Voltage	0	5.5	V
V _{OUT}	Output Voltage	0	5.5	V
T _A	Operating Free-Air Temperature	-55	+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate $ V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V} $ $ V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V} $	0 0	No Limit No Limit	ns/V

DC ELECTRICAL CHARACTERISTICS

			V _{cc}		T _A = 25 °C	;	T _A = -	+85°C		55°C to 25°C	
Symbol	Parameter	Conditions	(V)	Min	Тур	Max	Min	Max	Min	Max	Unit
V _{T+}	Positive Threshold Voltage		3.0 4.5 5.5	1.85 2.86 3.50	2.0 3.0 3.6	2.2 3.15 3.85		2.2 3.15 3.85		2.2 3.15 3.85	V
V _{T-}	Negative Threshold Voltage		3.0 4.5 5.5	0.9 1.35 1.65	1.5 2.3 2.9	1.65 2.46 3.05	0.9 1.35 1.65		0.9 1.35 1.65		V
V _H	Hysteresis Voltage		3.0 4.5 5.5	0.30 0.40 0.50	0.57 0.67 0.74	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	V
V _{OH}	Minimum High-Level Output	$V_{IN} \le V_{T-MIN}$ $I_{OH} = -50 \mu A$	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		1.9 2.9 4.4		V
	Voltage	$V_{IN} \le V_{T-MIN}$ $I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	3.0 4.5	2.58 3.94			2.48 3.80		2.34 3.66		
V _{OL}	Maximum Low-Level Output	$V_{IN} \ge V_{T+MAX}$ $I_{OL} = 50 \mu A$	2.0 3.0 4.5		0 0 0	0.1 0.1 0.1		0.1 0.1 0.1		0.1 0.1 0.1	V
	Voltage	$\begin{aligned} &V_{IN} \geq V_{T+MAX} \\ &I_{OL} = 4 \text{ mA} \\ &I_{OL} = 8 \text{ mA} \end{aligned}$	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52	
I _{IN}	Input Leakage Current	$0 \le V_{IN} \le 5.5 V$	0 to 5.5			±0.1		±1.0		±1.0	μΑ
I _{CC}	Quiescent Supply Current	V _{IN} = 5.5 V or GND	5.5			1.0		10		40	μΑ

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0 \text{ ns}$)

		V _{CC}	Test		T _A = 25 °	С	T _A =	+85°C	T _A = -5 +12		
Symbol	Parameter	(V)	Condition	Min	Тур	Max	Min	Max	Min	Max	Unit
t _{PLH} ,	Propagation Delay,	3.0 to	C _L = 15 pF		7.0	12.8	1.0	15.0	1.0	17.0	ns
t _{PHL}	Input A to Output ₹	3.6	C _L = 50 pF		8.5	16.3	1.0	18.5	1.0	20.5	
		4.5 to	C _L = 15 pF		4.0	8.6	1.0	10.0	1.0	11.5	
		5.5	C _L = 50 pF		5.5	10.6	1.0	12.0	1.0	13.5	
C _{IN}	Input Capacitance				5	10		10		10.0	pF
C _{PD}	Power Dissipation Capacitance (Note 6)	5.0			7.0						pF

^{6.} C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no-load dynamic power consumption: P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

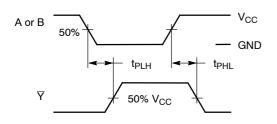
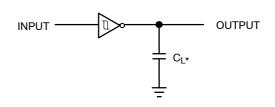


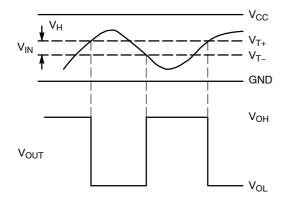
Figure 3. Switching Waveforms

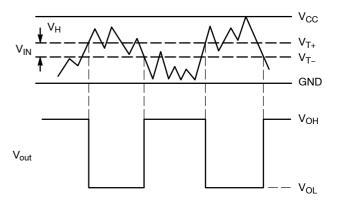


*Includes all probe and jig capacitance.

A 1-MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit





(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times

(b) A Schmitt-Trigger Offers Maximum Noise Immunity

Figure 5. Typical Schmitt-Trigger Applications

ORDERING INFORMATION

Device	Package	Shipping [†]
NLU1G14MUTCG	UDFN6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLU1G14AMX1TCG	ULLGA6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLU1G14BMX1TCG	ULLGA6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLU1G14CMX1TCG	ULLGA6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel
NLU1G14AMUTCG (In Development)	UDFN6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLU1G14CMUTCG (In Development)	UDFN6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

UDFN6 1.45x1.0, 0.5P CASE 517AQ ISSUE O D Α NOTES: NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP. В **DETAIL A** PIN ONE REFERENCE Ε OPTIONAL CONSTRUCTIONS MILLIMETERS | MILLIME | ENG | DIM | MIN | MAX | A | 0.45 | 0.55 | A1 | 0.00 | 0.05 | A2 | 0.07 | REF 0.10 C EXPOSED Cu MOLD CMPD **TOP VIEW** b 0.20 0.30 D 1.45 BSC |△| 0.10 | C 1.45 BSC 1.00 BSC Ē 0.50 BSC е DETAIL B **DETAIL B** L 0.30 0.40 L1 --- 0.15 OPTIONAL CONSTRUCTIONS 0.05 C **MOUNTING FOOTPRINT** 0.05 C **A1** C SEATING PLANE 6X 0.30 **A2 SIDE VIEW** PACKAGE OUTLINE е 6X L 1.24 DETAIL A 0.53 0.50 **PITCH** DIMENSIONS: MILLIMETERS

0.10

0.05

BOTTOM VIEW

C A B

C NOTE 3

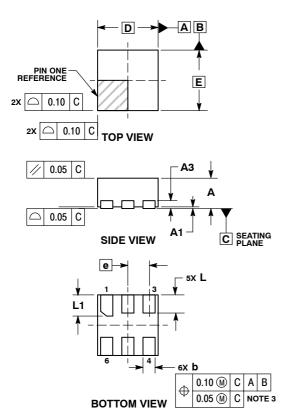
*For additional information on our Pb-Free strategy and soldering

Mounting Techniques Reference Manual, SOLDERRM/D.

details, please download the ON Semiconductor Soldering and

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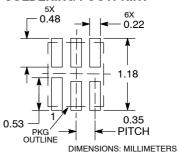
UDFN6 1.0x1.0, 0.35P CASE 517BX ISSUE O



- NOTES:
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 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

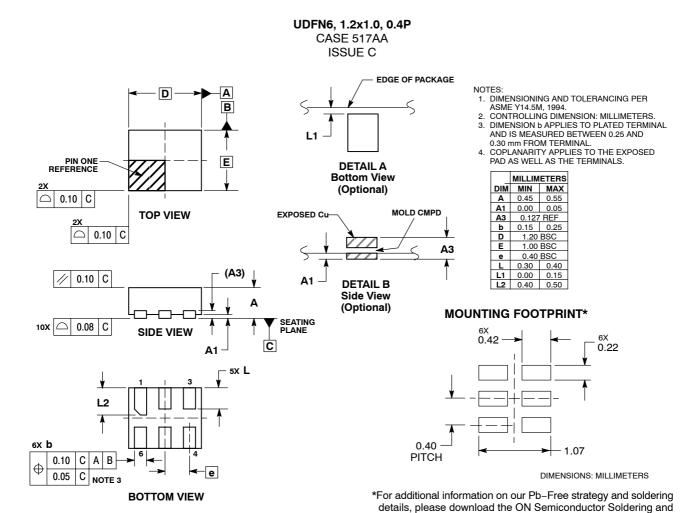
	MILLIMETERS					
DIM	MIN	MAX				
Α	0.45	0.55				
A1	0.00	0.05				
А3	0.13 REF					
b	0.12	0.22				
D	1.00	BSC				
Е	1.00	BSC				
е	0.35	BSC				
L	0.25	0.35				
L1	0.30	0.40				

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

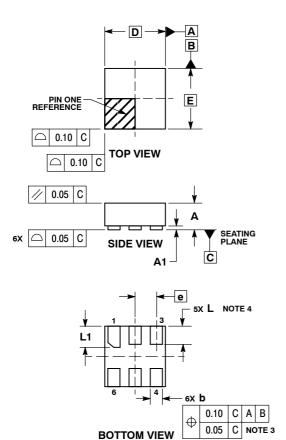
PACKAGE DIMENSIONS



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PACKAGE DIMENSIONS

ULLGA6 1.0x1.0, 0.35P CASE 613AD **ISSUE A**

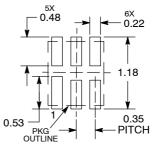


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- DIMENSIONING AND TOLERANCING PER
- 1. DIMENSIONING AND TOLERANGING FER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND
- 0.30 mm FROM THE TERMINAL TIP.
 4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

	MILLIMETERS				
DIM	MIN	MAX			
Α	-	0.40			
A1	0.00	0.05			
b	0.12	0.22			
D	1.00	BSC			
E	1.00	BSC			
е	0.35 BSC				
L	0.25	0.35			
L1	0.30	0.40			

MOUNTING FOOTPRINT SOLDERMASK DEFINED*

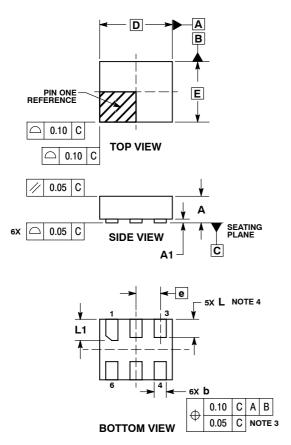


DIMENSIONS: MILLIMETERS

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

ULLGA6 1.2x1.0, 0.4P CASE 613AE ISSUE A



- NOTES:

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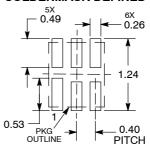
 2. CONTROLLING DIMENSION: MILLIMETERS.

 3. DIMENSION 5 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.

 4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED. PACKAGE IS ALLOWED.

Γ		MILLIMETERS					
	DIM	MIN	MAX				
Г	Α		0.40				
	A1	0.00	0.05				
	b	0.15	0.25				
	D	1.20	BSC				
Г	Е	1.00	BSC				
	е	0.40 BSC					
	L	0.25	0.35				
	L1	0.35	0.45				

MOUNTING FOOTPRINT SOLDERMASK DEFINED*

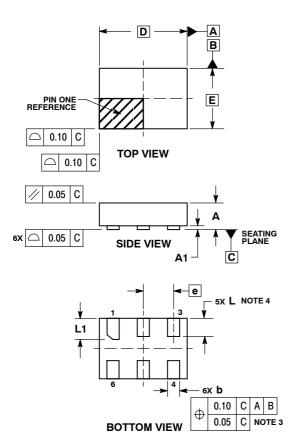


DIMENSIONS: MILLIMETERS

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

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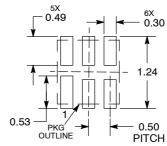


NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND
- 0.30 mm FROM THE TERMINAL TIP. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

	MILLIMETERS				
DIM	MIN	MAX			
Α		0.40			
A1	0.00	0.05			
b	0.15	0.25			
D	1.45	BSC BSC			
Е	1.00				
е	0.50 BSC				
L	0.25	0.35			
L1	0.30	0.40			

MOUNTING FOOTPRINT SOLDERMASK DEFINED*



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