

KSD1020

Audio Frequency Amplifier

Complement to KSB810



1.Emitter 2. Collector 3. Base

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a =25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{CBO}	Collector-Base Voltage	30	V
V _{CEO}	Collector-Emitter Voltage	25	V
V _{EBO}	Emitter-Base Voltage	5.0	V
I _C	Collector Current (DC)	700	mA
I _{CP}	* Collector Current (Pulse)	1.0	Α
P _C	Collector Power Dissipation	350	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

^{*} PW≤10ms, Duty Cycle≤50%

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	V_{CB} =30V, I_{E} =0			100	nA
I _{EBO}	Emitter Cut-off Current	V_{EB} =5V, I_{C} =0			100	nA
h _{FE1}	* DC Current Gain	V _{CE} =1V, I _C =100mA	120	200	400	
h_{FE2}		V _{CE} =1V, I _C =700mA	35	140		
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} =6V, I _C =10mA	600	640	700	mV
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =700mA, I _B =70mA		0.2	0.4	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =700mA, I _B =70mA		0.95	1.2	V
C _{ob}	Output Capacitance	V _{CB} =6V, I _E =0, f=1MHz		13	25	pF
f _T	Current Gain Bandwidth Product	V _{CE} =6V, I _C =10mA	50	170		MHz
Pulse Test DW/250va Duty Cycle C 20/					l	

^{*} Pulse Test: PW≤350μs, Duty Cycle≤ 2%

h_{FE1} Classification

Classification	Y	G	
h _{FE1}	120 ~ 240	200 ~ 400	

©2004 Fairchild Semiconductor Corporation Rev. B2, April 2004

Typical Characteristics

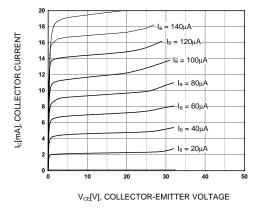


Figure 1. Static Characteristic

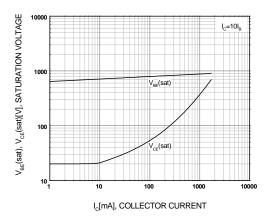


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

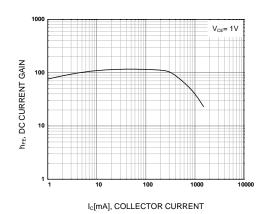


Figure 2. DC current Gain

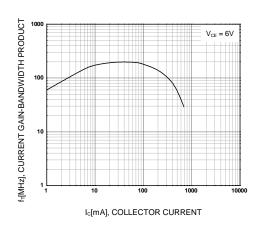
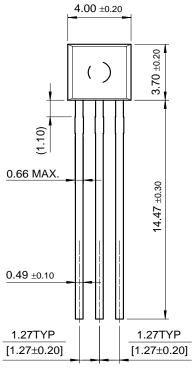


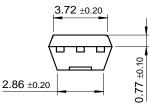
Figure 4. Current Gain-Bandwidth Product

©2004 Fairchild Semiconductor Corporation Rev. B2, April 2004

Package Dimensions

TO-92S







Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

$ACEx^{TM}$	FACT Quiet Series™	ImpliedDisconnect™	PACMAN™	SPM™
ActiveArray™	FAST [®]	ISOPLANAR™	POP^{TM}	Stealth™
Bottomless™	FASTr™	LittleFET™	Power247™	SuperFET™
CoolFET™	FPS™	MICROCOUPLER™	PowerSaver™	SuperSOT™-3
$CROSSVOLT^{TM}$	FRFET™	MicroFET™	PowerTrench [®]	SuperSOT™-6
DOME™	GlobalOptoisolator™	MicroPak™	QFET [®]	SuperSOT™-8
EcoSPARK™	GTO™	MICROWIRE™	QS™	SyncFET™
E ² CMOS TM	HiSeC™	MSX™	QT Optoelectronics™	TinyLogic [®]
EnSigna™	I ² C TM	MSXPro™	Quiet Series™	TINYOPTO™
FACT™	i-Lo™	OCX^{TM}	RapidConfigure™	TruTranslation™
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UHC™
The Power Francl	nise [®]	OPTOLOGIC [®]	SILENT SWITCHER®	UltraFET [®]
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2004 Fairchild Semiconductor Corporation Rev. I10