Data Sheet

LQM18PN_FR Series 0603/1608 (inch/mm)





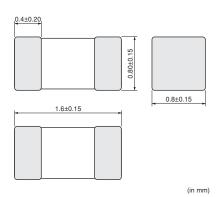








Dimensions



Packaging

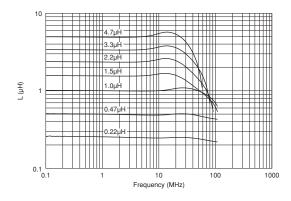
Code	Packaging	Minimum Quantity	
L	ø180mm Embossed Taping	4000	
В	Packing in Bulk	1000	

■ Rated Value (□: packaging code)

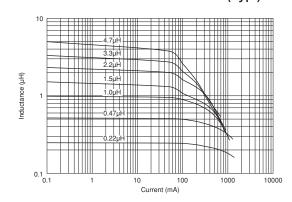
Part Number	Inductance	Inductance Test Frequency	Rated Current	DC Resistance	Self-Resonance Frequency (min.)
LQM18PNR22NFR□	0.22μH ±30%	1MHz	1250mA	0.11Ω±25%	100MHz
LQM18PNR47NFR□	0.47µH ±30%	1MHz	1100mA	$0.15\Omega \pm 25\%$	100MHz
LQM18PN1R0MFR□	1.0µH ±20%	1MHz	950mA	$0.20\Omega \pm 25\%$	100MHz
LQM18PN1R5MFR□	1.5µH ±20%	1MHz	800mA	$0.23\Omega \pm 25\%$	100MHz
LQM18PN2R2MFR□	2.2µH ±20%	1MHz	750mA	$0.30\Omega \pm 25\%$	70MHz
LQM18PN3R3MFR□	3.3µH ±20%	1MHz	700mA	0.35Ω±25%	60MHz
LQM18PN4R7MFR□	4.7µH ±20%	1MHz	620mA	0.44Ω±25%	40MHz

Class of Magnetic Shield: Magnetic shield of ferrite Operating Temperature Range: -40~85°C

■ Inductance-Frequency Characteristics (Typ.)



■ Inductance-Current Characteristics (Typ.)



Continued on the following page.

This data sheet is applied for CHIP INDUCTORS (CHIP COILS) used for General Electronics equipment for your design.

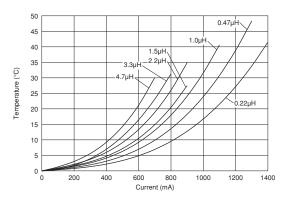
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Data Sheet



Continued from the preceding page.

■ Temperature Rise Characteristics (Typ.)



■ ①Caution/Notice

Do not use products beyond the rated current as this may create excessive heat.

Notice

Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.

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⚠ Note

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