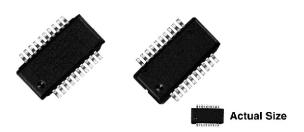


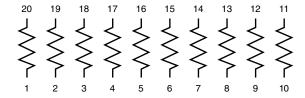


Molded, 25 mil Pitch, Dual-In-Line Thin Film Resistor, Surface Mount Network



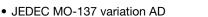
OSOP Series resistor networks feature a space saving 25 mil lead pitch versus the current 50 mil pitch standard. This allows users to reduce board space more than 50 % over current standards. The OSOP Series feature 10 isolated resistors in a 20 lead style available for immediate delivery in the standard values listed.

SCHEMATIC



FEATURES

- 0.068" (1.73 mm) maximum seated height
- Rugged molded case construction with no internal solder



- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition



Note

* Pb containing terminations are not RoHS compliant, exemptions may apply

TYPICAL PERFORMANCE

	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.05

STANDARD RESISTANCE OFFERING (R ₁ =)		
500 Ω	10 kΩ	
1 kΩ	20 kΩ	
2 kΩ	50 kΩ	
5 kΩ	100 kΩ	

Note

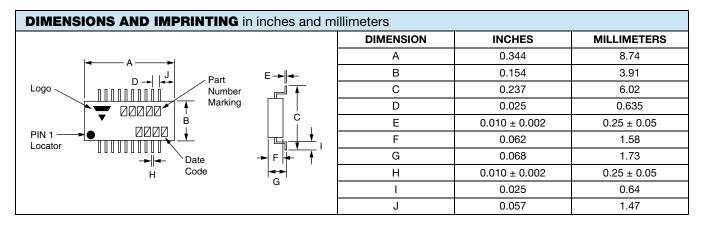
· Consult factory for additional values and schematics

TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	20	=
Resistance Range	500 Ω to 100 k Ω per resistor	=
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C
TCR: Tracking	± 5 ppm/°C	- 55 °C to + 125 °C
Tolerance: Absolute	± 0.1 % to 1 %	+ 25 °C
Tolerance: Ratio	± 0.025 % to 0.5 %	+ 25 °C
Power Rating: Resistor	100 mW	Maximum at + 70 °C
Power Rating: Package	400 mW	Maximum at + 70 °C
Stability: Absolute	ΔR ± 0.05 %	2000 h at + 70 °C
Stability: Ratio	ΔR ± 0.015 %	2000 h at + 70 °C
Voltage Coefficient	< 0.1 ppm/V (typical)	=
Working Voltage	100 V max. not to exceed √P x R	=
Operating Temperature Range	- 55 °C to + 125 °C	=
Storage Temperature Range	- 55 °C to + 150 °C	=
Noise	< - 30 dB	=
Thermal EMF	0.08 μV/°C	=
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C
Shelf Life Stability: Ratio	ΔR ± 0.002 %	1 year at + 25 °C

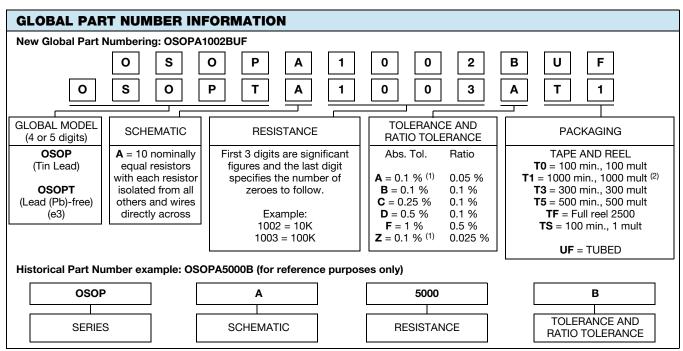
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Vishay Dale Thin Film



MECHANICAL SPECIFICATIONS		
Resistive Element	Passivated nichrome	
Substrate Material	Silicon	
Body	Molded epoxy	
Terminals	Copper alloy	
Lead (Pb)-free Option	100 % matte tin	
Tin Lead Option	Sn90	
Tin Lead and Lead (Pb)-free Finish	Plated	



Notes

- (1) Tolerance available 1K and up
- (2) Preferred packaging code



Legal Disclaimer Notice

Vishay

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