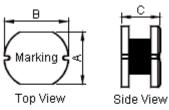


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## MCSDC0503-331KU

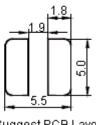
	REVISIONS							
ECN #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
-	Α	RELEASED	Ashok	10/2/11	Jagan	10/2/11	Farnell	24/2/11
						·		

# **Configurations and Dimensions**



Α	4.8 ±0.5 mm	-
В	5 ±0.3 mm	-
С	3 ±0.3 mm	-
D	2 mm	(Reference)





Suggest PCB Layout

Dimensions : Millimetres

Marking: 331

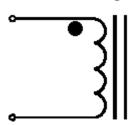
## **Electrical Characteristics**

(at 25°C)

Test Condition		
1KHz 1V	L	330μH ±10%
at 25°C	DCR 3.3Ω (Maximum)	
1KHz 1V I <sub>rms</sub> = 0.21A	ΔΤ	Temperature Rise 40°C (Maximum)

Operating temperature: -55°C to +130°C

## **Schematic Diagram**





- 1. Wire Ø0.1mm x 1P 2UEF1/U 155°C
- 2. 114.5TS (Reference)

#### **Test Data for Mechanical**

Test Item	A mm	B mm	C mm	D mm
Specification	4.8 ±0.5 5 ±0.3		3 ±0.3	2 (Reference)
1	4.6	4.86	3.05	1.74
2	4.54	4.88	3.02	1.77
3	4.56	4.85	3.03	1.74
4	4.61	4.87	3.02	1.81
5	4.53	4.85	3.03	1.68
Average	4.57	4.86	3.03	1.75

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Jagan	10/02/11
APPROVED BY:	DATE:
Farnell	24/02/11

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PART NO.

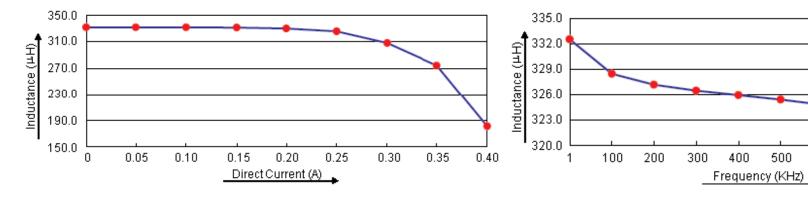
## MCSDC0503-331KU

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#### **Test Data for Electrical**

Test Item	L μH	DCR Ω	ΔΤ
Condition	1KHz 1V	at 25°C	1KHz 1V Irms = 0.21A
Specification	330 ±10%	3.3 (Maximum)	Temperature rise 40°C (Maximum)
1	333.9	2.69	OK
2	332.6	2.71	OK
3	330.8	2.74	OK
4	331.4	2.67	OK
5	331.5	2.73	OK
Average	332.04	2.71	ОК

### **Electric Characteristics**



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APPROVED BY:	DATE:
Farnell	24/02/11

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## MCSDC0503-331KU

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# **Reliability Test**

Test Item	Specifications	Test Method and Remarks				
Operating temperature range	-55°C to +130°C	Including temperature rise due to self-generated heat				
Storage condition	Ambient temperature : 0°C to 40°C Humidity : Below 70%RH	To maintain the solderability of terminal electrodes, care must be taken to control temperature and humidity in the storage area.				
Moisture sensitivity	Appearance : No abnormality No damage DCR change : Within ±20% Inductance change : Within ±20%	According to J-STD-020B level 3 Test condition :60°C 60% RH Test duration :40 hours Recovery :1 to 2 hours of recovery under the standard condition after the removal from the test chamber.				
Solderability	All termination shall exhibit a continuous solder coating free from defects for a minimum of 90% of the surface area of any individual lead.	According to J-STD-002B  Steam aging category : 97°C 98% RH  Steam aging duration : 8 hours  Solder : Lead-free solder  Solder temperature : 260 ±5°C  Dip time : 5 +0/-0.5 seconds.				

#### **Material List**

No.	Item	Material Description				
1	Core	R5A CDR5 x 3 (ST) B2 F1.5				
2	Wire	Ø0.1mm x 1P 2UEF1/U 155°C				
3	Solder (Lead Free)	Sn99.3%/Cu0.7%				
4	Glue	TH320				

## **Part Number Table**

Description	Part Number				
Inductors, 330μH, 10%, SMD	MCSDC0503-331KU				

http://www.farnell.com

http://www.newark.com

http://www.cpc.co.uk

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Farnell	24/02/11			

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