



SEMITRANS® 2

IGBT Modules

SKM 75GB123D

SKM 75GAL123D

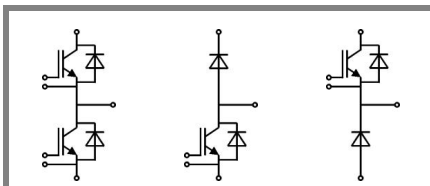
SKM 75GAR123D

Features

- MOS input (voltage controlled)
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \times I_{Cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (10 mm) and creepage distance (20 mm)

Typical Applications*

- AC inverter drives
- UPS



GB

GAL

GAR

Absolute Maximum Ratings			$T_c = 25\text{ }^{\circ}\text{C}$, unless otherwise specified	
Symbol	Conditions		Values	Units
IGBT				
V_{CES}	$T_j = 25\text{ }^{\circ}\text{C}$		1200	V
I_C	$T_j = 150\text{ }^{\circ}\text{C}$	$T_{case} = 25\text{ }^{\circ}\text{C}$	75	A
		$T_{case} = 80\text{ }^{\circ}\text{C}$	60	A
I_{CRM}	$I_{CRM}=2 \times I_{Cnom}$		150	A
V_{GES}			± 20	V
t_{psc}	$V_{CC} = 600\text{ V}; V_{GE} \leq 20\text{ V}; T_j = 125\text{ }^{\circ}\text{C}$ $V_{CES} < 1200\text{ V}$		10	μs
Inverse Diode				
I_F	$T_j = 150\text{ }^{\circ}\text{C}$	$T_{case} = 25\text{ }^{\circ}\text{C}$	75	A
		$T_{case} = 80\text{ }^{\circ}\text{C}$	50	A
I_{FRM}	$I_{FRM}=2 \times I_{Fnom}$		150	A
I_{FSM}	$t_p = 10\text{ ms}; \sin.$	$T_j = 150\text{ }^{\circ}\text{C}$	480	A
Freewheeling Diode				
I_F	$T_j = 150\text{ }^{\circ}\text{C}$	$T_{case} = 25\text{ }^{\circ}\text{C}$	95	A
		$T_{case} = 80\text{ }^{\circ}\text{C}$	65	A
I_{FRM}	$I_{FRM}=2 \times I_{Fnom}$		200	A
I_{FSM}	$t_p = 10\text{ ms}; \sin$	$T_j = 150\text{ }^{\circ}\text{C}$	720	A
Module				
$I_{t(RMS)}$			200	A
T_{vj}			- 40 ...+ 150	$^{\circ}\text{C}$
T_{stg}			- 40 ...+ 125	$^{\circ}\text{C}$
V_{isol}	AC, 1 min.		2500	V

Characteristics			T _c = 25 °C, unless otherwise specified			
Symbol	Conditions		min.	typ.	max.	Units
IGBT						
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 2 mA		4,5	5,5	6,5	V
I _{CES}	V _{GE} = 0 V, V _{CE} = V _{CES}	T _j = 25 °C		0,1	0,3	mA
V _{CE0}		T _j = 25 °C		1,4	1,6	V
		T _j = 125 °C		1,6	1,8	V
r _{CE}	V _{GE} = 15 V	T _j = 25°C		22	28	mΩ
		T _j = 125°C		30	38	mΩ
V _{CE(sat)}	I _{Cnom} = 50 A, V _{GE} = 15 V	T _j = °C _{chiplev.}		2,5	3	V
C _{ies}	V _{CE} = 25, V _{GE} = 0 V	f = 1 MHz		3,3	4,3	nF
C _{oes}				0,5	0,6	nF
C _{res}				0,22	0,3	nF
Q _G	V _{GE} = -8 - +20V			500		nC
R _{Gint}	T _j = °C			5		Ω
t _{d(on)}	R _{Gon} = 22 Ω	V _{CC} = 600V I _C = 50A		44	100	ns
t _r				56	100	ns
E _{on}				8		mJ
t _{d(off)}	R _{Goff} = 22 Ω	T _j = 125 °C V _{GE} = ±15V		380	500	ns
t _f				70	100	ns
E _{off}				5		mJ
R _{th(j-c)}	per IGBT				0,27	K/W



SEMITRANS® 2

IGBT Modules

SKM 75GB123D

SKM 75GAL123D

SKM 75GAR123D

Features

- MOS input (voltage controlled)
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \times I_{cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (10 mm) and creepage distance (20 mm)

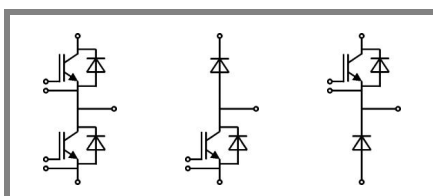
Typical Applications*

- AC inverter drives
- UPS

Characteristics					
Symbol	Conditions	min.	typ.	max.	Units
Inverse Diode					
$V_F = V_{EC}$	$I_{Fnom} = 50 \text{ A}; V_{GE} = 0 \text{ V}$				
	$T_j = 25^\circ\text{C}_{chiplev.}$		2	2,5	V
	$T_j = 125^\circ\text{C}_{chiplev.}$		1,8		V
V_{F0}	$T_j = 25^\circ\text{C}$		1,1	1,2	V
	$T_j = 125^\circ\text{C}$				V
r_F	$T_j = 25^\circ\text{C}$		18	26	mΩ
	$T_j = 125^\circ\text{C}$				mΩ
I_{RRM}	$I_F = 50 \text{ A}$		35		A
Q_{rr}	$di/dt = 800 \text{ A}/\mu\text{s}$				μC
E_{rr}	$V_{GE} = 0 \text{ V}; V_{CC} = 600 \text{ V}$				mJ
$R_{th(j-c)D}$	per diode			0,6	K/W
Freewheeling Diode					
$V_F = V_{EC}$	$I_{Fnom} = 50 \text{ A}; V_{GE} = 0 \text{ V}$				
	$T_j = 25^\circ\text{C}_{chiplev.}$		1,85	2,2	V
	$T_j = 125^\circ\text{C}_{chiplev.}$		1,6		V
V_{F0}	$T_j = 25^\circ\text{C}$		1,1	1,2	V
	$T_j = 125^\circ\text{C}$				V
r_F	$T_j = 25^\circ\text{C}$		15	20	V
	$T_j = 125^\circ\text{C}$				V
I_{RRM}	$I_F = 50 \text{ A}$		40		A
Q_{rr}					μC
E_{rr}	$V_{GE} = 0 \text{ V}; V_{CC} = 600 \text{ V}$				mJ
$R_{th(j-c)FD}$	per diode			0,5	K/W
Module					
L_{CE}				30	nH
$R_{CC'+EE'}$	res., terminal-chip	$T_{case} = 25^\circ\text{C}$	0,75		mΩ
		$T_{case} = 125^\circ\text{C}$	1		mΩ
$R_{th(c-s)}$	per module			0,05	K/W
M_s	to heat sink M6		3	5	Nm
M_t	to terminals M5		2,5	5	Nm
w				160	g

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our personal.



GB

GAL

GAR

SKM 75GB123D



SEMITRANS® 2

IGBT Modules

SKM 75GB123D
SKM 75GAL123D
SKM 75GAR123D

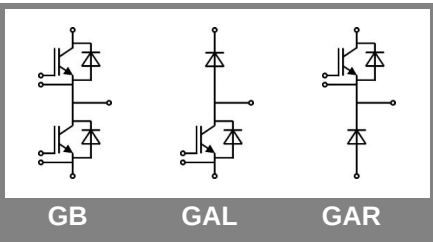
Features

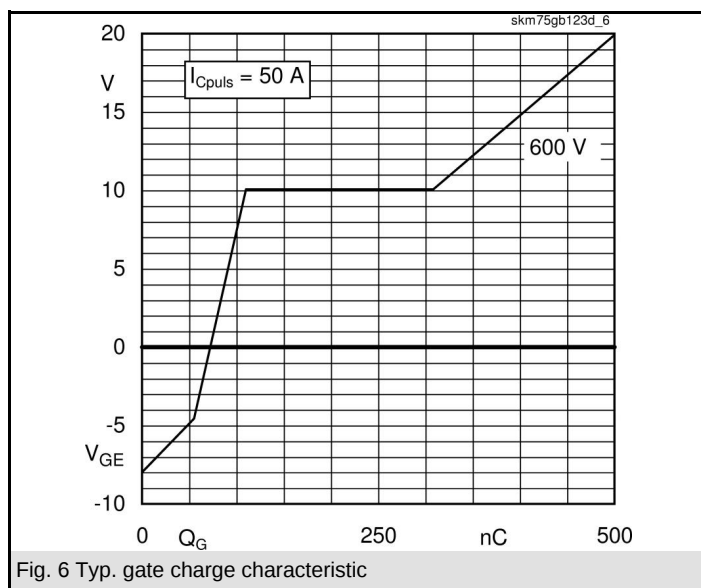
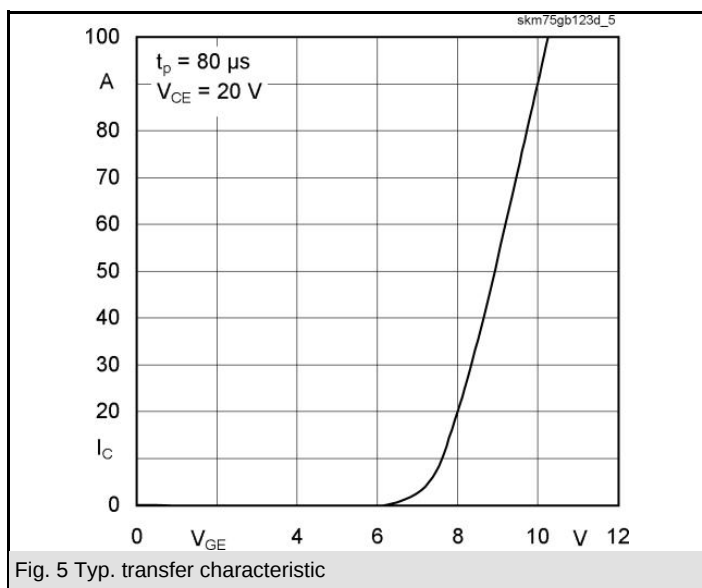
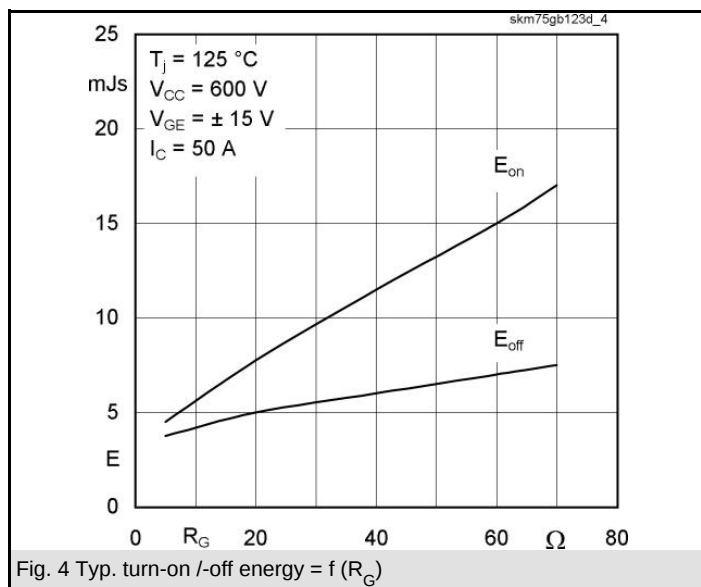
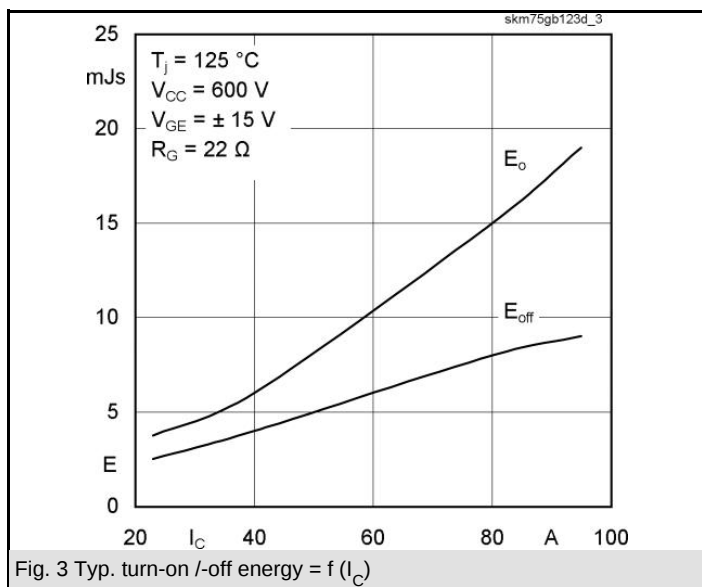
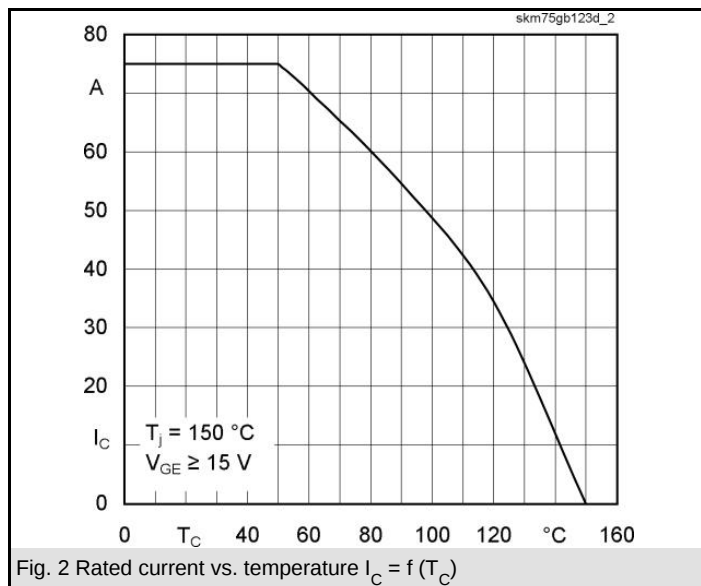
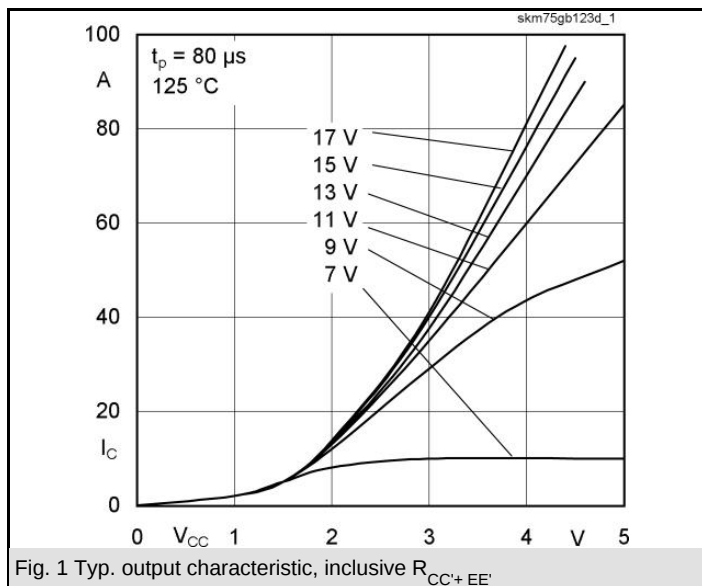
- MOS input (voltage controlled)
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \times I_{cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (10 mm) and creepage distance (20 mm)

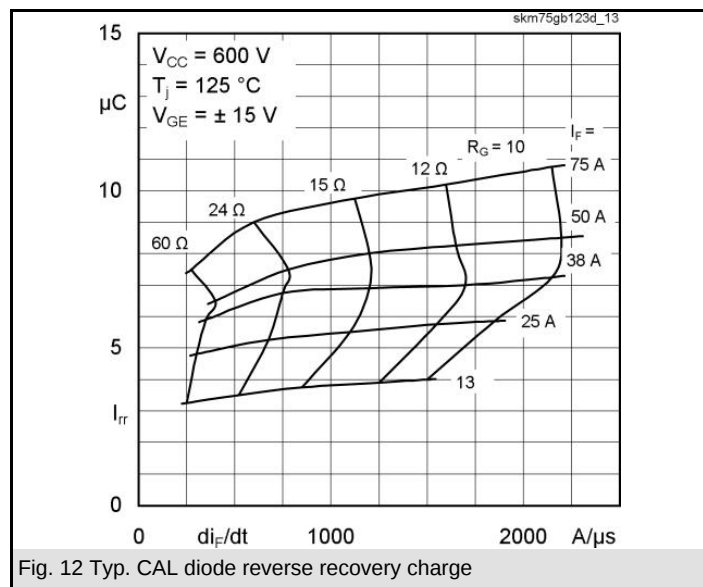
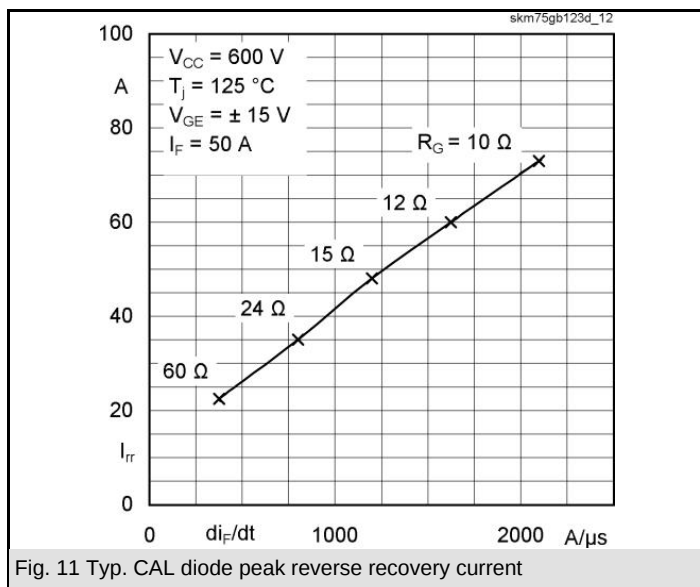
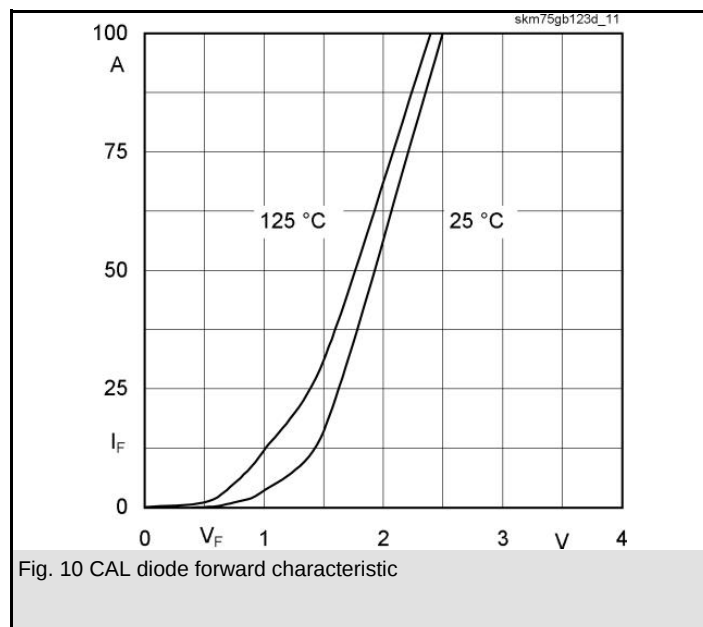
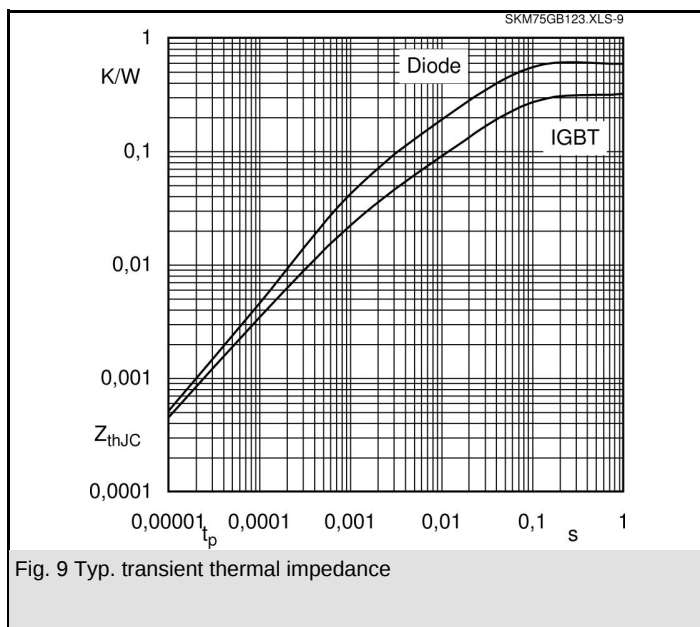
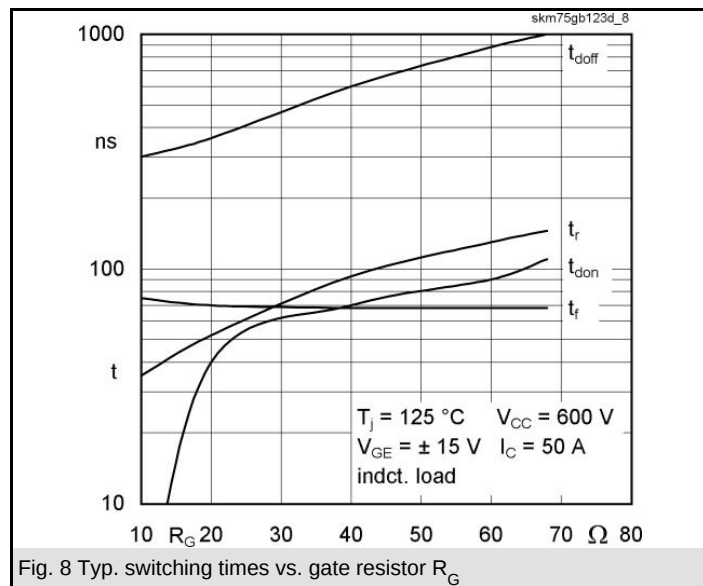
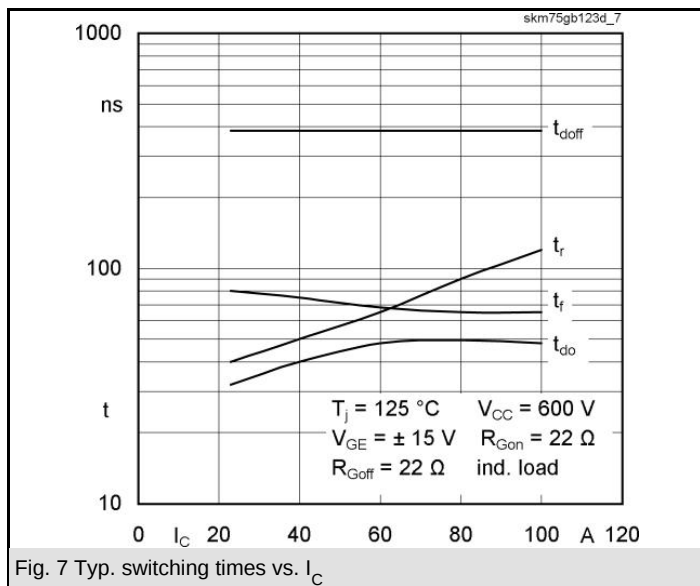
Typical Applications*

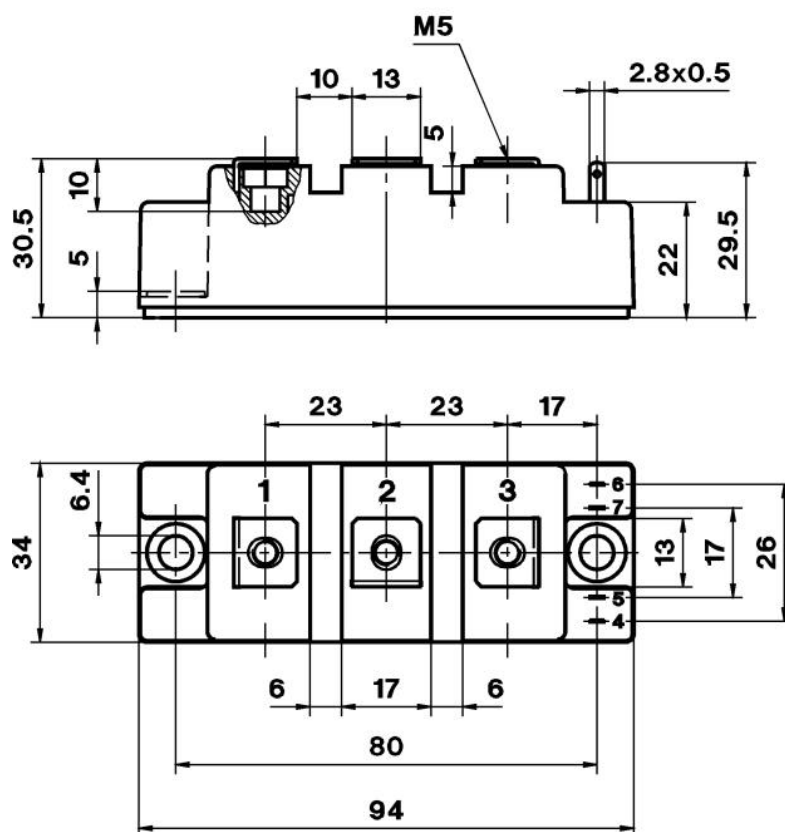
- AC inverter drives
- UPS

Z_{th}			
Symbol	Conditions	Values	Units
$Z_{th(j-c)I}$			
R_i	$i = 1$	180	mk/W
R_i	$i = 2$	64	mk/W
R_i	$i = 3$	22	mk/W
R_i	$i = 4$	4	mk/W
τ_{u_i}	$i = 1$	0,0327	s
τ_{u_i}	$i = 2$	0,0479	s
τ_{u_i}	$i = 3$	0,008	s
τ_{u_i}	$i = 4$	0,005	s
$Z_{th(j-c)D}$			
R_i	$i = 1$	380	mk/W
R_i	$i = 2$	190	mk/W
R_i	$i = 3$	26	mk/W
R_i	$i = 4$	4	mk/W
τ_{u_i}	$i = 1$	0,0947	s
τ_{u_i}	$i = 2$	0,006	s
τ_{u_i}	$i = 3$	0,08	s
τ_{u_i}	$i = 4$	0,003	s

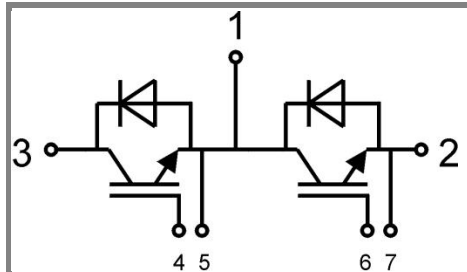






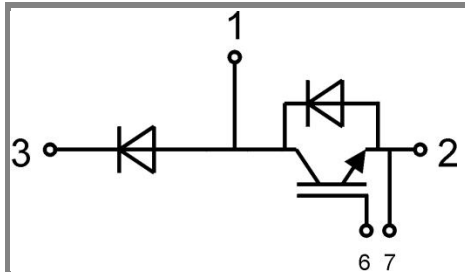


Case D 61



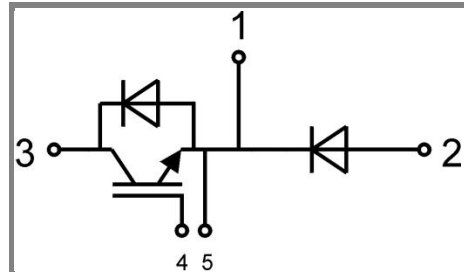
GB

Case D 61



GAL

Case D 62 (→ D 61)



GAR

Case D 63 (→ D 61)