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HMC487* Product Page Quick Links

Last Content Update: 08/30/2016

Comparable Parts

View a parametric search of comparable parts

Evaluation Kits

- HMC487LP5 Evaluation Board.

Documentation

Application Notes

- AN-1363: Meeting Biasing Requirements of Externally Biased RF/Microwave Amplifiers with Active Bias Controllers
- Broadband Biasing of Amplifiers General Application Note
- MMIC Amplifier Biasing Procedure Application Note
- Thermal Management for Surface Mount Components General Application Note

Data Sheet

- HMC487 Data Sheet

Tools and Simulations

- HMC487 S-Parameter

Reference Materials

Quality Documentation

- Package/Assembly Qualification Test Report: 32L 5x5mm QFN Package (QTR: 10009 REV: 05)
- Package/Assembly Qualification Test Report: LP5 & LP5G (QTR: 2014-00150 REV: 02)
- Package/Assembly Qualification Test Report: Plastic Encapsulated QFN (QTR: 05006 REV: 02)
- Semiconductor Qualification Test Report: PHEMT-B (QTR: 2013-00233)

Design Resources

- HMC487 Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

Discussions

View all HMC487 EngineerZone Discussions

Sample and Buy

Visit the product page to see pricing options

Technical Support

Submit a technical question or find your regional support number

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HMC487LP5 / 487LP5E

SURFACE MOUNT PHEMT 2 WATT POWER AMPLIFIER, 9 - 12 GHz

Typical Applications

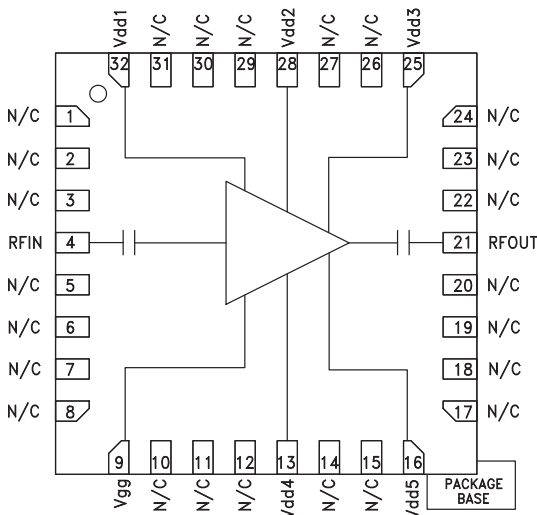
The HMC487LP5 / HMC487LP5E is ideal for use as a power amplifier for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios
- Test Equipment and Sensors
- Military End-Use

Features

Saturated Power: +33 dBm @ 20% PAE
Output IP3: +36 dBm
Gain: 20 dB
+7V @ 1300 mA Supply
50 Ohm Matched Input/Output
25 mm² Leadless SMT Package

Functional Diagram



General Description

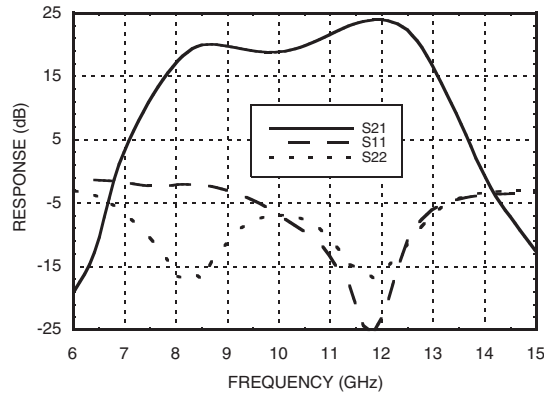
The HMC487LP5 & HMC487LP5E are high dynamic range GaAs PHEMT MMIC 2 Watt Power Amplifiers housed in leadless 5 x 5 mm surface mount packages. Operating from 9 to 12 GHz, the amplifier provides 20 dB of gain, +33 dBm of saturated power and 20% PAE from a +7V supply voltage. Output IP3 is +36 dBm typical. The RF I/Os are DC blocked and matched to 50 Ohms for ease of use. The HMC487LP5(E) eliminate the need for wire bonding, allowing use of surface mount manufacturing techniques.

Electrical Specifications, $T_A = +25^\circ \text{C}$, $V_{dd1, 2, 3, 4, 5} = +7\text{V}$, $I_{dd} = 1300 \text{ mA}^*$

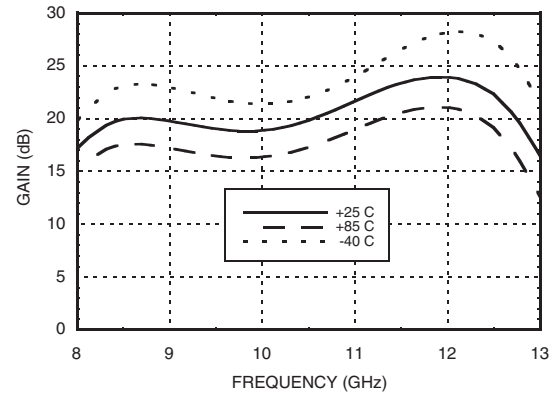
Parameter	Min.	Typ.	Max.	Min.	Typ.	Max.	Units
Frequency Range	9 - 11			11 - 12			GHz
Gain	17	20		19	22		dB
Gain Variation Over Temperature		0.05	0.07		0.05	0.07	dB/°C
Input Return Loss		7			15		dB
Output Return Loss		7			15		dB
Output Power for 1 dB Compression (P1dB)	29	32		28	31		dBm
Saturated Output Power (Psat)		33			32		dBm
Output Third Order Intercept (IP3)		36			35		dBm
Noise Figure		9			8		dB
Supply Current (Idd)(Vdd = +7V, Vgg = -0.3V Typ.)		1300			1300		mA

* Adjust Vgg between -2 to 0V to achieve Idd = 1300 mA typical.

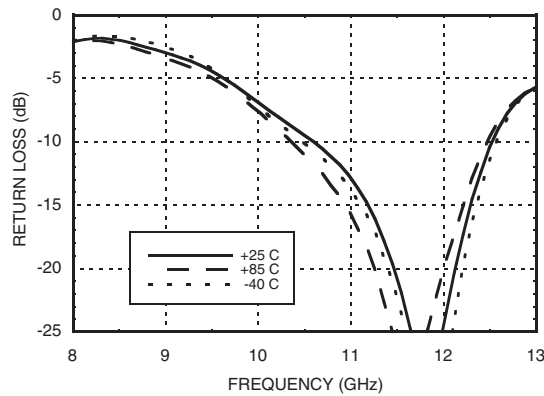
Broadband Gain and Return Loss



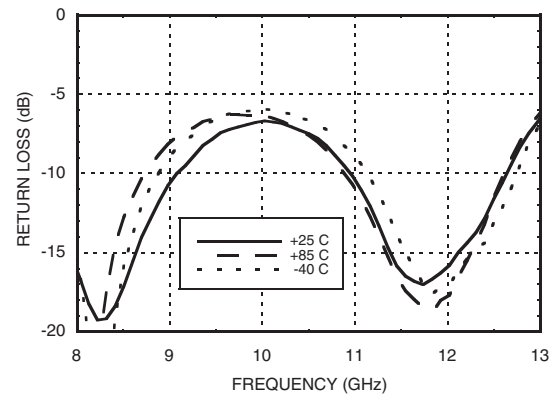
Gain vs. Temperature



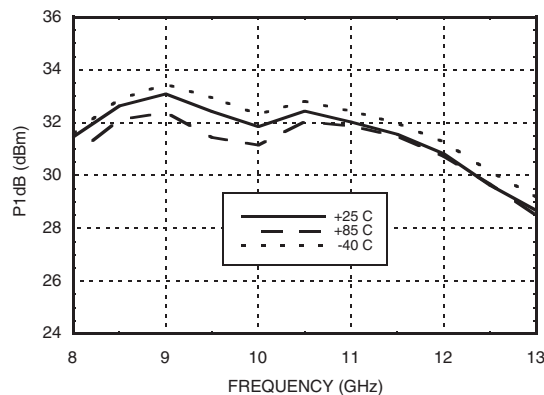
Input Return Loss vs. Temperature



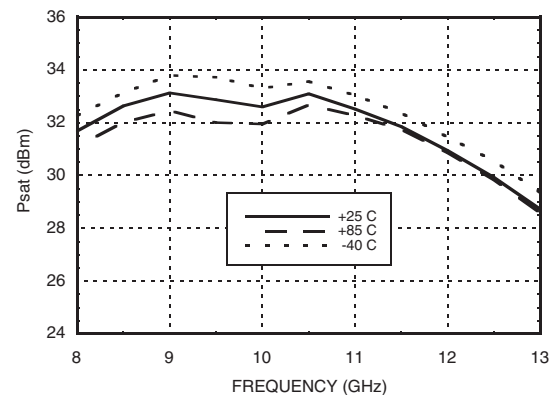
Output Return Loss vs. Temperature



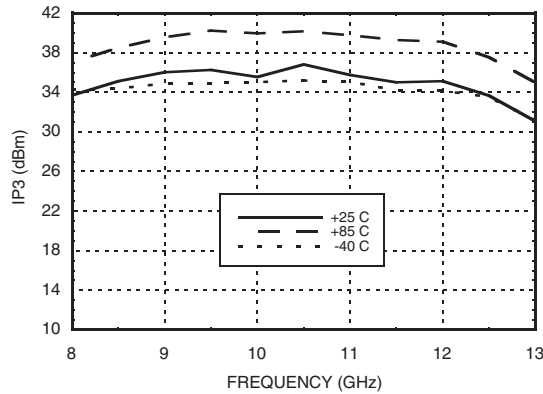
P1dB vs. Temperature



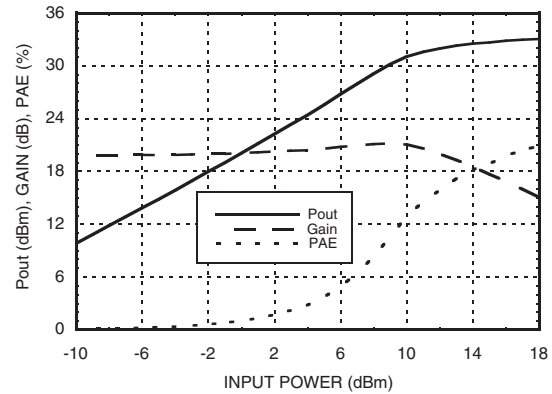
Psat vs. Temperature



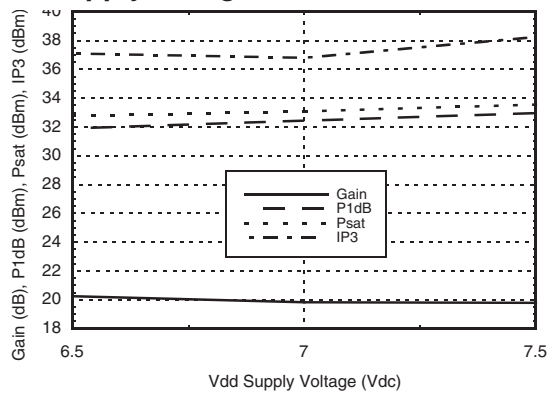
Output IP3 vs. Temperature



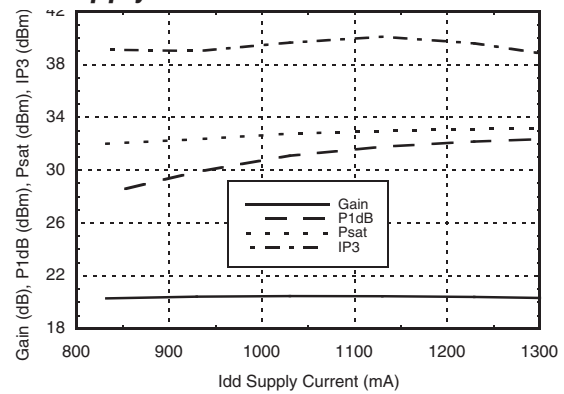
Power Compression @ 10.5 GHz



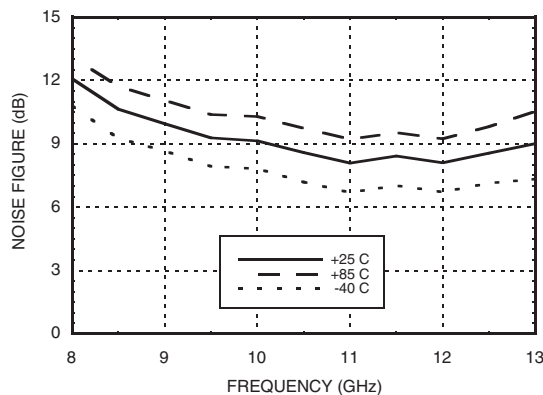
**Gain Power and OIP3
vs. Supply Voltage @10.5 GHz**



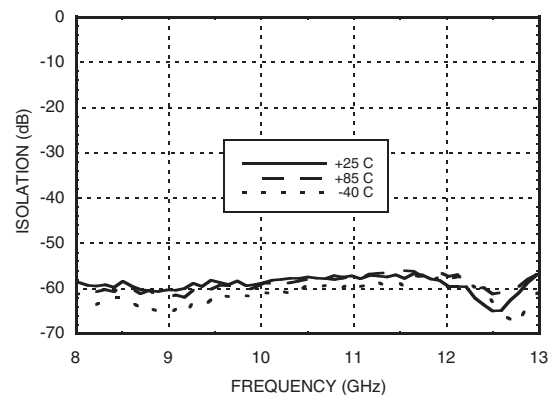
**Gain, Power and OIP3
vs. Supply Current @ 10.5 GHz**



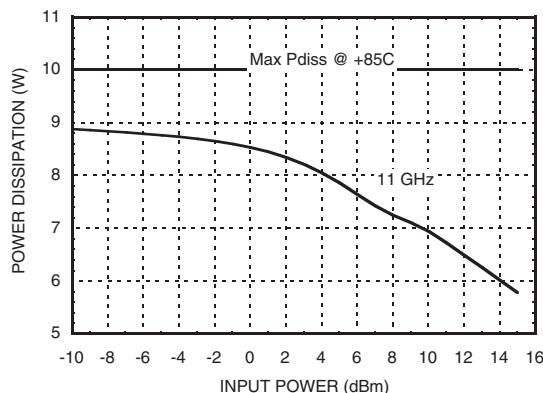
Noise Figure vs. Temperature



Reverse Isolation vs. Temperature



Power Dissipation*



* Refer to "Thermal Management for Surface Mount Components" application note herein.



ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Typical Supply Current vs. Vdd

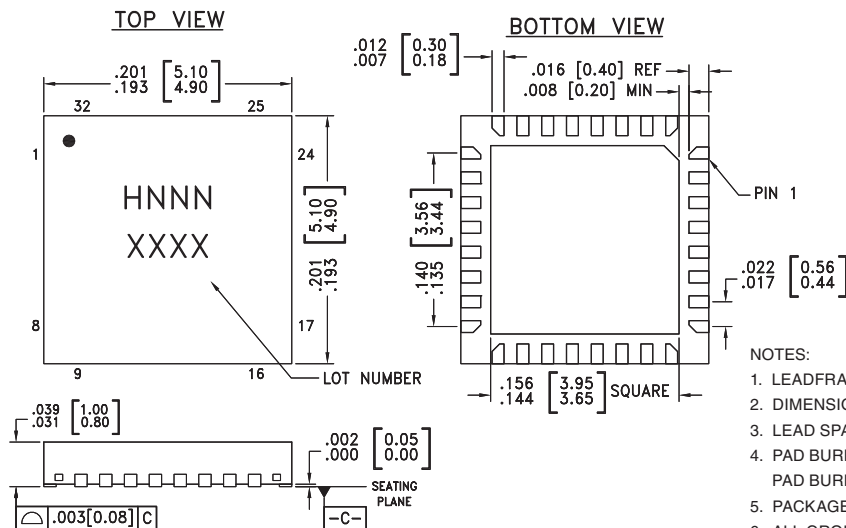
Vdd (Vdc)	Idd (mA)
+6.5	1330
+7.0	1300
+7.5	1285

Note: Amplifier will operate over full voltage ranges shown above. Vgg adjusted to achieve Idd= 1300 mA at +7.0V.

Absolute Maximum Ratings

Drain Bias Voltage (Vdd1, 2, 3, 4, 5)	+8 Vdc
Gate Bias Voltage (Vgg)	-2.0 to 0 Vdc
RF Input Power (RFIN)(Vdd = +7.0 Vdc)	+20 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 154 mW/°C above 85 °C)	10 W
Thermal Resistance (channel to ground paddle)	6.5 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

Outline Drawing



NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE.
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

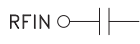
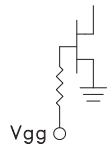
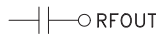
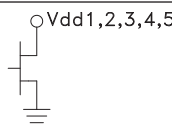

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC487LP5	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H487 XXXX
HMC487LP5E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	H487 XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

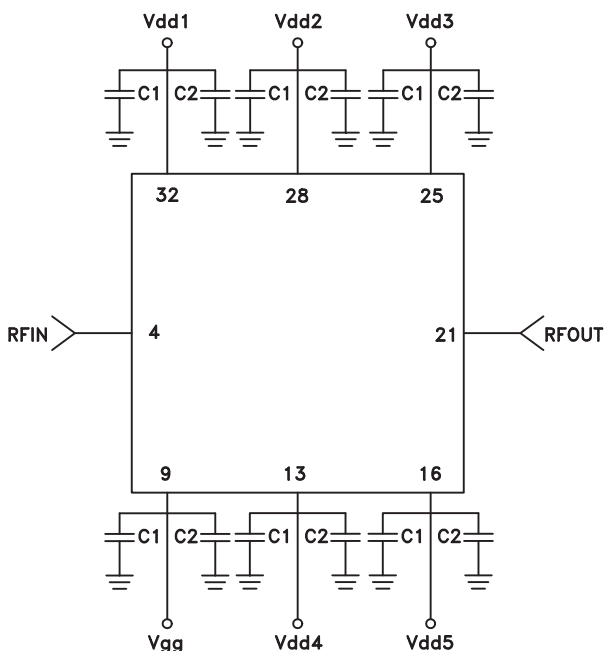
[3] 4-Digit lot number XXXX

Pin Descriptions

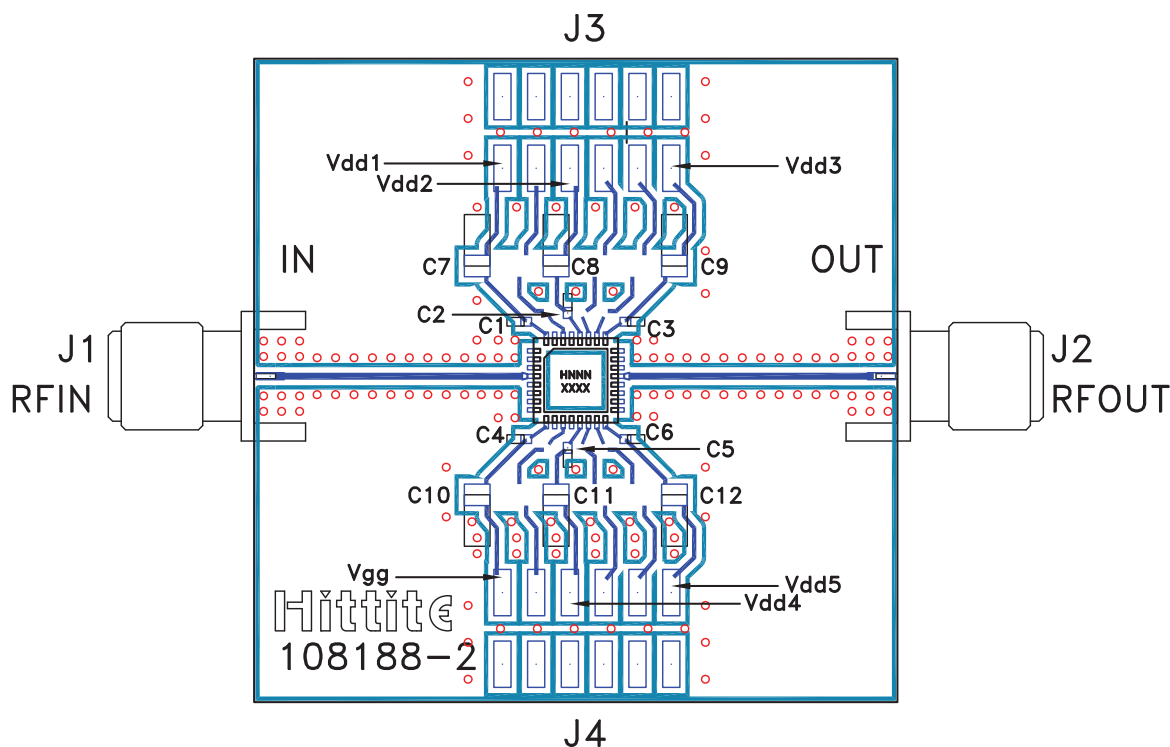
Pin Number	Function	Description	Interface Schematic
1 - 3, 5 - 8, 10 - 12, 14, 15, 17 - 20, 22 - 24, 26, 27, 29 - 31	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance.	
4	RFIN	This pin is AC coupled and matched to 50 Ohms.	
9	Vgg	Gate control for amplifier. Adjust to achieve I _{dd} of 1300 mA. Please follow "MMIC Amplifier Biasing Procedure" Application Note. External bypass capacitors of 100 pF and 2.2 μF are required.	
21	RFOUT	This pin is AC coupled and matched to 50 Ohms.	
32, 28, 25, 13, 16	Vdd1, Vdd2, Vdd3, Vdd4, Vdd5	Power Supply Voltage for the amplifier. External bypass capacitors of 100 pF and 2.2 μF are required.	
	GND	Ground: Backside of package has exposed metal ground slug that must be connected to ground through a short path. Vias under the device are required	

Application Circuit

Component	Value
C1	100 pF
C2	2.2 μF



Evaluation PCB



List of Materials for Evaluation PCB 108190 ^[1]

Item	Description
J1, J2	SRI PC Mount SMA Connector
J3, J4	2mm DC Header
C1 - C6	100 pF capacitor, 0402 pkg.
C7 - C12	2.2µF Capacitor, Tantalum
U1	HMC487LP5 / HMC487LP5E Amplifier
PCB [2]	108188 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350.

The circuit board used in this application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. Copper filled vias under the device are recommended. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.