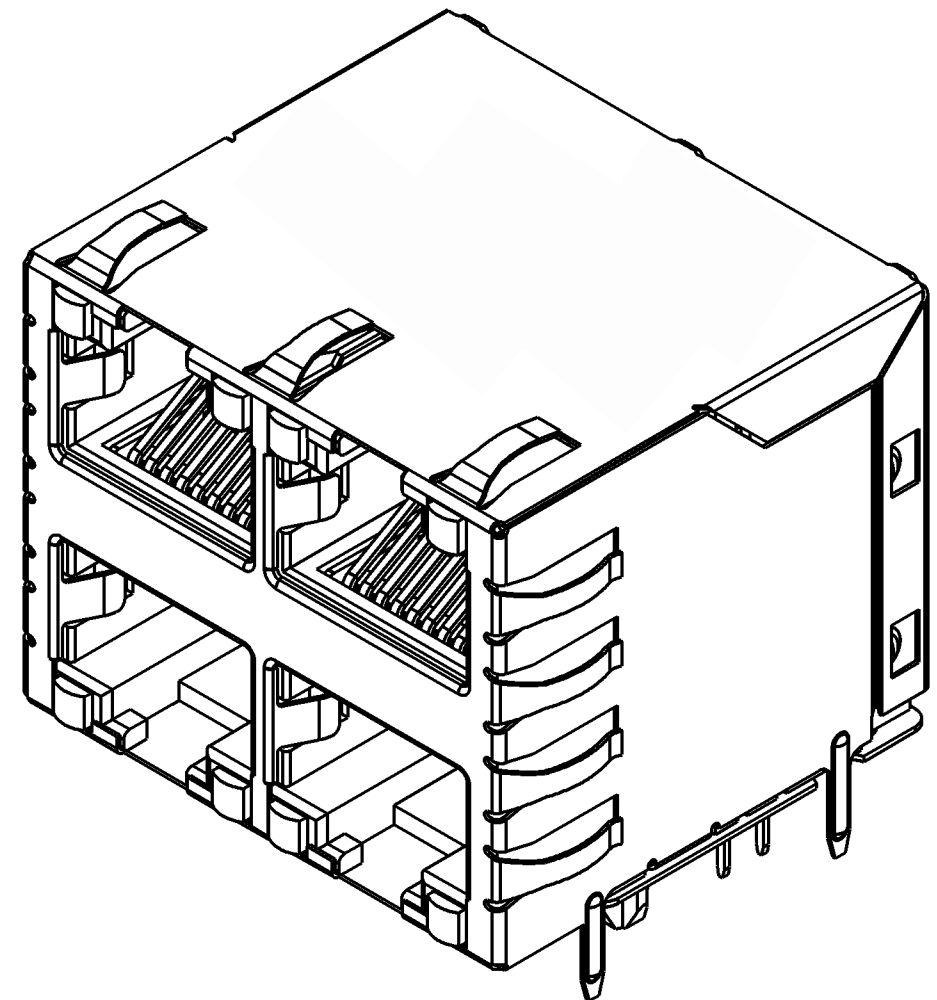
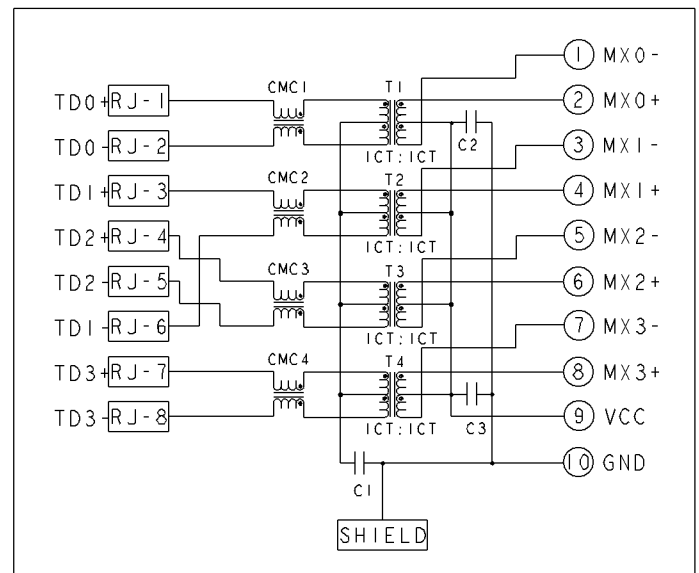


REVISONS		DATE	BY	APPD
D	ECO-09-012057	18AUG2008	RO	RAO
E	ECO-11-014530	01JUL2011	PP	LJ



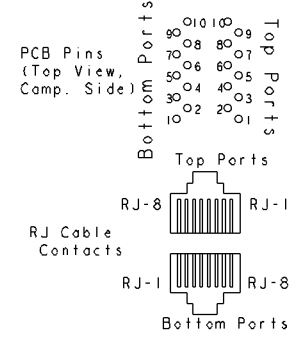
S8G17 GIGABIT CIRCUIT
 Top and Bottom Ports



C1 = 1000pF, 2kV, ±10%, X7R
 Decoupling cap
 C2 - C3 = 470pF, 50V, ±5% Capacitors

Pin Designations

(Repeat for each vertical pair of ports.)



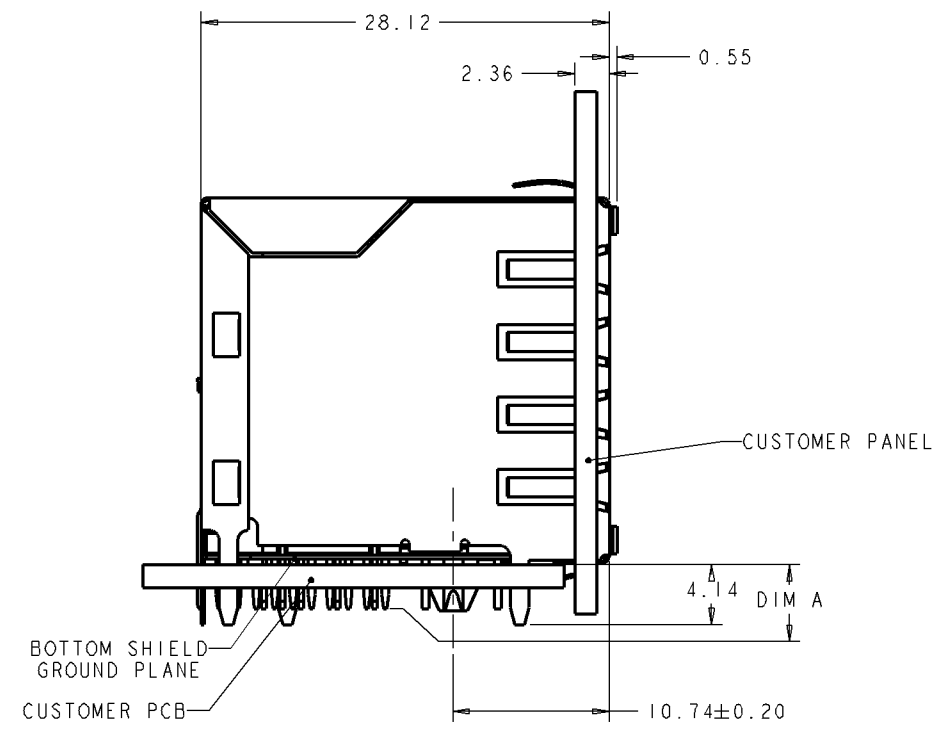
- MATERIALS:**
 PLASTIC HOUSING: BLACK, THERMOPLASTIC
 FLAMMABILITY RATING UL 94V-0
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
 UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE
 AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
 ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
 UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH
 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
- MAGNETICS**
 APPLICATION: 10/100/1000 BASE-T
 IMPEDANCE: 100 OHMS
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
 8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
 ALL FOUR PAIRS BI-DIRECTIONAL
 PERFORMANCE @ 25°C:
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM b.
- PART NUMBER, DATE CODE, COUNTRY OF ORIGIN, LOCATED IN THE APPROXIMATE AREA SHOWN.
 DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK, WITH SUNDAY=1
- AGENCY APPROVAL LOGO, TE CONNECTIVITY LOGO AND PRODUCT LOGO
 TO BE LOCATED IN THE APPROXIMATE AREA SHOWN.
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRIC AND SUPPORT AUTO-MDIX.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
 LED COLOR: DOMINANT WAVELENGTH (ID): GREEN 568 nm TYP. @ IF=20mA
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
 DOMINANT WAVELENGTH (ID): YELLOW 588 nm TYP. @ IF=20mA
 FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT NOT TO BE GREATER THAN 5.08.
- OPERATING TEMPERATURE: 0°C TO +70°C.
- THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK SOLDERING
 TEMPERATURE IS 260 °C MAX, 10 SECONDS MAX.

DIM A	BOTTOM LED NO. 2	BOTTOM LED NO. 1	TOP LED NO. 2	TOP LED NO. 1	PART NUMBER
4.06	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	I-1368511-4
4.06	GREEN	YELLOW	GREEN	YELLOW	I-1368511-3
4.06	YELLOW	GREEN	GREEN	YELLOW	I-1368511-2
4.06	GREEN	GREEN	GREEN	GREEN	I-1368511-1
3.04	GRN/ORG	GRN/ORG	GRN/ORG	GRN/ORG	I368511-5
3.04	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	I368511-4
3.04	GREEN	YELLOW	GREEN	YELLOW	I368511-3
3.04	YELLOW	GREEN	GREEN	YELLOW	I368511-2
3.04	GREEN	GREEN	GREEN	GREEN	I368511-1

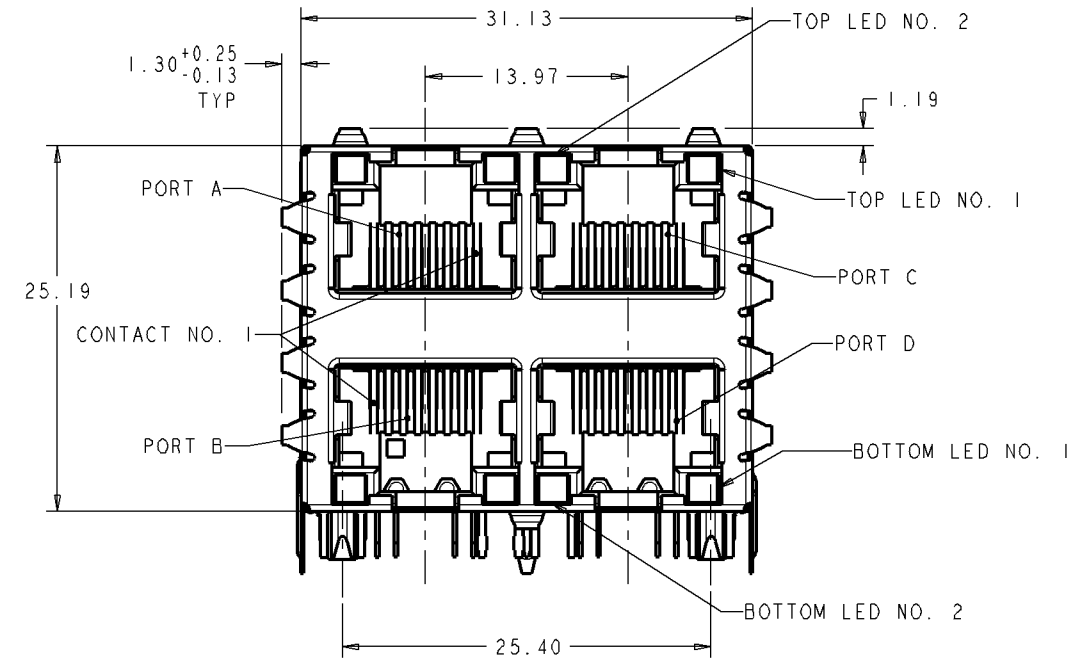
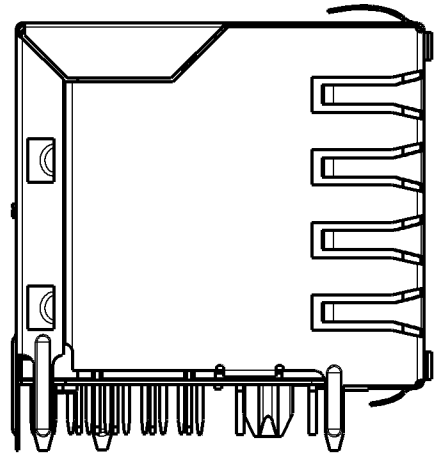
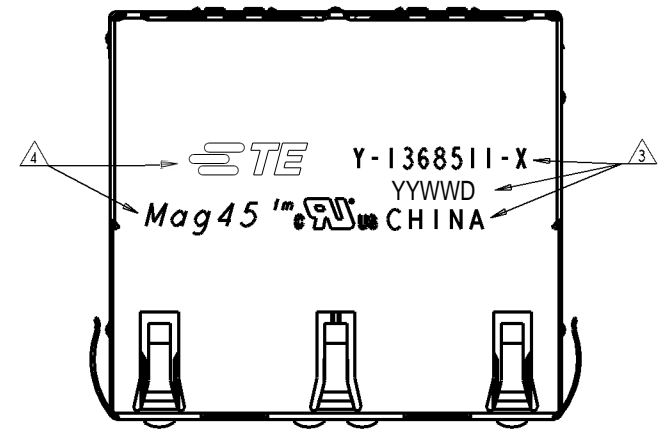
THIS DRAWING IS A CONTROLLED DOCUMENT.

DATE: 20 JUL 2006	BY: TERRY XIONG	TE Connectivity
DATE: 20 JUL 2006	NAME: TERRY XIONG	TE Connectivity
DATE: 20 JUL 2006	PRODUCT SPEC: INTEGRATED MAGNETIC STACKED MODULAR JACK, 2X2 W/LEDS, GIGABIT ETHERNET, RJ45, S8G17 CIRCUIT	
DATE: 20 JUL 2006	APPLICATION SPEC: 108-2108	
DATE: 20 JUL 2006	SCALE: 4:1	
DATE: 20 JUL 2006	SHEET: 1 OF 4	
DATE: 20 JUL 2006	REV: E	

LOC	DIST	REVISIONS				
AA	00	REV	DESCRIPTION	DATE	BY	APPD
			SEE SHEET 1			



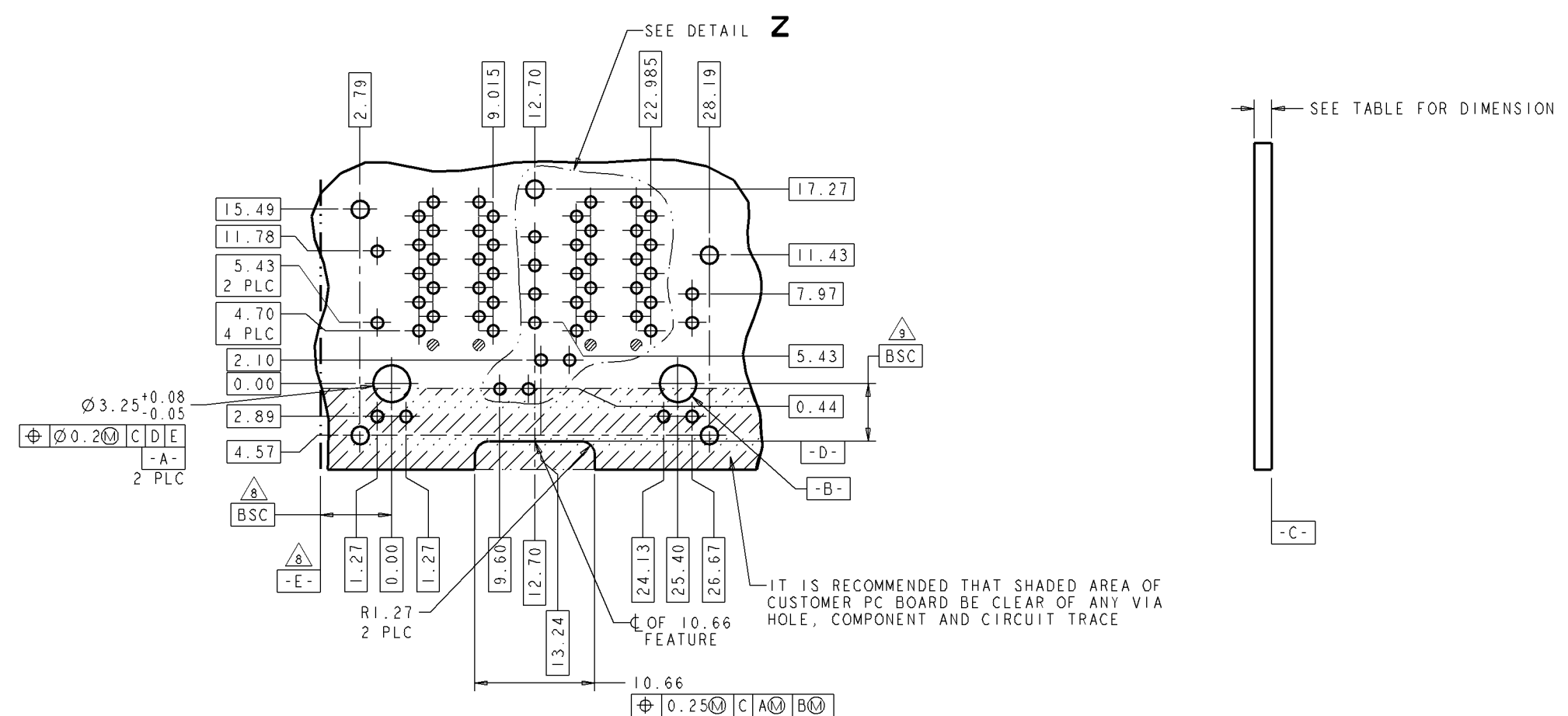
SIDE VIEW SHOWN WITH
 PANEL AND PCB
 FOR LOCATION



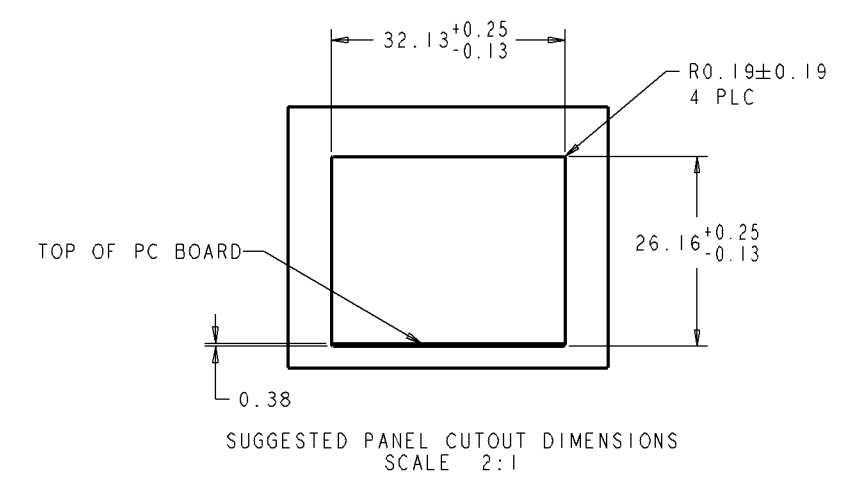
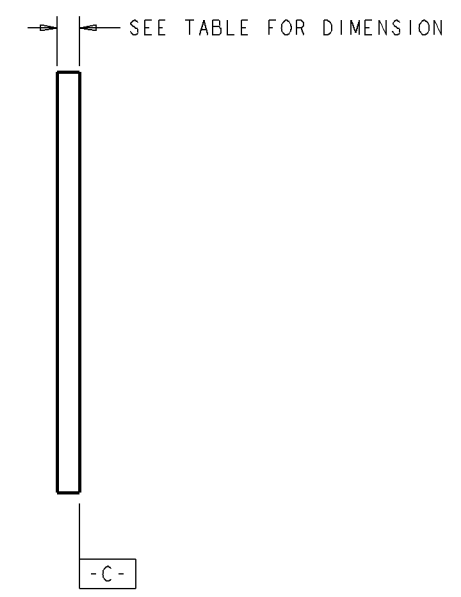
SCALE 4:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 20 JUL 2006	NAME: FREDDY XIONG	TE Connectivity
DIMENSIONS: mm		DATE: 20 JUL 2006	NAME: FREDDY XIONG	INTEGRATED MAGNETIC STACKED MODULAR JACK, 2X2 W/LEDS, GIGABIT ETHERNET, RJ45, S8G17 CIRCUIT
TOLERANCES UNLESS OTHERWISE SPECIFIED:		DATE: 20 JUL 2006	NAME: FREDDY XIONG	PROJECT SPEC: 108-2108
0 PLC ±0.25		DATE: 20 JUL 2006	NAME: FREDDY XIONG	APPLICATION SPEC
1 PLC ±0.25		DATE: 20 JUL 2006	NAME: FREDDY XIONG	RESTRICTED TO
2 PLC ±0.25		DATE: 20 JUL 2006	NAME: FREDDY XIONG	SIZE: A1
3 PLC ±0.25		DATE: 20 JUL 2006	NAME: FREDDY XIONG	CAGE CODE: 1368511
4 PLC ±0.25		DATE: 20 JUL 2006	NAME: FREDDY XIONG	DRAWING NO: 1368511
ANGLES ±0.5		DATE: 20 JUL 2006	NAME: FREDDY XIONG	SHEET 2 OF 4
FINISH SEE NOTE 1		DATE: 20 JUL 2006	NAME: FREDDY XIONG	REV E
MATERIAL SEE NOTE 1		DATE: 20 JUL 2006	NAME: FREDDY XIONG	SCALE: 5:1
WEIGHT		DATE: 20 JUL 2006	NAME: FREDDY XIONG	
CUSTOMER DRAWING		DATE: 20 JUL 2006	NAME: FREDDY XIONG	

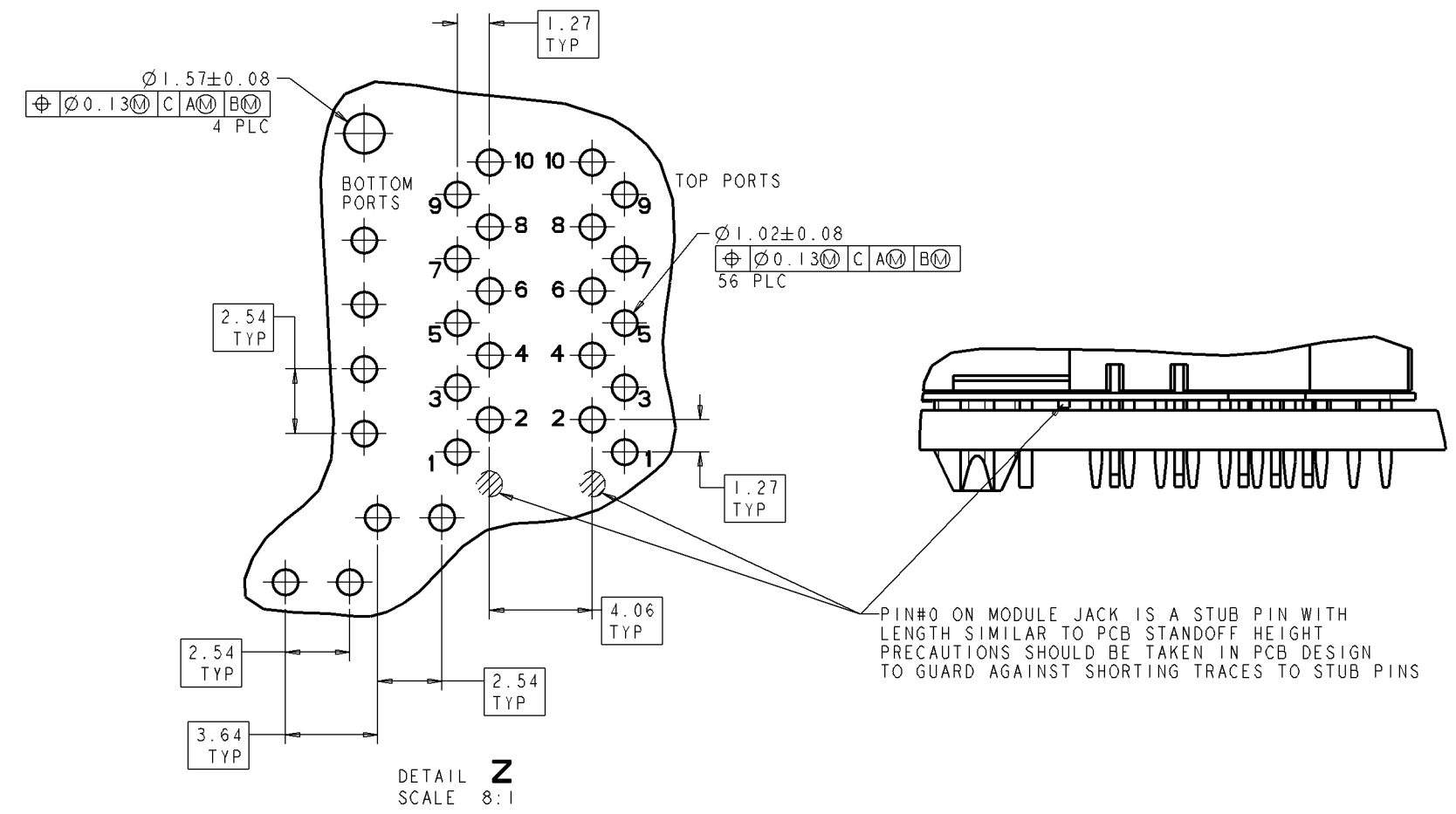
REVISONS		DATE	BY	APP'D
AA	00			
SEE SHEET 1				



PC BOARD LAYOUT VIEW FROM COMPONENT SIDE



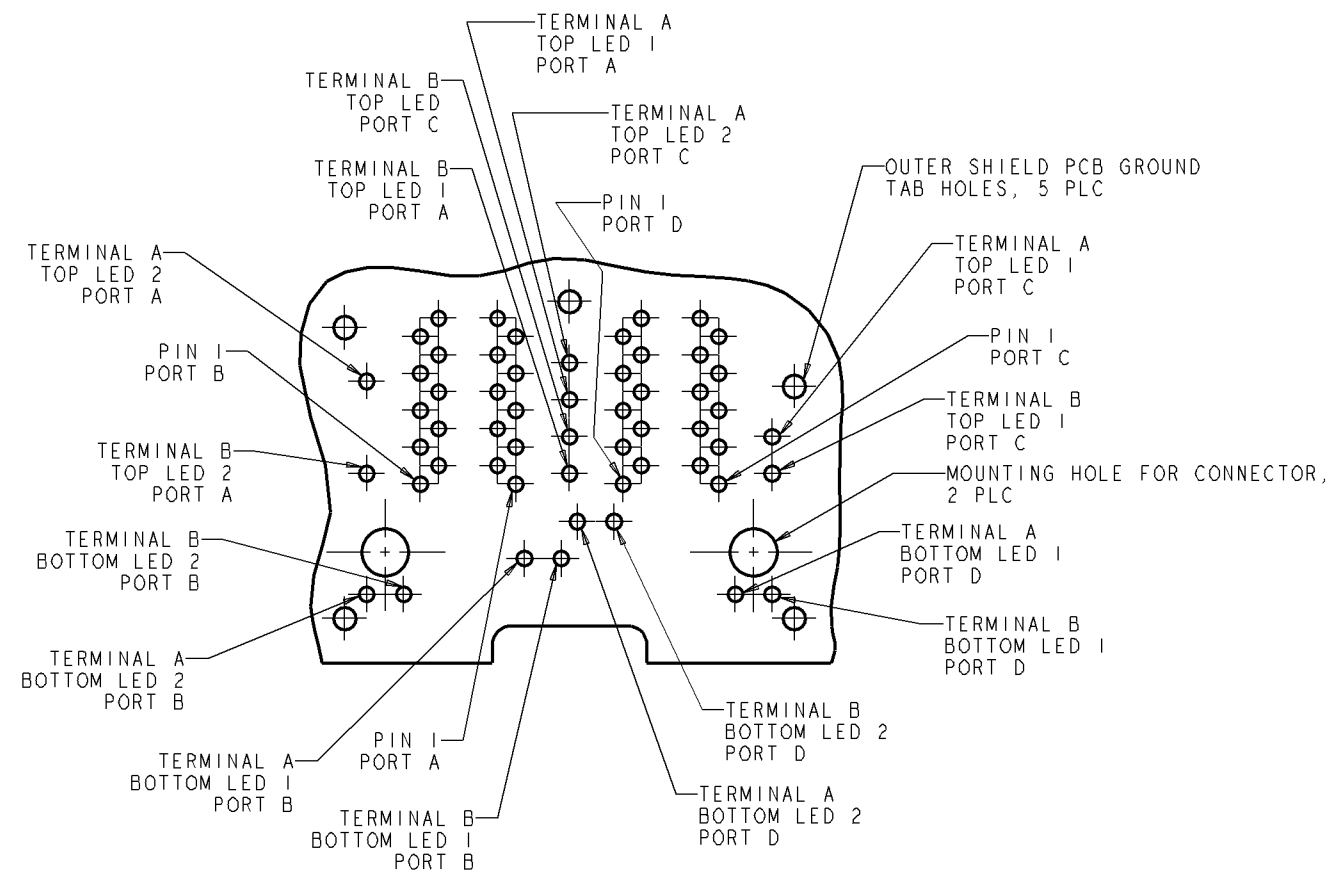
SUGGESTED PANEL CUTOUT DIMENSIONS
SCALE: 2:1



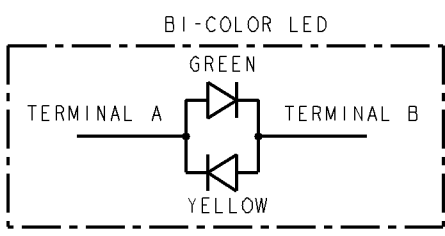
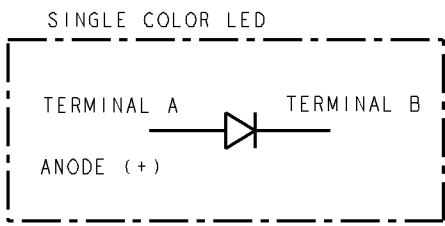
DETAIL Z
SCALE 8:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 20 JUL 2006	DRAWN BY: XIFEN YOU		TE Connectivity	
DIMENSIONS: mm		DATE: 20 JUL 2006	CHKD BY: EBIC GE		NAME: TEDDY XIONG	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		DATE: 20 JUL 2006	APPRD BY: TEDDY XIONG		PROJECT SPEC: INTEGRATED MAGNETIC STACKED MODULAR JACK, 2X2 W/LEDS, GIGABIT ETHERNET, RJ45, S8G17 CIRCUIT	
0 PLC ±0.10		PROJECT SPEC: 108-2108		APPLICATION SPEC: -		SIZE: A
1 PLC ±0.15		MATERIAL: SEE NOTE 1		WEIGHT: -		CAGE CODE: 1368511
2 PLC ±0.25		FINISH: SEE NOTE 1		SCALE: 4:1		SHEET 3 OF 4
3 PLC ±0.30		CUSTOMER DRAWING		REV: E		
4 PLC ±0.40						
ANGLES: ±0.5						

LOC	DIST	REVISIONS				
AA	00	PLT	DESCRIPTION	DATE	DRW	APPR
			SEE SHEET 1			



LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE
 SCALE 4:1



THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 20 JUL 2006	NAME: XIFENG
DRAWN BY: XIFENG		DATE: 20 JUL 2006	NAME: XIFENG
CHECKED BY: XIFENG		DATE: 20 JUL 2006	NAME: XIFENG
APPROVED BY: XIFENG		DATE: 20 JUL 2006	NAME: XIFENG
PROJECT SPEC: 108-2108		APPLICATION SPEC: -	
MATERIAL: SEE NOTE 1		WEIGHT: -	RESTRICTED TO: -
FINISH: SEE NOTE 1		CUSTOMER DRAWING	
DIMENSIONS: mm		SCALE: 4:1	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		SHEET 4 OF 4	
0 PLC ±0.15		REV E	
1 PLC ±0.25			
2 PLC ±0.30			
3 PLC ±0.40			
4 PLC ±0.50			
ANGLES ±0.5°			