

# HA13408

9-Channel Power Driver

## HITACHI

ADE-207-206 (Z)

1st Edition

July 1996

### Description

The HA13408 9-channel power driver IC is designed to drive dot matrix printer head. This IC can drive 9 pins without using any external components. HA13408 can be used for 2 system four-phase step drive, as every channel is used independently.

### Features

- High output current: 1.5 A/channel Max
- High sustaining voltage: 50 V Min
- Low saturation voltage
- Low supply current
- Low input current
- Compatible with TTL, LSTTL & 5 V CMOS
- Low thermal resistance package
- Zener diodes

### Truth Table

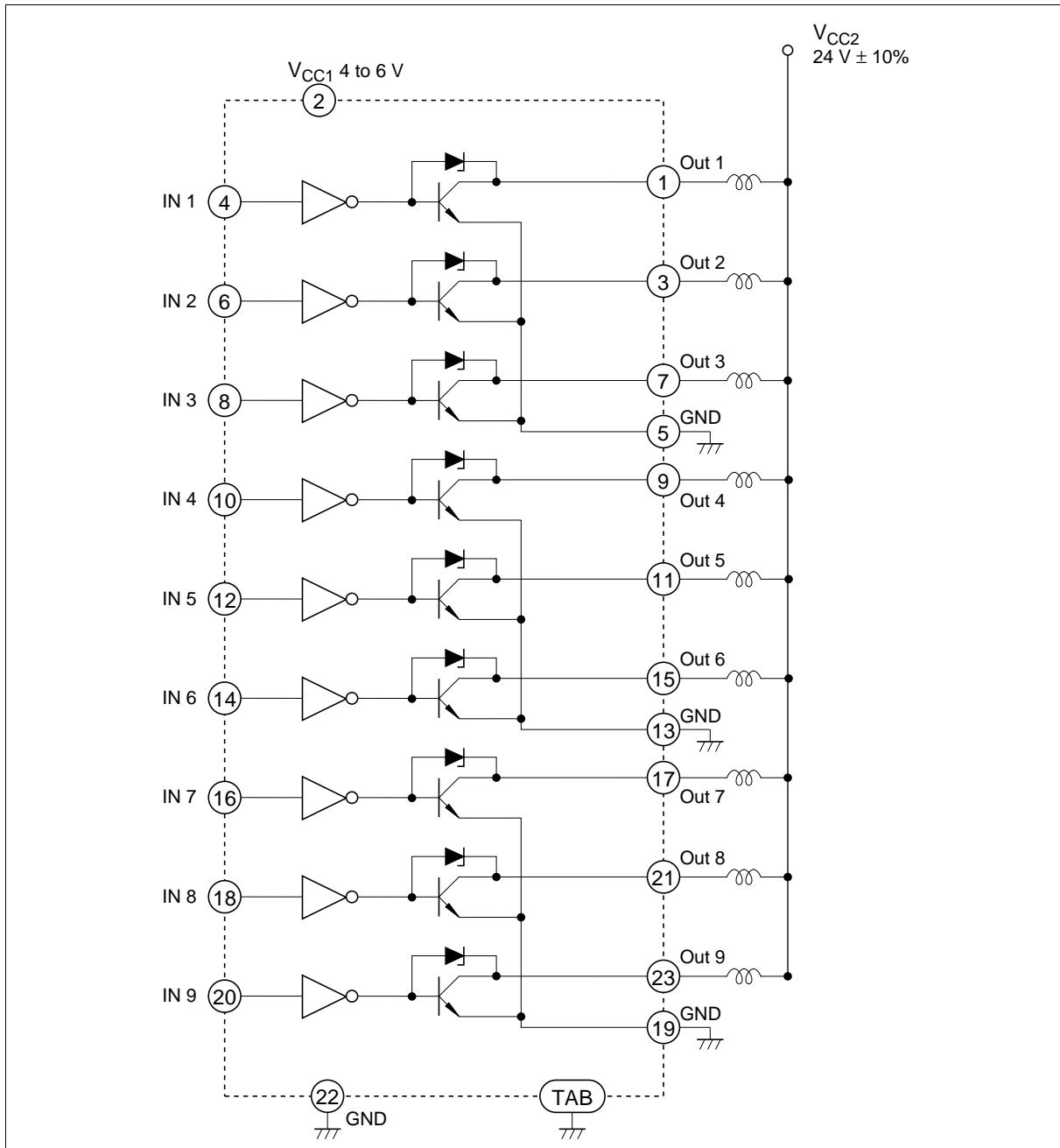
Input	Output
Low	On
High	Off
Open	Off

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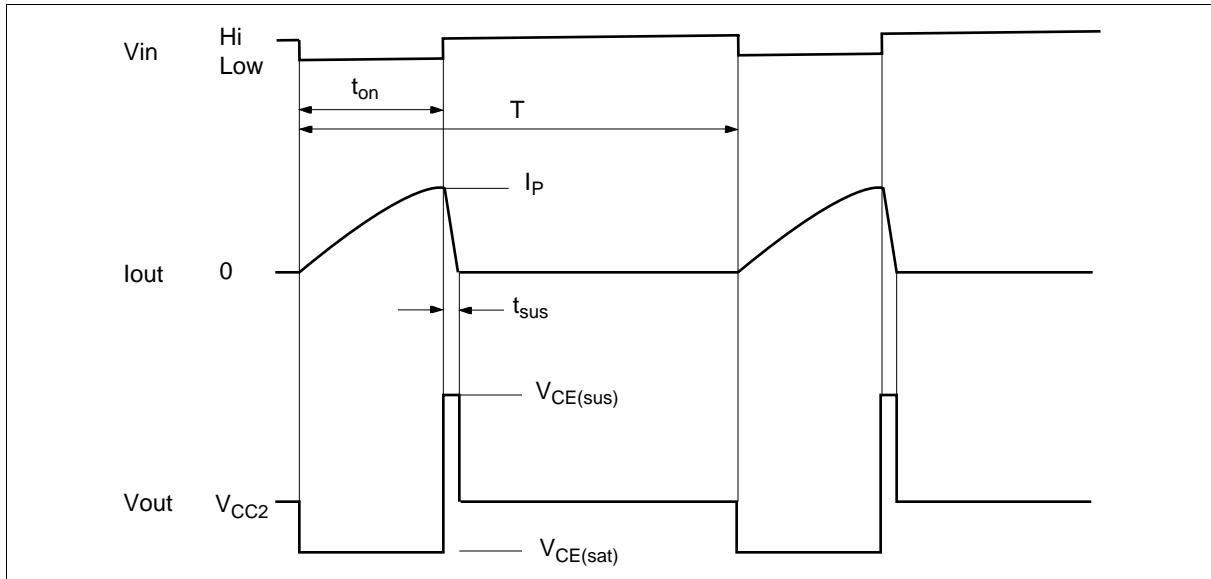
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### Block Diagram



## Peak Current and Turn-Off Time

Figure 1 shows load current ( $I_{out}$ ) and output terminal voltage ( $V_{out}$ ) waveforms for the HA13408 driving an inductive load.



**Figure 1 Output Waveforms**

The peak output current ( $I_p$ ) and sustain time ( $t_{sus}$ ) are obtained as follows;

$$I_p = \frac{V_{CC2} - V_{CE(sat)}}{R} \left( 1 - \exp \left( -\frac{R}{L} t_{on} \right) \right) \div \frac{V_{CC2}}{R} \left( 1 - \exp \left( -\frac{R}{L} t_{on} \right) \right) \quad (1)$$

$$t_{sus} = \frac{L}{R} \ln \left( 1 + \frac{I_p \cdot R}{V_{CE(sus)} - V_{CC2}} \right) \quad (2)$$

Where  $L$  is load self-inductance and  $R$  is load direct current resistance.

For example, under the following conditions:

$$L = 5 \text{ mH},$$

$$R = 22 \Omega$$

$$\text{Supply voltage } V_{CC2} = 24 \text{ V},$$

$$\text{Time to drive load } t_{on} = 0.42 \text{ ms.}$$

Peak current ( $I_p$ ) and sustain time ( $t_{sus}$ ) are then:

$$I_p = 0.87 \text{ A}$$

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$$t_{sus} = 0.118 \text{ ms}$$

Where  $V_{CE(sat)} = 1.3 \text{ V typ}$  and  $V_{CE(sus)} = 52 \text{ V typ}$ .

### Power Dissipation

Power dissipation driving an inductive load for an HA13408 is determined as follows:

First, average power dissipation ( $P_{on}$ ) per channel at  $t_{on}$  is obtained as follows:

$$P_{on} \doteq V_{CE(sat)} I_P \left( \frac{V_{CC2}}{R \cdot I_P} - \frac{1}{t_{on}} \frac{L}{R} \right) \quad (3)$$

Average power dissipation ( $P_{sus}$ ) at  $t_{sus}$ :

$$P_{sus} \doteq V_{CE(sus)} I_P \left( \frac{1}{t_{sus}} \frac{L}{R} - \frac{V_{CE(sus)} - V_{CC2}}{R \cdot I_P} \right) \quad (4)$$

Where  $I_p$  and  $t_{sus}$  are obtained in equations (1) and (2).

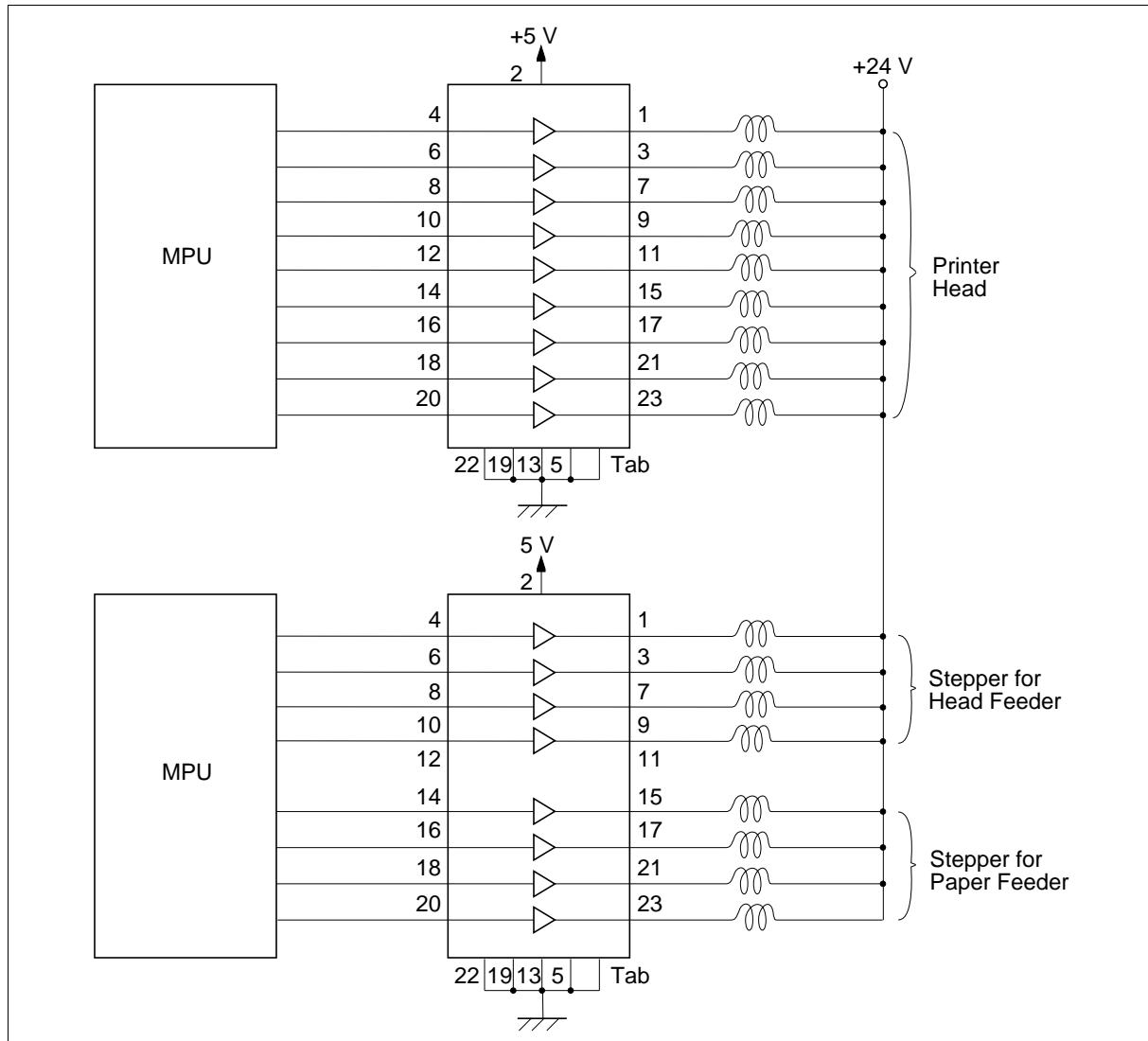
Average power dissipation ( $P_T$ ) per channel for a period is obtained as follows:

$$P_T \doteq \frac{1}{T} (P_{on} \cdot t_{on} + P_{sus} \cdot t_{sus}) \quad (5)$$

Where drive period is defined as  $T$ .

Power dissipation ( $P_T$ ) for 9 channels driven at the same time:

$$P_T \doteq \frac{9}{T} (P_{on} \cdot t_{on} + P_{sus} \cdot t_{sus}) \quad (6)$$

**Application**

**Figure 2 Dot Matrix Printer**

## HA13408

### Absolute Maximum Ratings (Ta = 25°C)

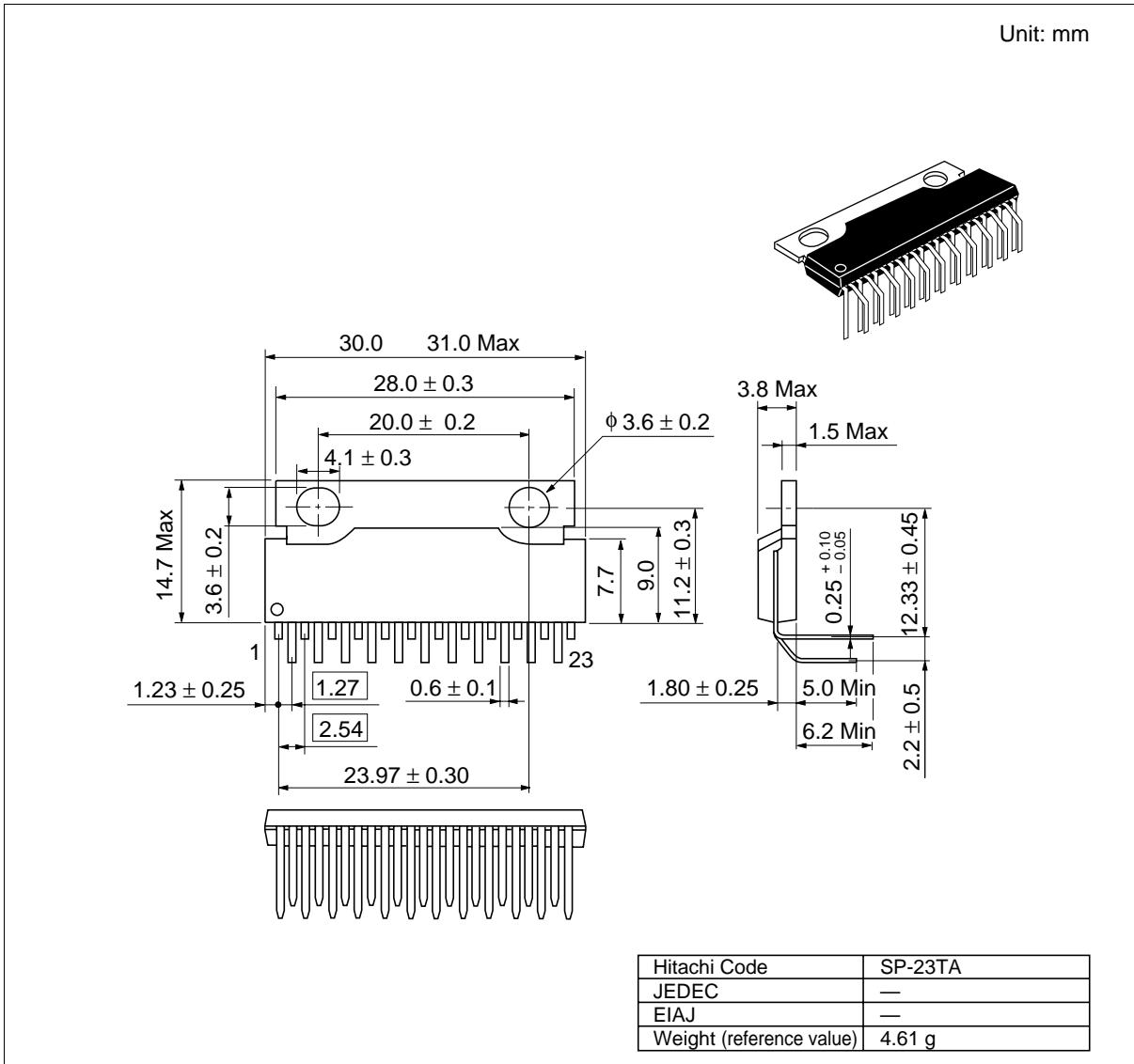
Item	Symbol	Rating	Unit	Notes
Supply voltage	V <sub>CC1</sub>	7.0	V	
Input voltage	V <sub>I</sub>	V <sub>CC1</sub>	V	
Output voltage	V <sub>CE(sus)</sub>	50	V	
Output current	I <sub>O</sub>	1.5	A	
Power dissipation	P <sub>T</sub>	20	W	1
Junction temperature	T <sub>J</sub>	150	°C	
Operating junction temperature range	T <sub>jop</sub>	−20 to +125	°C	
Storage temperature range	T <sub>stg</sub>	−55 to +125	°C	

Notes: 1. Thermal resistance  $\theta_{j-a} \leq 40^{\circ}\text{C/W}$   
 $\theta_{j-c} \leq 3^{\circ}\text{C/W}$

### Electrical Characteristics (Ta = 25°C, V<sub>CC1</sub> = 5 V)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions	Note
Input Low voltage	V <sub>IL</sub>	—	—	0.8	V	V <sub>CC1</sub> = 4.0 V	
Input High voltage	V <sub>IH</sub>	2.0	—	—	V	V <sub>CC1</sub> = 6.0 V	
Input Low current	I <sub>IL</sub>	−100	−15	+10	μA	V <sub>I</sub> = 0 V	
Input High current	I <sub>IH</sub>	−10	0	+10	μA	V <sub>I</sub> = 2.4 V	
Supply current	I <sub>CC0</sub>	—	30	45	mA	All V <sub>I</sub> = 2.4 V	
	I <sub>CC</sub>	—	33	50	mA	All V <sub>I</sub> = 0 V	
Output cut off current	I <sub>CEO</sub>	—	—	1.0	mA	V <sub>CC1</sub> = 6 V, V <sub>CC2</sub> = 40 V, V <sub>I</sub> = 2.0 V	
Output saturation voltage	V <sub>CE(sat)</sub>	—	1.6	2.2	V	V <sub>CC1</sub> = 4 V, I <sub>O</sub> = 1.0 A, V <sub>I</sub> = 0.8 V	
Output sustaining voltage	V <sub>CE(sus)</sub>	50	—	—	V	I <sub>O</sub> = 1.0 A	1
Delay time	t <sub>PLH</sub>	—	1.5	5	μs	Turn OFF	
	t <sub>PHL</sub>	—	0.3	5	μs	Turn ON	

Note: 1. The conditions of loading; Measure at L<sub>s</sub> = 5 mH, R<sub>s</sub> = 22 Ω.

**Package Dimensions**

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