



## Molded, Dual-In-Line Resistor, Through Hole Network

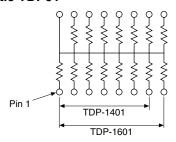


**Actual Size** 

Vishay Thin Film offers two standard circuits in a 14 pins and 16 pins molded dual-in-line over a 100  $\Omega$  to 100  $k\Omega$ resistance range. The networks feature ratio tolerance to 0.05 % with a TCR tracking of 5 ppm/°C.

#### **SCHEMATIC**

#### **Schematic TDP01**



Models: TDP1401 and TDP1601 13 or 15 resistors with one pin common

#### **FEATURES**

- · Standard rugged, molded case construction (14 pins and 16 pins)
- Highly stable thin film (500 ppm at + 70 °C at 2000 h)



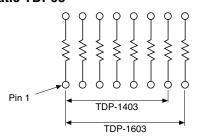
COMPLIANT

- Low temperature coefficient (± 25 ppm/°C)
- · Compatible with automatic insertion equipment
- Standard isolated pin one common schematic
- Compliant to RoHS directive 2002/95/EC

#### **TYPICAL PERFORMANCE**

	ABSOLUTE	TRACKING	
TCR	25	5	
	ABSOLUTE	RATIO	
TOL.	0.1	0.05	

#### **Schematic TDP03**



Models: TDP1403 and TDP1603 7 or 8 isolated resistors

TECT	CDECIFICATIONS	CONDITIONS	
TEST	SPECIFICATIONS	CONDITIONS	
Material	Passivated nichrome	-	
Pin/Lead Number	14, 16	-	
Resistance Range	100 Ω to 100 kΩ	-	
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C	
TCR: Tracking	± 5 ppm/°C	- 55 °C to + 125 °C	
Tolerance: Absolute	± 0.1 %	+ 25 °C	
Tolerance: Ratio	± 0.05 % to ± 0.5 %	+ 25 °C	
Danier Daties - Danietes	0.05 W/resistor = 01 circuit	-1 05 00	
Power Rating: Resistor	0.10 W/resistor = 03 circuit	at + 25 °C	
Power Rating: Package	0.8 W/package	Maximum at + 70 °C	
Stability: Absolute	$\Delta R \pm 0.05 \%$	2000 h at + 70 °C	
Stability: Ratio	$\Delta R \pm 0.015 \%$	2000 h at + 70 °C	
Voltage Coefficient	< 1 ppm/V (typical)	-	
Working Voltage	100 V	-	
Operating Temperature Range	- 55 °C to + 125 °C	-	
Storage Temperature Range	- 55 °C to + 150 °C	-	
Noise	< - 30 dB	-	
Thermal EMF	0.08 μV/°C	-	
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C	
Shelf Life Stability: Ratio	$\Delta R \pm 0.002 \%$	1 year at + 25 °C	

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

# Vishay Thin Film Molded, Dual-In-Line Resistor, Through Hole Network



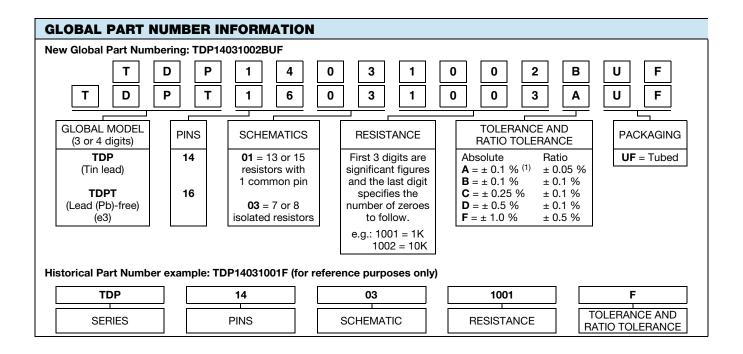
	DIMENSION	INCHES	MILLIMETERS
Part A	А	0.755	19.18
	В	0.250	6.35
B TDP14XX MMM	С	0.075	1.91
Pin 1  Vishay Date Code	D	0.100	2.54
Logo	E	0.018	0.46
<u> ТДД</u> ф	F	0.060	1.52
	G	0.025	0.64
F L	Н	0.190	4.83
→   ←E	J	0.130	3.30
	К	0.320	8.13
M→  ←	L	0.310	7.87
	М	0.010	0.25
<del></del>	А	0.755	19.18
Part	В	0.250	6.35
TDP16XX	С	0.025	0.64
Pin 1	D	0.100	2.54
Vishay Date Code Logo	Е	0.018	0.46
<u> </u>	F	0.060	1.52
H L <sub>G</sub>	G	0.025	0.64
	Н	0.190	4.83
F'	J	0.130	3.30
→  ←E	К	0.320	8.13
W	L	0.310	7.87
M→  ←	М	0.010	0.25





# Molded, Dual-In-Line Resistor, Through Hole Network Vishay Thin Film

MECHANICAL SPECIFICATIONS		
Resistive Element	Passivated nichrome	
Substrate Material	Alumina	
Body	Conformal coated	
Terminals	Copper alloy	
Tin/Lead Option	Sn90	
Lead (Pb)-free Option	100 % matte tin	
Tin/Lead and Lead (Pb)-free Finish	Hot solder dip	





Vishay

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