



RESISTORS • CAPS & COILS • DELAY LINES

SURFACE MOUNT ACTIVE (DIGITAL) DELAY LINES

SMA1405 SERIES - SMALL OUTLINE 5-TAP

SMA2805 SERIES - PLCC PACKAGE 5-TAP

SMA2810 SERIES - PLCC PACKAGE 10-TAP



FEATURES

- SO and PLCC formats with 50-MIL pin spacing
- Excellent for applications requiring high delay stability, fast rise time and no jitter, such as memory boards, disk drives, and signal processing
- Available on 24mm embossed plastic Tape & Reel
- .002" coplanarity
- Economical price
- TTL and DTL compatible
- Operating temperature: 0°C to +70°C
- Temperature coefficient: 100ppm/°C

OPTIONS

- Non-standard delay times or tolerances
- Non-symmetrical tap delays
- Faster rise times
- Measurement at both leading and trailing edges
- ECL and High Speed CMOS
- 100-MIL pin spacing available
- Fast Logic TTL Interface
- Military screening per MIL-D-83532

INPUT PULSE TEST CONDITIONS

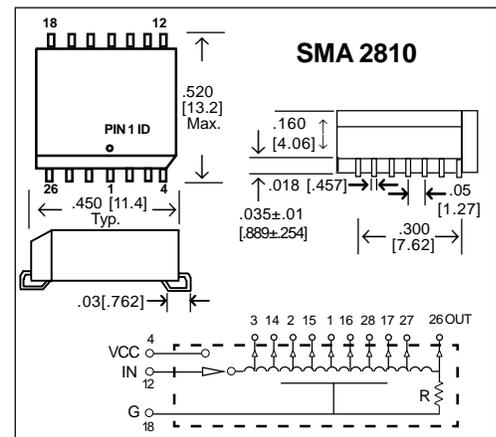
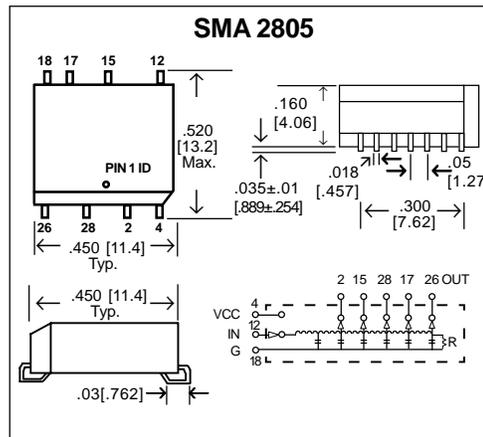
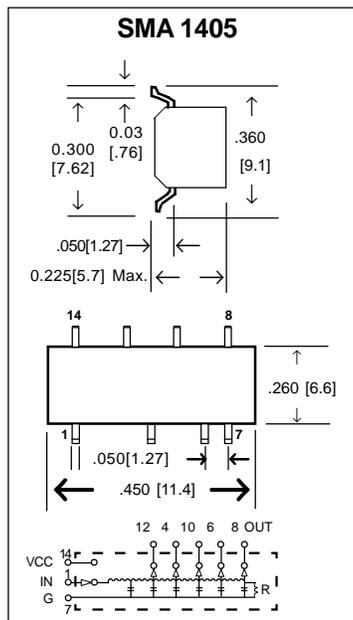
Pulse Voltage	3.2V±0.1V
Rise Time	3.0nS Max.
Typical Supply Current	60mA
Min. Pulse Width	40% of Total Delay

DC ELECTRICAL CHARACTERISTICS

Total Delay Tolerance	±5% or 2nS whichever is greater
Tap Delay Tolerance	±5% or 2nS whichever is greater
Insulation Resistance	1000MΩ min.
Dielectric Strength	100VDC min.
Supply Voltage	5±0.5V
Logic Input Voltage	2.0 to 5.5V
Max. Logic 1 Input Current	50µA
Max. Logic 0 Input Voltage	0.8V
Max. Logic 0 Input Current	-2mA
Min. Logic 1 Output Voltage	2.4V (3.4V Typ.)
Max. Logic 0 Output Voltage	0.5V

TEST METHOD @ 25°C

- 1) Delay measured at 1.5V on leading edge only with no loads on output.
- 2) Rise time measured from 0.75V to 2.4V.
- 3) Delay will inversely vary approximately 4% for every 5% change in supply voltage.
- 4) Delay time and rise time measurements made with no loads on tap.
- 5) Pulse period to be a minimum of 3x the pulse width.



P/N DESIGNATION:

SMA1405 - 10nS - T

RCD Type

Total Delay

Packaging: B = Bulk, T = Tape & Reel

Total Delay (nSec)	SMA 1405		SMA 2805		SMA 2810	
	Rise Time (nS, Max.)	Tap Delay (nS)	Rise Time (nS, Max.)	Tap Delay (nS)	Rise Time (nS, Max.)	Tap Delay (nS)
20	4.0	4	3.0	4.0	*	*
25	4.0	5	3.0	5.0	*	*
30	4.0	6	3.5	6.0	*	*
40	4.0	8	3.5	8.0	*	*
50	4.0	10	3.5	10	3.5	5.0
60	4.0	12	3.6	12	4.0	6.0
75	4.0	15	3.8	15	4.0	7.5
100	4.0	20	4.0	20	4.0	10
125	4.0	25	4.0	25	4.0	12.5
150	4.0	30	4.0	30	4.0	15
175	4.0	35	4.0	35	4.0	17.5
200	4.0	40	4.0	40	4.0	20
250	4.0	50	4.0	50	4.0	25

* Consult factory for availability.

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FA specifications subject to change without notice