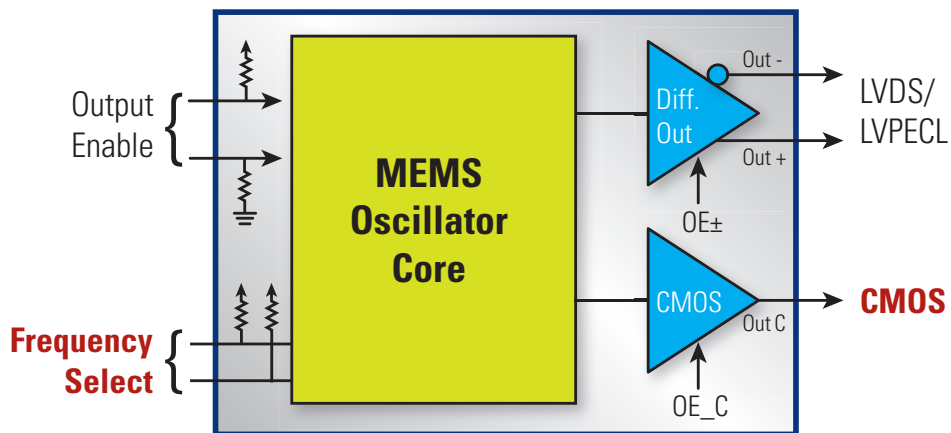


## BENEFITS / FEATURES

- **Dual outputs**
  - LVPECL + CMOS
  - LVDS + CMOS
- **Phase-aligned outputs**
  - No asynchronous timing concerns
- **Quad frequency**
  - Up to 4 frequencies in one Oscillator
- **No external XTAL/XO necessary**
- **± 50 ppm frequency stability**
- **Backward compatible package**
  - Compatible to standard 7050 XO package (unused pins turned off)
- **Shock and vibration resistant**
- **Up to 40x higher reliability than Quartz**

[www.idt.com/go/MEMS](http://www.idt.com/go/MEMS)

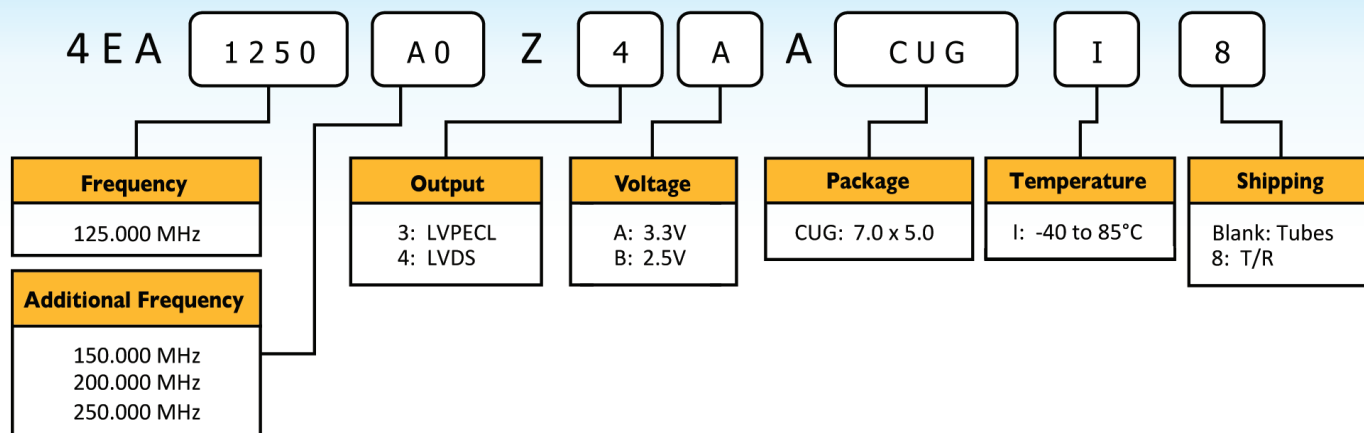


## Standard Frequencies

	LVPECL / LVDS Frequency				CMOS Frequency
	# 1	# 2	# 3	# 4	
4EA1250A0	125 MHz	150 MHz	200 MHz	250 MHz	50 MHz
4EA1000A0	100 MHz	125 MHz	156.25 MHz	200 MHz	25 MHz

CONTACT IDT FOR COMPLETE LIST OF AVAILABLE CONFIGURATIONS

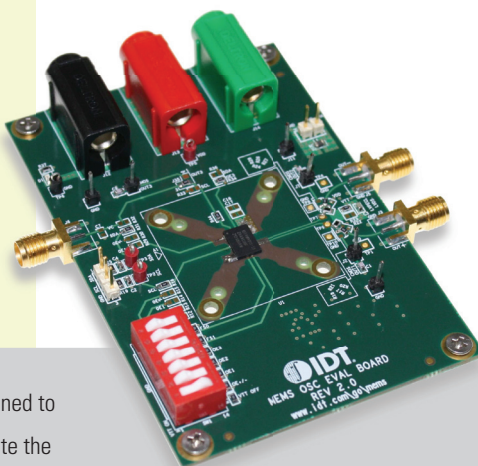
## PART ORDERING GUIDELINE



## MEMS OSCILLATOR EVALUATION BOARD

### FEATURES

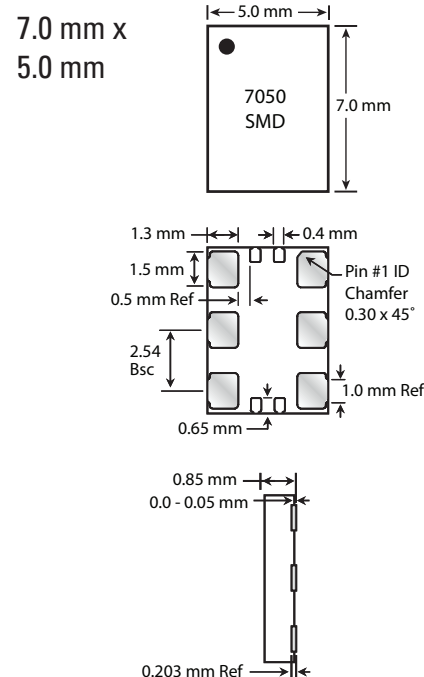
- Differential SMA output
- Standard banana plug power connectors
- Available in standard 5032 and 7050 size packages
- Selectable output termination for both LVDS and LVPECL
- Switch control for Output Enable (OE) and Frequency selection (FS0/FS1)



The MEMS Oscillator Evaluation Board is designed to provide a convenient platform to quickly evaluate the performance and operation of IDT's family of MEMS oscillators.

The board includes provisions for LVPECL and LVDS termination and can also be configured to terminate directly into a 50  $\Omega$  oscilloscope. For VTT terminated LVPECL, a separate bias voltage (VDD - 2V) is also generated onboard for measurement convenience.

## PACKAGE OUTLINE AND DIMENSIONS



## TECHNICAL SUPPORT

EMAIL: [MEMS\\_Support@idt.com](mailto:MEMS_Support@idt.com)

WEBSITE: [www.idt.com/go/MEMS](http://www.idt.com/go/MEMS)



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PB\_MEMS4E\_REV0413

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## IDT (Integrated Device Technology):

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