International Rectifier

50WQ10FN

SCHOTTKY RECTIFIER

5.5 Amp

$$I_{F(AV)} = 5.5Amp$$
$$V_{R} = 100V$$

Major Ratings and Characteristics

Characteris	tics	Values	Units
I _{F(AV)} Rectange waveforr		5.5	А
V _{RRM}		100	V
I _{FSM} @tp=5µ	is sine	330	Α
V _F @5 Apk,	T _J =125°C	0.63	٧
T _J range		-40 to 150	°C

Description/ Features

The 50WQ10FN surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.

- Popular D-PAK outline
- Small foot print, surface moutable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



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Voltage Ratings

Part number	50WQ10FN	
V _R Max. DC Reverse Voltage (V)		
V _{RWM} Max. Working Peak Reverse Voltage (V)	100	

Absolute Maximum Ratings

	Parameters	50WQ	Units	Conditions		
I _{F(AV)}	Max. Average Forward Current * See Fig. 5	5.5	А	50% duty cycle @ T _C = 135°C, r	ectangular wave form	
I _{FSM}	Max. Peak One Cycle Non-Repetitive	330	Α	5μs Sine or 3μs Rect. pulse	Following any rated load condition and with	
	Surge Current *See Fig. 7	110	1 ^	10ms Sine or 6ms Rect. pulse	rated V _{RRM} applied	
E _{AS}	Non-Repetitive Avalanche Energy	6.0	mJ	T _J = 25 °C, I _{AS} = 0.5 Amps, L = 40 mH		
I _{AR}	Repetitive Avalanche Current	0.5	А	Current decaying linearly to zero in 1 μ sec Frequency limited by T _J max. V _A = 1.5 x V _R typical		

Electrical Specifications

	Parameters	50WQ	Units		Conditions
V _{FM}	Max. Forward Voltage Drop	0.77	V	@ 5A	T ₁ = 25 °C
	* See Fig. 1 (1)	0.91	V	@ 10A	1 _J = 25 C
		0.63	V	@ 5A	T, = 125 °C
		0.74	V	@ 10A	1 _J = 120 0
I _{RM}	Max. Reverse Leakage Current	1	mA	T _J = 25 °C	V _P = rated V _P
	* See Fig. 2 (1)	4	mA	T _J = 125 °C	V _R - rated V _R
V _{F(TO}	Threshold Voltage	0.47	V	$T_J = T_J \text{ max.}$	
r _t	Forward Slope Resistance	21.46	mΩ		
C _T	Typical Junction Capacitance	183	pF	V _R = 5V _{DC} (test signal range 100Khz to 1Mhz) 25 °C	
L _s	Typical Series Inductance	5.0	nH	Measured lead to lead 5mm from package body	

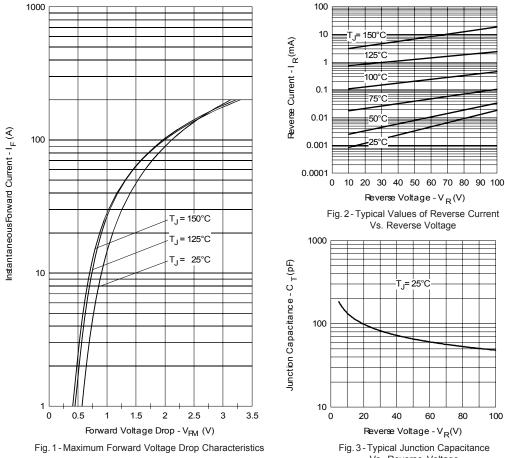
⁽¹⁾ Pulse Width < 300 μ s, Duty Cycle < 2%

Thermal-Mechanical Specifications

	-			
	Parameters	50WQ	Units	Conditions
T _J	Max. Junction Temperature Range (*)	-40 to 150	°C	
T _{stg}	Max. Storage Temperature Range	-40 to 150	°C	
R _{thJC}	Max. Thermal Resistance Junction to Case	3.0	°C/W	DC operation *See Fig. 4
wt	Approximate Weight	0.3 (0.01)	g(oz.)	
	Case Style	D-PAK		Similar to TO-252AA
	Device Marking	50WQ10FN		

 $[\]frac{\text{(*)}}{\text{dTj}} < \frac{1}{\text{Rth(j-a)}} \text{ thermal runaway condition for a diode on its own heatsink}$

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Vs. Reverse Voltage

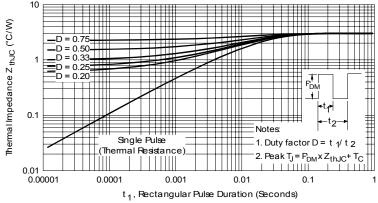


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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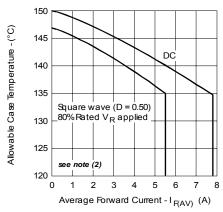


Fig. 5 - Maximum Allowable Case Temperature Vs. Average Forward Current

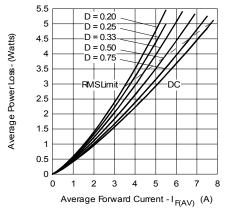


Fig. 6 - Forward Power Loss Characteristics

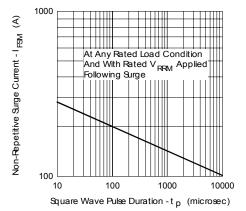
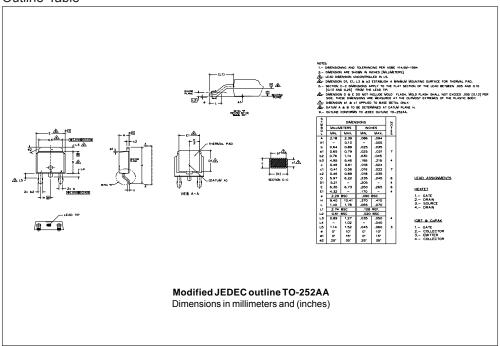


Fig. 7 - Maximum Non-Repetitive Surge Current

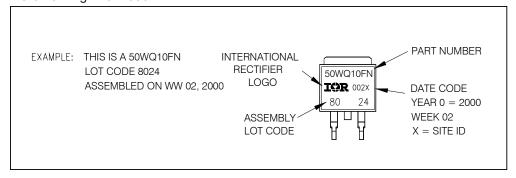
$$\begin{aligned} \textbf{(2)} \ \ & \text{Formula used:} \ & \text{$T_C = T_J - (Pd + Pd_{REV})$ } \times \text{R_{thJC}}; \\ & \text{$Pd = Forward PowerLoss = I_{F(AV)}$ } \times \text{$V_{FM} @ (I_{F(AV)}/D)$ (see Fig. 6);} \\ & \text{$Pd_{REV} = Inverse PowerLoss = V_{R1}$ } \times \text{$I_R (1 - D); $I_R @ V_{R1} = 80\%$ rated V_R } \end{aligned}$$

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Outline Table



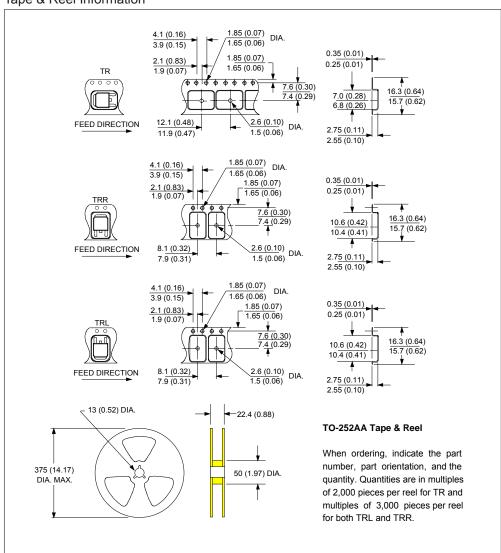
Part Marking Information



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Tape & Reel Information

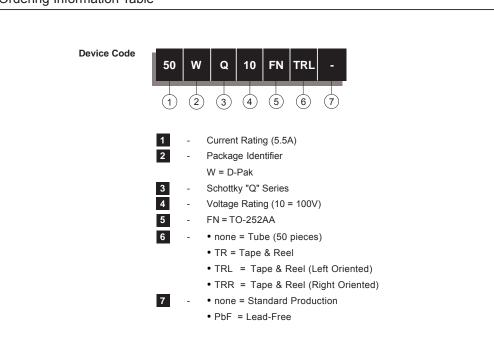
Bulletin PD-20526 rev. G 05/06



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Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for AEC Q101 Level. Qualification Standards can be found on IR's Web site.

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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105 TAC Fax: (310) 252-7309 05/06

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Vishay

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