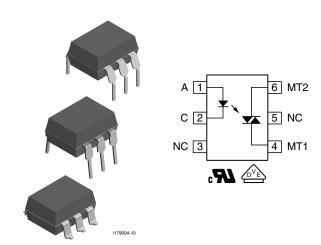


Optocoupler, Phototriac Output, Non-Zero Crossing

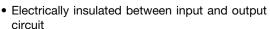


DESCRIPTION

The BRT11, BRT12, and BRT13 product family consists of AC optocouplers non-zero voltage detectors consisting of two electrically insulated lateral power ICs which integrate a thyristor system, a photo detector and noise suppression at the output and an IR GaAs diode input.

FEATURES

- I_{TRMS} = 300 mA
- High static dV/dt < 10 000 V/µs





 Microcomputer compatible - very low trigger current

RoHS COMPLIAN

- Trigger current
- (I_{FT} < 1.2 mA) BRT12**F**
- (I_{FT} < 2 mA) BRT11**H**, BRT12**H**, BRT13**H**
- (I_{FT} < 3 mA) BRT11**M**, BRT12**M**, BRT13**M**
- Non-zero voltage detectors high input sensitivity
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL 1577, file no. E52744 system code H
- DIN EN 60747-5-2 (VDE 0844)/DIN EN 60747-5-5 (pending) available with option 1
- CQC

ORDERING INFORMATION									
B R T 1 # x - X 0 # # T PART NUMBER PACKAGE OPTION TAPE AND REEL Option 7 Option 9 > 0.1 mm									
AGENCY									
CERTIFIED/PACKAGE	≤ 400			≤ 600		≤ 800			
UL	I _{FT} = 2 mA	I _{FT} = 3 mA	I _{FT} = 1.2 mA	I _{FT} = 2 mA	I _{FT} = 3 mA	I _{FT} = 2 mA	I _{FT} = 3 mA		
DIP-6	BRT11H	BRT11M	BRT12F	BRT12H	BRT12M	BRT13H	BRT13M		
DIP-6, 400 mil, option 6	-	-	BRT12F-X006	BRT12H-X006	ı	BRT13H-X006	-		
SMD-6, option 7	-	-	BRT12F-X007T (1)	BRT12H-X007T (1)	i	BRT13H-X007T (1)	-		
SMD-6, option 9	-	-	-	BRT12H-X009T	ı	BRT13H-X009T	-		
UL, VDE	I _{FT} = 2 mA	$I_{FT} = 3 \text{ mA}$	I _{FT} = 1.2 mA	I _{FT} = 2 mA	I _{FT} = 3 mA	I _{FT} = 2 mA	$I_{FT} = 3 \text{ mA}$		
DIP-6	-	-	-	BRT12H-X001	BRT12M-X001	-	-		
DIP-6, option 6	-	-	BRT12F-X016	BRT12H-X016	BRT12M-X016	BRT13H-X016	-		
SMD-6, option 7	-	-	-	-	-	BRT13H-X017T (1)	-		

Note

(1) Also available in tube, do not put T on the end.



PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
INPUT						
Reverse voltage			V _R	6	V	
Forward current			I _F	20	mA	
Surge forward current			I _{FSM}	1.5	Α	
Power dissipation	t ≤ 10 μs		P _{diss}	30	mW	
ОUТРUТ						
		BRT11	V_{DRM}	400	V	
Repetitive peak off-state voltage		BRT12	V_{DRM}	600	V	
		BRT13	V _{DRM}	800	V	
RMS on-state current			I _{TRMS}	300	mA	
Single cycle surge current	50 Hz		I _{TSM}	3	Α	
Power dissipation			P _{diss}	600	mW	
COUPLER						
Maximum power dissipation			P _{tot}	630	mW	
Isolation test voltage (between emitter and detector, climate per DIN 500414, part 2, Nov. 74) (1)			V _{ISO}	5300	V_{RMS}	
Reference voltage in accordance with VDE 0110 b			V _{ref}	500	V _{RMS}	
Reference voltage in accordance with VDE 0110 b (insulation group C)			V _{ref}	600	V_{DC}	
Creepage distance				≥ 7.2	mm	
Clearance distance				≥ 7.2	mm	
Comparative tracking index per DIN IEC 112/VDE 0303 part 1	group IIIa according to DIN VDE 0109		СТІ	≥ 175		
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C		R _{IO}	≥ 10 ¹²	Ω	
ISOIALIOH TESISLATICE	V _{IO} = 500 V, T _{amb} = 100 °C		R _{IO}	≥ 10 ¹¹	Ω	
Storage temperature range			T _{stg}	- 40 to + 150	°C	
Ambient temperature range			T _{amb}	- 40 to + 100	°C	

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

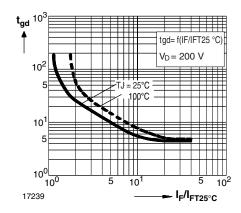
⁽¹⁾ Test AC voltage in accordance with DIN 57883, June 1980.

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT		•			•		
Forward voltage	I _F = 10 mA		V_{F}		1.1	1.35	V
Reverse current	V _R = 6 V		I _R			10	μΑ
Thermal resistance, junction to ambient (1)			R _{thJA}			750	°C/W
OUTPUT							
Peak off-state voltage	I _{D(RMS)} = 100 μA	BRT11	V _{DM}		400		μΑ
		BRT12			600		μA
		BRT13			800		μΑ
Off-state current	$T_C = 80 ^{\circ}C, V_{DRM}$		I_{D}		0.5	100	μA
On-state voltage	$I_T = 300 \text{ mA}$		V_{T}			2.3	V
Pulse current	$t_p \le 5 \mu s$, $f = 100 Hz$, $dl_{tp}/dt \le 8 A/\mu s$		I _{tp}			2	А
Critical rate of rise of off-state voltage	$V_D = 0.67 \ V_{DRM}, \ T_j = 25 \ ^{\circ}C$		dV/dt _{cr}	10			kV/μs
	$V_D = 0.67 \ V_{DRM}, \ T_j = 80 \ ^{\circ}C$		dV/dt _{cr}	5			kV/μs
Critical rate of rise of voltage at current commutation	$V_D = 0.67 \ V_{DRM}, \ T_j = 25 \ ^{\circ}C,$ $dI/dt_{crq} \le 15 \ A/ms$		dV/dt _{crq}	10			kV/μs
	$V_D = 0.67 \ V_{DRM}, \ T_j = 80 \ ^{\circ}C,$ $dI/dt_{crq} \le 15 \ A/ms$		dV/dt _{crq}	5			kV/μs
Critical rate of rise of on-state at current			dl/dt _{cr}	8			A/µs
Holding current	V _D = 10 V		I _H		80	500	μΑ
Thermal resistance, junction to ambient			R _{thJA}			125	°C/W
COUPLER							
	$V_D = 10 \text{ V}, \text{ F - versions}$		I _{FT}			1.2	mA
Trigger current	V _D = 10 V, H - versions		I _{FT}	0.4		2	mA
	V _D = 10 V, M - versions		I _{FT}	0.8		3	mA
Trigger current temperature gradient			$\Delta I_{FT}/\Delta T_{j}$		7	14	μΑ/°C
Capacitance (input to output)	f = 1 MHz, V _R = 0 V		C _{IO}			2	pF

Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.
- (1) Static air, SITAC soldered in PCB or base plate.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)





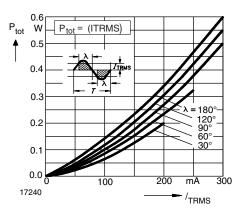


Fig. 2 - Power Dissipation 60 Hz to 60 Hz Line Operation

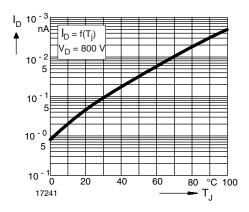


Fig. 3 - Typical Off-State Current

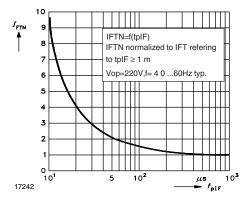


Fig. 4 - Pulse Trigger Current

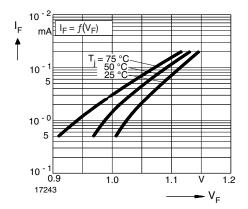


Fig. 5 - Typical Input Characteristics

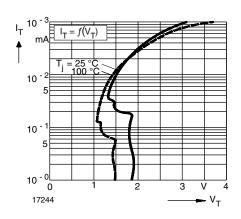


Fig. 6 - Typical Output Characteristics

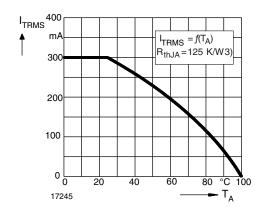


Fig. 7 - Current Reduction

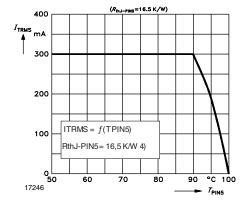
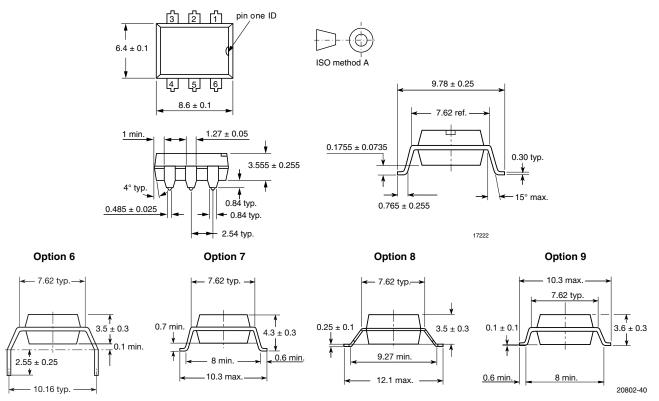


Fig. 8 - Current Reduction



PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Notes

- Only options 1, and 7 are reflected in the package marking.
- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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