BUK7227-100B



N-channel TrenchMOS standard level FET Rev. 02 — 17 February 2011

Product data sheet

Product profile

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 185 °C rating

1.3 Applications

- 12 V, 24 V and 42 V loads
- Automotive systems

- General purpose power switching
- Motors, lamps and solenoids

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 185 °C	-	-	100	V
I _D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C};$ see <u>Figure 1</u> ; see <u>Figure 3</u>	-	-	48	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	167	W
Static chara	acteristics					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 11}}{\text{See } \frac{\text{Figure 12}}{\text{Figure 12}};$	-	23	27	mΩ
Avalanche	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$\begin{split} I_D &= 48 \text{ A; } V_{sup} \leq 100 \text{ V;} \\ R_{GS} &= 50 \text{ \Omega; } V_{GS} = 10 \text{ V;} \\ T_{j(init)} &= 25 \text{ °C; } unclamped \end{split}$	-	-	145	mJ
Dynamic ch	naracteristics					
Q _{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $V_{DS} = 80 \text{ V}; T_j = 25 \text{ °C};$ see Figure 13	-	13	-	nC



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain[1]	mb	D
3	S	source		
mb	D	mounting base; connected to drain	1 3	mbb076 S
			SOT428 (DPAK)	

^[1] It is not possible to make a connection to pin 2 of the SOT428 package.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK7227-100B	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
-			.*!!!!		
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 185 °C	-	100	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	100	V
V_{GS}	gate-source voltage		-20	20	V
I _D	drain current	$T_{mb} = 100 \text{ °C}; V_{GS} = 10 \text{ V}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	-	34	Α
		T_{mb} = 25 °C; V_{GS} = 10 V; see <u>Figure 1</u> ; see <u>Figure 3</u>	-	48	А
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \mu s$; see <u>Figure 3</u>	-	196	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	167	W
T _{stg}	storage temperature		-55	185	°C
Tj	junction temperature		-55	185	°C
Source-drain	diode				
I _S	source current	T _{mb} = 25 °C	-	48	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 ^{\circ}C$	-	196	Α
Avalanche rug	ggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 48 A; $V_{sup} \le$ 100 V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped	-	145	mJ

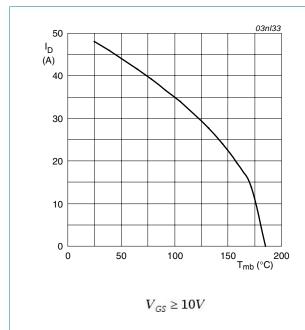


Fig 1. Continuous drain current as a function of mounting base temperature

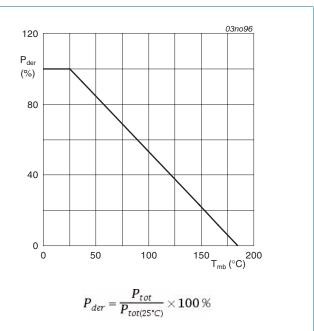
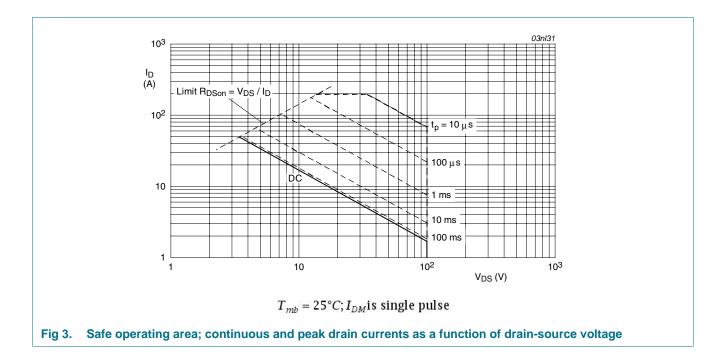


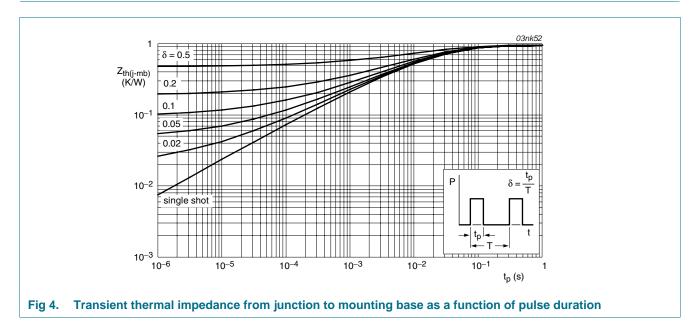
Fig 2. Normalized total power dissipation as a function of mounting base temperature



5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	-	0.95	K/W
R _{th(j-a)}	thermal resistance from junction to ambient		-	71.4	-	K/W



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6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
V _{(BR)DSS} drain-source		$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	100	-	-	V
breakdown voltage	breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	89	-	-	V
()	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 185$ °C; see <u>Figure 10</u>	0.9	-	-	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = 25 °C; see <u>Figure 10</u>	2	3	4	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see <u>Figure 10</u>	-	-	4.4	V
I _{DSS} drain leaka	drain leakage current	$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μΑ
		$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 185 ^{\circ}\text{C}$	-	-	500	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nΑ
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nΑ
R _{DSon} drain-source on-star resistance	drain-source on-state resistance	V_{GS} = 10 V; I_D = 25 A; T_j = 185 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	-	70	mΩ
		V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	23	27	mΩ
Dynamic o	haracteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A; } V_{DS} = 80 \text{ V; } V_{GS} = 10 \text{ V;}$ $T_j = 25 \text{ °C; see } \frac{\text{Figure } 13}{\text{Figure } 13}$	-	37	-	nC
Q _{GS}	gate-source charge		-	9	-	nC
Q_{GD}	gate-drain charge		-	13	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	2092	2789	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 14</u>	-	241	289	pF
C _{rss}	reverse transfer capacitance		-	102	140	pF
d(on)	turn-on delay time	$V_{DS} = 25 \text{ V}; R_L = 1.0 \Omega; V_{GS} = 10 \text{ V};$	-	18	-	ns
r	rise time	$R_{G(ext)} = 10 \Omega$; $T_j = 25 °C$	-	99	-	ns
t _{d(off)}	turn-off delay time		-	50	-	ns
t _f	fall time		-	20	-	ns
L _D	internal drain inductance	measured from drain to centre of die	-	2.5	-	nΗ
L-s	internal source inductance	measured from source lead to source bond pad	-	7.5	-	nΗ
Source-dr	ain diode					
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 15</u>	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}$; $dI_S/dt = -100 \text{ A/}\mu\text{s}$;	-	94	-	ns
Q _r	recovered charge	$V_{GS} = -10 \text{ V}; V_{DS} = 30 \text{ V}; T_j = 25 \text{ °C}$	-	114	-	nC

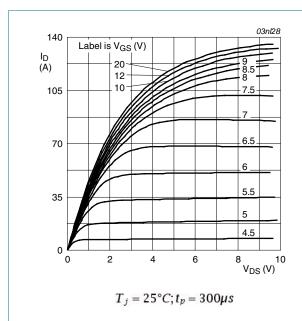


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

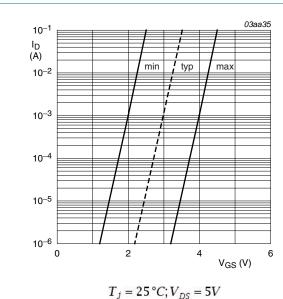


Fig 7. Sub-threshold drain current as a function of gate-source voltage

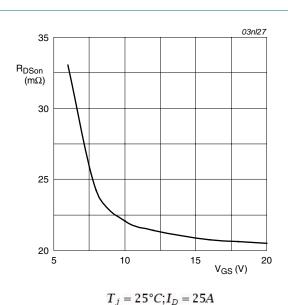


Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

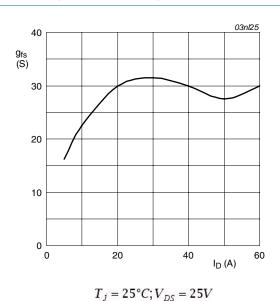


Fig 8. Forward transconductance as a function of drain current; typical values

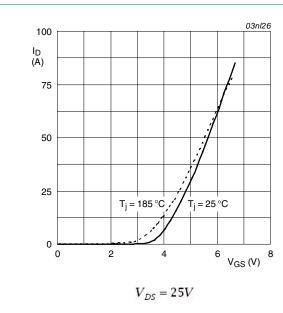


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

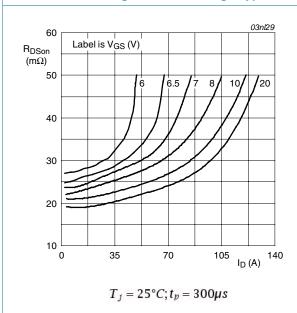


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

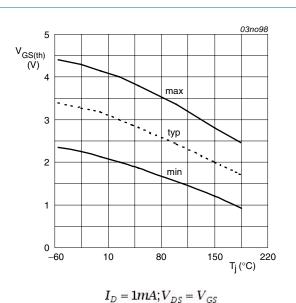


Fig 10. Gate-source threshold voltage as a function of junction temperature

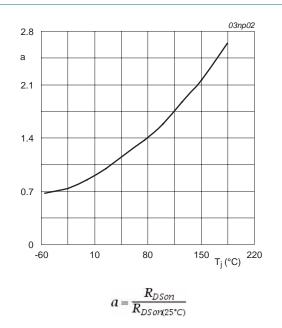


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature

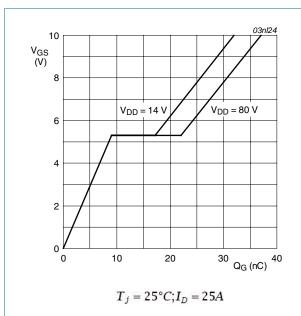


Fig 13. Gate-source voltage as a function of gate charge; typical values

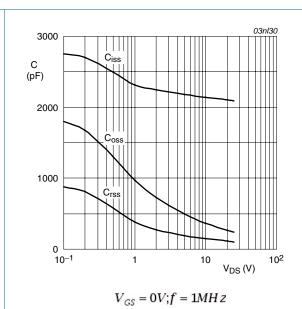


Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

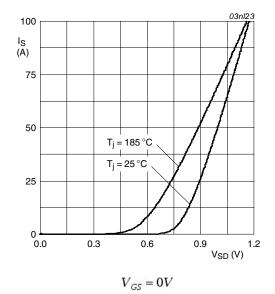


Fig 15. Source current as a function of source-drain voltage; typical values

7. Package outline

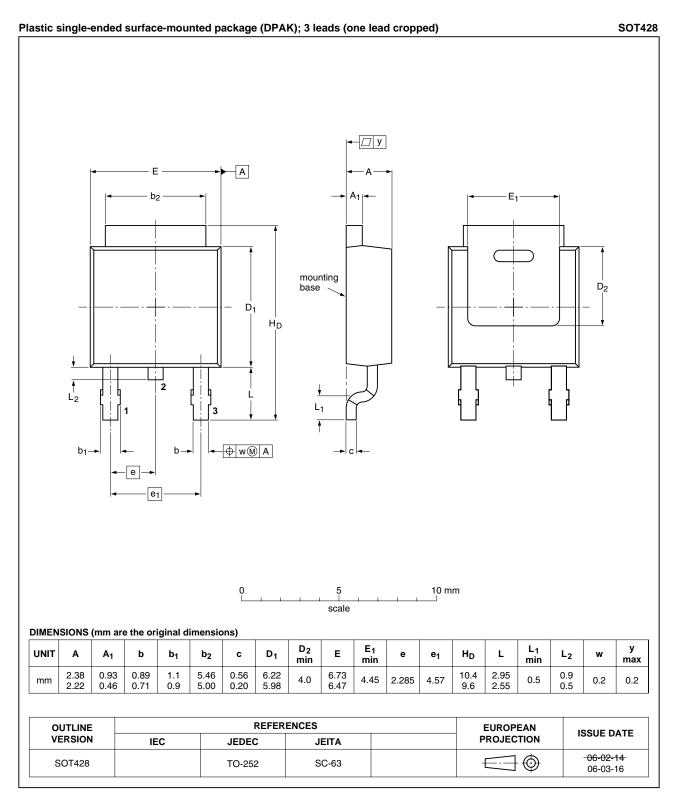


Fig 16. Package outline SOT428 (DPAK)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK7227-100B v.2	20110217	Product data sheet	-	BUK7227_100B v.1
Modifications:	 The format of of NXP Semior 	this data sheet has been reconductors.	designed to comply with	the new identity guidelines
	 Legal texts ha 	ve been adapted to the new	company name where	appropriate.
BUK7227_100B v.1	20040126	Product data	-	-

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9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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