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<b>Revision</b>	<b>Date</b>	<b>Change comments</b>
0.0	10/17/07	Initial data sheet converted from IRS2130xD data sheet
0.1	03/05/08	Initial Review
0.2	03/18/08	Included tri-temp plots
0.3	03/18/08	Updated test conditions
0.4	03/26/08	Updated limits using DR3 Limits table
0.5	03/27/08	Included application notes
0.6	03/27/08	Updated minor errors and completed review for DR3
0.7	03/28/08	Corrected reflow temperature for PLCC44 to 245°C
0.8	04/02/08	Added Integrated Operational Amplifier feature on front page and RoHS compliant.
0.9	04/11/08	Corrected logic level compatible on Page1 from 2.5V to 3.3V
1.0	04/15/08	Added MDT parameter
1.1	04/16/08	Updated MDT spec. and changed latch-up level to A
1.2	04/28/08	Removed typical MDT spec.; MDT expected to be zero and cannot be more than maximum spec.
	May 8, 08	Changed file format from "rev1.2" to May 8, 2008. Corrected part number in Fig. 15
	July 8, 08	changed Iqcc test condition to Vin=4V from 0V.
	June 1, 11	Add bootstrap fet limitation