

## STB13007DT4

## High voltage fast-switching NPN power transistor

#### **General features**

- Improved specification: Lower leakage current, Tighter gain range, DC current gain preselection, Tighter storage time range
- High voltage capability
- Integrated free-wheeling diode
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Fully characterized at 125 °C
- Large RBSOA
- In compliance with the 2002/93/EC European Directive

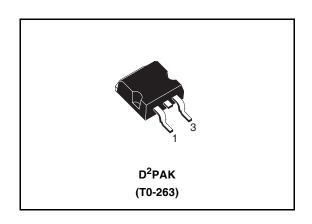
## **Description**

The device is manufactured using high voltage Multi-Epitaxial Planar technology for high switching speeds and medium voltage capability.

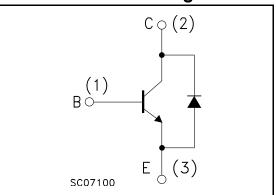
It uses a Cellular Emitter structure to enhance switching speeds.

## **Applications**

- Electronic transformers for halogen lamps
- Switch mode power supplies



### Internal schematic diagram



#### Order codes

Part Number	Marking	Package	Packing
STB13007DT4	B13007D	D <sup>2</sup> PAK	Tape & Reel

April 2007 Rev 2 1/9

Electrical ratings STB13007DT4

# 1 Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V <sub>CEV</sub>	Collector-emitter voltage (V <sub>BE</sub> = -1.5V)	700	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	9	V
I <sub>C</sub>	Collector current	8	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5ms)	16	Α
I <sub>B</sub>	Base current	4	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5ms)	8	Α
P <sub>tot</sub>	Total dissipation at T <sub>c</sub> = 25°C	80	W
T <sub>stg</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

Symbol	Parameter	Value	Unit	
R <sub>thj-case</sub>	Thermal resistance junction-case	max	1.56	°C/W
R <sub>thj-amb</sub>	Thermal resistance junction-amb	max	62.5	°C/W

## 2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$ 

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> =0V)	V <sub>CE</sub> =700V V <sub>CE</sub> =700V T <sub>c</sub> =100°C			10 0.5	μA mA
I <sub>CEO</sub>	Collector cut-off current (I <sub>B</sub> =0)	V <sub>CE</sub> =400V			100	μА
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> =0)	V <sub>EB</sub> =9V			100	μА
V <sub>CEO(sus)</sub> (1)	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> =10mA	400			<b>V</b>
		$I_C = 2A$ $I_B = 0.4A$			0.8	V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = 5 A$ $I_B = 1 A$			1.5	V
- OL(Sai)		$I_C = 8 A$ $I_B = 2A$ $I_C = 5 A$ $I_B = 1A$			2	V
		$T_c = 100^{\circ}C$			3	V
		$I_C = 2A$ $I_B = 0.4A$			1.2	V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_C = 5A$ $I_B = 1A$			1.6	٧
▼BE(sat)		$I_C = 5A$ $I_B = 1A$			1.5	V
		T <sub>c</sub> =100°C				
h <sub>FE</sub>	DC current gain	$I_C = 2A$ $V_{CE} = 5V$	18		40	
		$I_C = 5A$ $V_{CE} = 5V$	8		25	
V <sub>f</sub>	Diode forward voltage	I <sub>C</sub> = 3A			2.5	V
	Inductive load	$I_C = 5A$ $V_{Clamp} = 250V$				
t <sub>s</sub>	Storage time	$I_{B1} = 1A$ $V_{BE(off)} = -5V$ $R_{BB} = 0\Omega$ $L = 200\mu H$		1.7	2.3	μs
t <sub>f</sub>	Fall time	(see fig. 11)		90	150	ns
t <sub>s</sub>	Inductive load Storage time Fall time	$I_C = 5A$ $V_{Clamp} = 250V$ $I_{B1} = 1A$ $V_{BE(off)} = -5V$ $R_{BB} = 0\Omega$ $L = 200\mu H$		2.2 150		μs ns
		$T_c = 125^{\circ}C$ (see fig. 11)				-

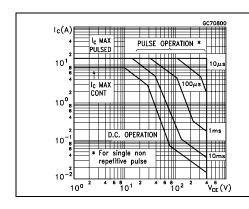
Note (1) Pulsed duration = 300  $\mu s,$  duty cycle  $\leq \! 1.5\%$ 

Electrical characteristics STB13007DT4

## 2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

Figure 2. Derating curve



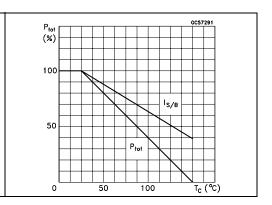
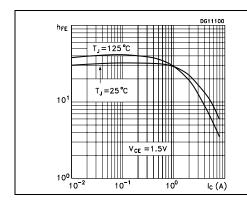


Figure 3. DC current gain

Figure 4. DC current gain



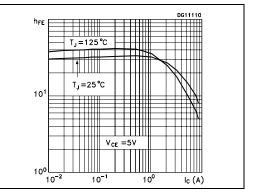
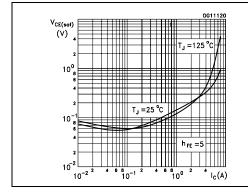


Figure 5. Collector-emitter saturation voltage

Figure 6. Base-emitter saturation voltage



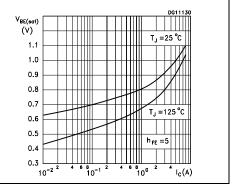
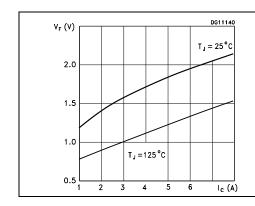


Figure 7. Diode forward voltage

Figure 8. Switching times inductive load



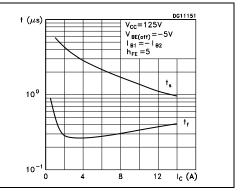
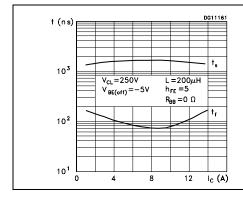
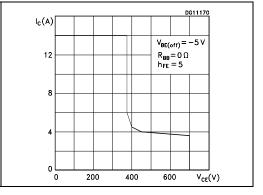


Figure 9. Switching times inductive load

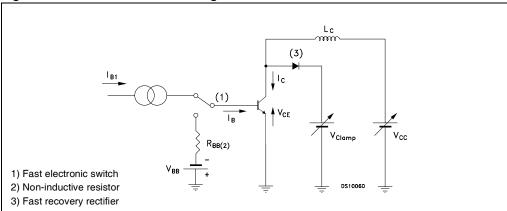
Figure 10. Reverse biased safe operating area





### 2.2 Test circuits

Figure 11. Inductive load switching test circuit



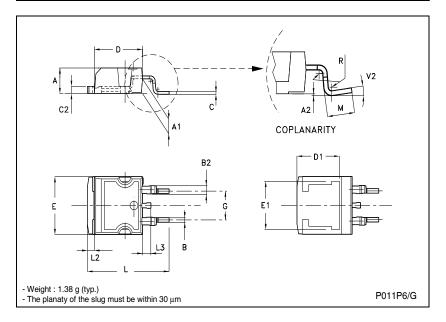
## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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## TO-263 (D<sup>2</sup>PAK) MECHANICAL DATA

DIM.	mm			inch			
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α	4.40		4.60	0.173		0.181	
A1	2.49		2.69	0.098		0.106	
A2	0.03		0.23	0.001		0.009	
В	0.70		0.93	0.027		0.036	
B2	1.14		1.70	0.044		0.067	
С	0.45		0.60	0.017		0.023	
C2	1.23		1.36	0.048		0.053	
D	8.95		9.35	0.352		0.368	
D1		8.00			0.315		
Е	10.00		10.40	0.393		0.409	
E1		8.50			0.334		
G	4.88		5.28	0.192		0.208	
L	15.00		15.85	0.590		0.624	
L2	1.27		1.4	0.050		0.055	
L3	1.40		1.75	0.055		0.068	
М	2.40		3.2	0.094		0.126	
R		0.40			0.016		
V2	0°		8°	0°		8°	



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Revision history STB13007DT4

# 4 Revision history

Table 4. Revision history

Date	Revision	Changes	
19-Jun-2006	1	Initial release.	
27-Apr-2007	2	The package's mechanical data has been update on page 7	

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