



### P-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on) max</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C
-40V	$11m\Omega$ @ $V_{GS} = -10V$	-10.1A
	15mΩ @ $V_{GS} = -4.5V$	-8.8A

### **Features and Benefits**

- 100% Unclamped Inductive Switch (UIS) test in production
- Low Input Capacitance
- Lead, Halogen, and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

## **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

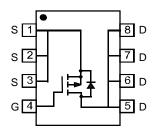
- DC-DC Converters
- Power management functions
- Analog Switch

### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208
- Weight: 0.0072 grams (approximate)



Top View



Top View Internal Schematic

### **Ordering Information** (Note 3)

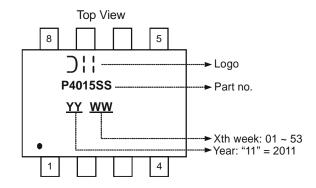
Part Number	Qualification	Case	Packaging
DMP4015SSS-13	Commercial	SO-8	2,500/Tape & Reel
DMP4015SSSQ-13	Automotive	SO-8	2,500/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.

3. For packaging details, go to our website at http://www.diodes.com.

## Marking Information





# **Maximum Ratings** $@T_A = 25$ °C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	$V_{DSS}$	-40	V		
Gate-Source Voltage	$V_{GSS}$	±25	V		
Continuous Drain Current (Note 4) V <sub>GS</sub> = -10V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	-9.1 -7.2	А
Continuous Drain Current (Note 4) V <sub>GS</sub> = -4.5V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-7.8 -6.2	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-10.1 -8.0	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = -4.5V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-8.8 -7.0	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	-100	Α		
Avalanche Current (Note 6)	I <sub>AS</sub>	-22	Α		
Avalanche Energy (Note 6)	E <sub>AS</sub>	242	mJ		

# **Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	$P_{D}$	1.45	W
Thermal Resistance, Junction to Ambient (Note 4)	$R_{ hetaJA}$	88	°C/W
Total Power Dissipation (Note 5)	$P_{D}$	1.82	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ hetaJA}$	70	°C/W
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta Jc}$	7.6	°C/W
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

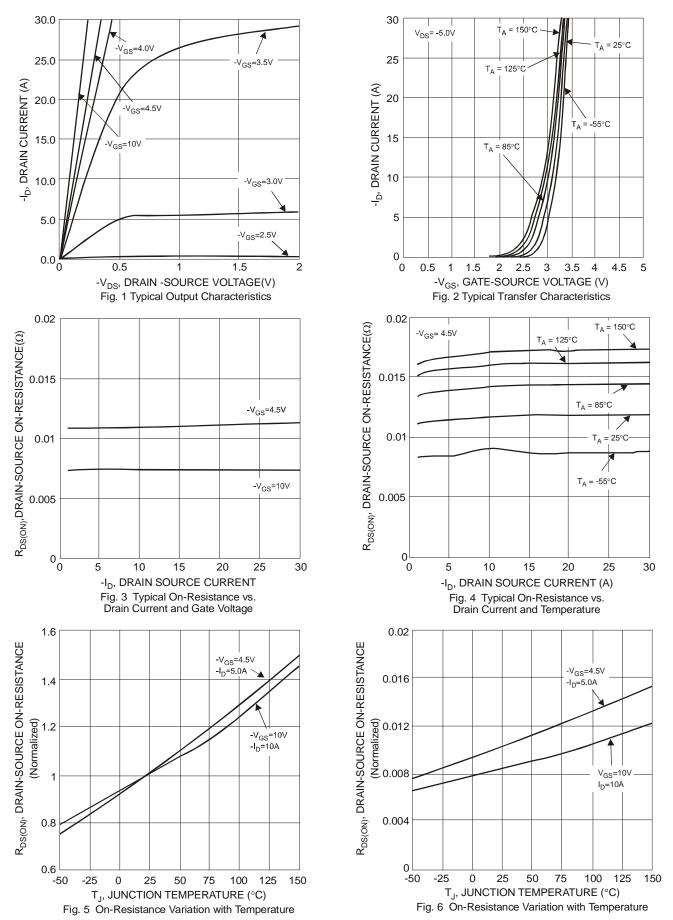
## Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	-1	μA	$V_{DS} = -40V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		_	±100	nA	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.5	-2.0	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	D		7	11	mΩ	$V_{GS} = -10V, I_D = -9.8A$	
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)		9	15	1117.5	$V_{GS} = -4.5V, I_D = -9.8A$	
Forward Transfer Admittance	Y <sub>fs</sub>		26		S	$V_{DS} = -20V, I_{D} = -9.8A$	
Diode Forward Voltage (Note 5)	$V_{SD}$		-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		4234			V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	Coss		1036		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		526	_		1 = 1.000112	
Gate Resistance	$R_{G}$		7.77		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg		47.5				
Gate-Source Charge	$Q_{gs}$	_	14.2	_	nC	$V_{DS} = -20V, V_{GS} = -5V$ $I_{D} = -9.8A$	
Gate-Drain Charge	$Q_{gd}$	_	13.5	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	13.2	_			
Turn-On Rise Time	t <sub>r</sub>	_	10.0	_		$V_{GS} = -10V, V_{DD} = -20V, R_G = 6\Omega,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	302.7	_	ns	$I_D = -1A$ , $R_L = 20\Omega$	
Turn-Off Fall Time	t <sub>f</sub>	_	137.9	_			

Notes:

- 4. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate
- 6 .UIS in production with L = 1mH, TJ = 25°C
  7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.







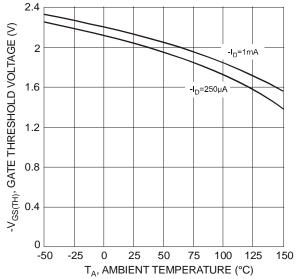
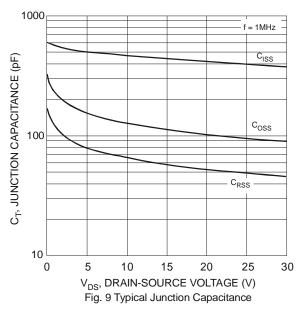
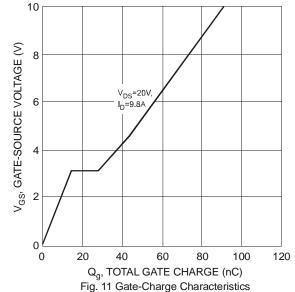


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





30 25 T<sub>A</sub>= 25°C T<sub>A</sub>= 25°C 10 0 0.2 0.4 0.6 0.8 1 1.2 1.4 -V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Fig. 8 Diode Forward Voltage vs. Current

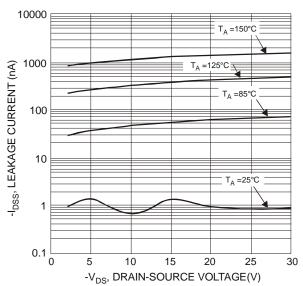
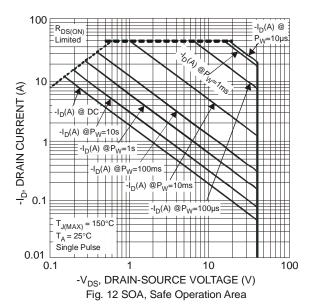
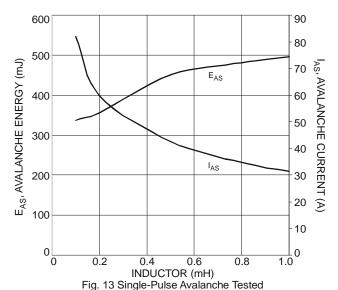
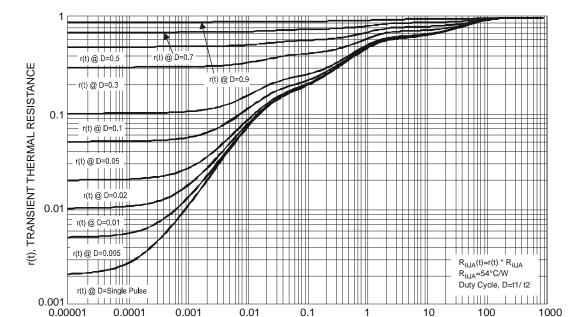


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage



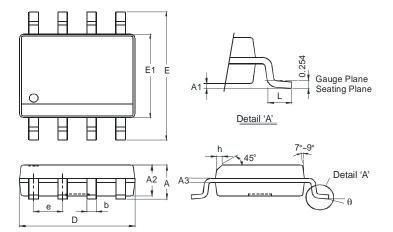






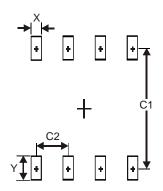


# **Package Outline Dimensions**



SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
А3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
Ĺ	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

# **Suggested Pad Layout**



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27



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