

# vPolyTan<sup>TM</sup> Solid Tantalum Surface Mount Chip Capacitors, **Leadframeless Molded Polymer Type**



#### PERFORMANCE / ELECTRICAL CHARACTERISTICS

Operating Temperature: -55 °C to +105 °C (above 85 °C, voltage derating is required) Capacitance Range: 10 µF to 330 µF Capacitance Tolerance: ± 20 % Voltage Rating: 4 V<sub>DC</sub> to 25 V<sub>DC</sub>

### **FEATURES**

- Low ESR
- Molded case available in 6 case codes including 0603 and 0805 footprint
- Lead (Pb)-free L-shaped face-down terminations
- 8 mm tape and reel packaging available per EIA-481 standard
- Moisture sensitivity level 3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



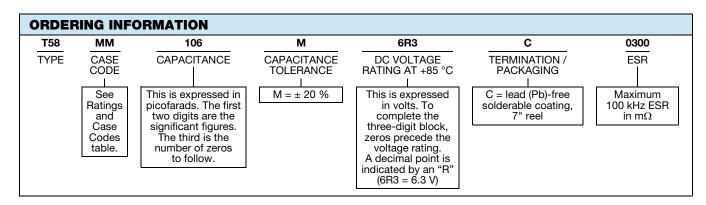
RoHS COMPLIANT

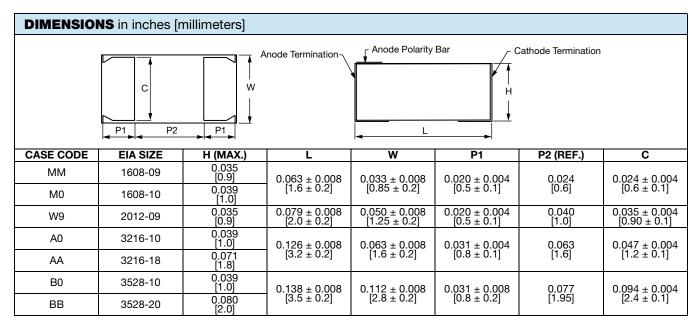
**HALOGEN** FREE

**GREEN** (5-2008)

#### **APPLICATIONS**

- · Decoupling, smoothing, filtering
- · Bulk energy storage in wireless cards
- Infrastructure equipment
- Storage and networking
- · Computer motherboards
- · Smartphones and tablets

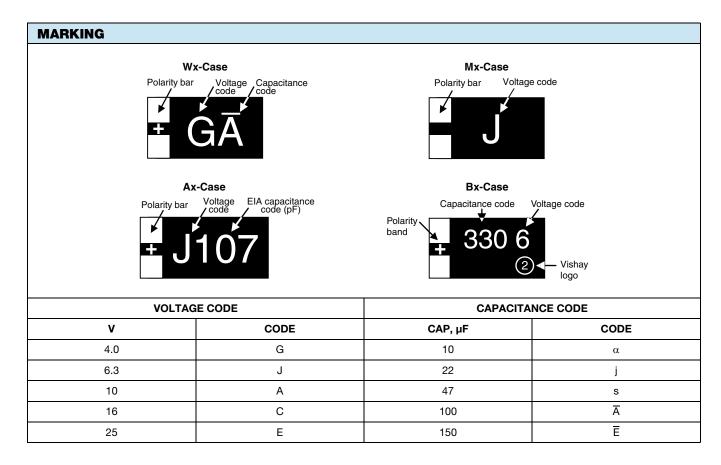




RATINGS AND CASE CODES					
μF	4.0 V	6.3 V	10 V	16 V	25 V
10		MM (200) <sup>(1)</sup> / MM (300, 500)			
22		MM (200) <sup>(1)</sup> / MM (300, 500)			BB (90, 200) <sup>(1)</sup>
47		M0 (300, 500) <sup>(1)</sup> / W9 (200) <sup>(1)</sup>	A0 (100) <sup>(1)</sup>	BB (90, 200) <sup>(1)</sup>	
100	W9 (150) <sup>(1)</sup>	A0 (100, 150) <sup>(1)</sup>			
150		B0 (200) <sup>(1)</sup>			
220			BB (50, 200) <sup>(1)</sup>		
330		BB (50, 100) <sup>(1)</sup>			

#### Notes

- (1) In development.
- In brackets is available ESR, mΩ



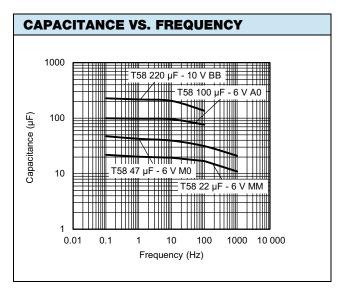
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT +25 °C (μA)	MAX. DF AT +25 °C 120 Hz (%)	MAX. ESR AT +25 °C 100 kHz (mΩ)	MAX. RIPPLE, 100 kHz I <sub>RMS</sub> (A)
		4.0 V <sub>DC</sub> AT +85 °C,	3.2 V <sub>DC</sub> AT +1	05 °C		
100	W9 <sup>(1)</sup>	T58W9107M004C0150	40.0	14	150	0.516
		6.3 V <sub>DC</sub> AT +85 °C	, 5 V <sub>DC</sub> AT +10	5 °C		
10	MM	T58MM106M6R3C0500	6.3	8	500	0.224
10	MM	T58MM106M6R3C0300	6.3	8	300	0.289
10	MM <sup>(1)</sup>	T58MM106M6R3C0200	6.3	8	200	0.354
22	MM	T58MM226M6R3C0500	14	10	500	0.224
22	MM	T58MM226M6R3C0300	14	10	300	0.289
22	MM <sup>(1)</sup>	T58MM226M6R3C0200	14	10	200	0.354
47	M0 <sup>(1)</sup>	T58M0476M6R3C0500	30	14	500	0.224
47	M0 <sup>(1)</sup>	T58M0476M6R3C0300	30	14	300	0.289
47	W9 <sup>(1)</sup>	T58W9476M6R3C0200	30	14	200	0.447
100	A0 <sup>(1)</sup>	T58A0107M6R3C0150	63	14	150	0.606
100	A0 <sup>(1)</sup>	T58A0107M6R3C0100	63	14	100	0.742
150	B0 <sup>(1)</sup>	T58B0157M6R3C0200	95	14	200	0.592
330	BB <sup>(1)</sup>	T58BB337M6R3C0100	208	14	100	0.922
330	BB <sup>(1)</sup>	T58BB337M6R3C0050	208	14	50	1.304
		10 V <sub>DC</sub> AT +85 °C	, 8 V <sub>DC</sub> AT +10	5 °C		
47	A0 <sup>(1)</sup>	T58A0476M010C0100	47	14	100	0.742
220	BB <sup>(1)</sup>	T58BB227M010C0200	220	14	200	0.652
220	BB <sup>(1)</sup>	T58BB227M010C0050	220	14	50	1.304
		16 V <sub>DC</sub> AT +85 °C,	12.8 V <sub>DC</sub> AT +1	05 °C		
47	BB <sup>(1)</sup>	T58BB476M016C0200	75	14	200	0.652
47	BB <sup>(1)</sup>	T58BB476M016C0090	75	14	90	0.972
		25 V <sub>DC</sub> AT +85 °C,	20 V <sub>DC</sub> AT +10	05 °C	-	
22	BB <sup>(1)</sup>	T58BB226M025C0200	55	14	200	0.652
22	BB <sup>(1)</sup>	T58BB226M025C0090	55	14	90	0.972

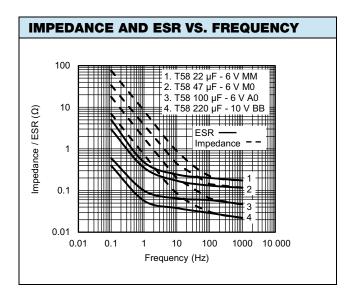
### Note

<sup>(1)</sup> In development.

RECOMMENDED VOLTAGE DERATING GUIDELINES (for temperature below +85 °C)			
CAPACITOR VOLTAGE RATING	OPERATING VOLTAGE		
4.0	3.2		
6.3	5.0		
10	8.0		
16	12.8		
25	20.0		







POWER DISSIPATION				
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION AT +25 °C (W) IN FREE AIR			
MM / M0	0.025			
W9	0.040			
A0	0.055			
AA	0.075			
В0	0.070			
ВВ	0.085			

STANDARD PACKAGING QUANTITY				
CASE CODE	UNITS PER 7" REEL			
MM / M0	4000			
W9	3000			
A0	2500			
AA	2000			
В0	3000			
BB	2000			



ITEM	CONDITION	POST TEST PERFORMANCE		
Life test at +85 °C	1000 h application of rated voltage at 85 °C,	Capacitance change	Within ± 20 % of initial value	
	MIL-STD-202 method 108	Dissipation factor	Within initial limits	
		Leakage current	Shall not exceed 300 % of initial limit	
Humidity tests	At 40 °C / 90 % RH 500 h, no voltage applied	Capacitance change	-20 % to +40 % of initial value	
		Dissipation factor	Within initial limit	
		Leakage current	Shall not exceed 300 % of initial limit	
Resistance	MIL-STD-202, method 210, with peak body temperature: less than 260 °C, time: 5 s max.	Capacitance change	Within ± 20 % of initial value	
to solder heat		Dissipation factor	Within initial limit	
		Leakage current	Shall not exceed 300 % of initial limit	
Stability at low and high temperatures	-55 °C	Capacitance change	Within -20 % to 0 % of initial value	
		Dissipation factor	Shall not exceed 150 % of initial limit	
		Leakage current	n/a	
	25 °C	Capacitance change	Within ± 20 % of initial value	
		Dissipation factor	Within initial limit	
		Leakage current	Within initial limit	
	105 °C	Capacitance change	Within -50 % to +30 % of initial value	
		Dissipation factor	Within initial limits	
		Leakage current	Shall not exceed 1000 % of initial limits	
Surge voltage	85 °C, 1000 successive test cycles at 1.3 of rated voltage in series with a 1 kΩ resistor at the rate of 30 s ON, 30 s OFF	Capacitance change	Within ± 20 % of initial value	
		Dissipation factor	Within initial limit	
		Leakage current	Shall not exceed 300 % of initial limit	
Shock	MIL-STD-202, method 213, condition I, 100 $g$ peak	Capacitance change	Within ± 20 % of initial value	
(specified pulse)		Dissipation factor	Within initial limit	
		Leakage current	Shall not exceed 300 % of initial limit	
Vibration	MIL-STD-202, method 204, condition D, 10 Hz to 2000 Hz 20 <i>g</i> peak	There shall be no mechanical or visual damage to capacitors post-conditioning.		
Shear test	Apply a pressure load of 5 N for 10 s ± 1 s	Capacitance change	Within ± 20 % of initial value	
	horizontally to the center of capacitor side body	Dissipation factor	Within initial limit	
		Leakage current	Shall not exceed 300 % of initial limit	



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Revision: 02-Oct-12 Document Number: 91000